

Service  
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# Service Manual

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# 1. Technical Specifications, Connections, and Chassis Overview

## Index of this chapter:

- 1.1 Technical Specifications
- 1.2 Connections
- 1.3 Chassis Overview

## Notes:

- Figures can deviate due to the different set executions.
- Specifications are indicative (subject to change).

## 1.1 Technical Specifications

### 1.1.1 Vision

Display type	: LCD
Screen size	: 32" (82 cm), 16:9
	: 47" (120 cm), 16:9
Resolution (HxV pixels)	: 1366x768p (32")
	: 1920x1080p (47")
Light output (cd/m <sup>2</sup> )	: 550
Viewing angle (HxV degrees)	: 176x176
Tuning system	: PLL
Colour systems	: NTSC
	: PAL
	: SECAM
	: DVB-T
Video playback	: NTSC
	: PAL
	: SECAM
Cable	: Unscrambled digital cable - QAM
Tuner bands	: UHF, VHF, S, Hyper
Supported video formats	: 480i @ 60 Hz
	: 480p @ 60 Hz
	: 576i @ 50 Hz
	: 576p @ 50 Hz
	: 720p @ 50/60 Hz
	: 1080i @ 50/60 Hz
	: 1080p @ 24/25/30/50/60 Hz
Supported computer formats (60 Hz)	: 640x480
	: 800x600
	: 1024x768
	: 1280x1024
	: 1360x768
	: 1920x1080i
	: 1920x1080p

### 1.1.2 Sound

Maximum power (W <sub>RMS</sub> )	: 2 x 8
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### 1.1.3 Multimedia

Supported file formats	: JPEG
	: MPEG1, MPEG2
	: MP3
	: Slideshow (.alb)

USB input	: USB2.0
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### 1.1.4 Miscellaneous

Power supply:	
- Mains voltage (V <sub>AC</sub> )	: 220 - 240 ±10%
- Mains frequency (Hz)	: 50 / 60

Ambient conditions:	
- Temperature range (°C)	: +5 to +35

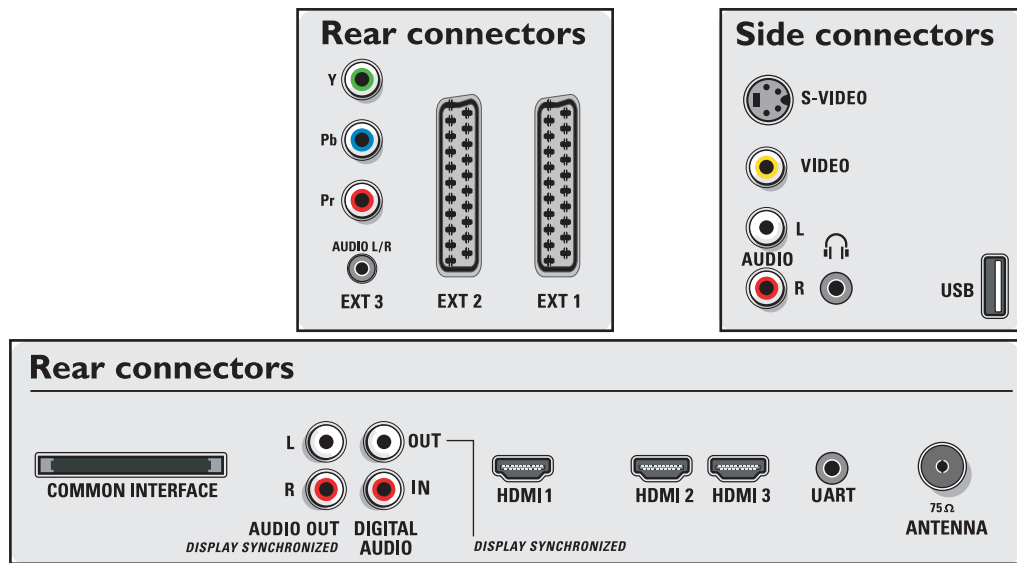
Power consumption (values are indicative)	
- Normal operation (W)	: 132 (32")
	: 293 (47")
- Standby (W)	: < 0.79

Dimensions (WxHxD in cm)	: 82.9x54.3x12.5 (32")
	: 117.0x73.6x12.5 (47")

Weight (kg)	: 18.2 (32")
	: 33.2 (47")



1.2 Connections



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Figure 1-1 Connection overview

**Note:** The following connector colour abbreviations are used (acc. to DIN/IEC 757): Bk= Black, Bu= Blue, Gn= Green, Gy= Grey, Rd= Red, Wh= White, Ye= Yellow.

1.2.1 Side Connections

**S-Video (Hosiden): Video Y/C - In**

- 1 - Ground Y           Gnd
- 2 - Ground C           Gnd
- 3 - Video Y            1 V<sub>PP</sub> / 75 ohm
- 4 - Video C            0.3 V<sub>PP</sub> / 75 ohm

**Cinch: Video CVBS - In, Audio - In**

- Rd - Audio R           0.5 V<sub>RMS</sub> / 10 kohm
- Wh - Audio L           0.5 V<sub>RMS</sub> / 10 kohm
- Ye - Video CVBS       1 V<sub>PP</sub> / 75 ohm

**Headphone (Output)**

- Bk - Headphone        32 - 600 ohm / 10 mW

**USB2.0**

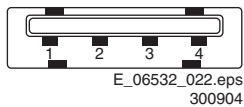


Figure 1-2 USB (type A)

- 1 - +5V
- 2 - Data (-)
- 3 - Data (+)
- 4 - Ground            Gnd

1.2.2 Rear Connections

**Common Interface**

- 68p - See diagram B07A

**Cinch: Audio - Out**

- Wh - Audio - L        0.5 V<sub>RMS</sub> / 10 kohm
- Rd - Audio - R        0.5 V<sub>RMS</sub> / 10 kohm

**Digital Audio Out: Cinch: S/PDIF - Out**

- Bk - Coaxial           0.4 - 0.6V<sub>PP</sub> / 75 ohm

**Digital Audio In: Cinch: S/PDIF - In**

- Bk - Coaxial           0.2 - 0.6V<sub>PP</sub> / 75 ohm

**HDMI 1, 2 & 3: Digital Video, Digital Audio - In**



Figure 1-3 HDMI (type A) connector

- 1 - D2+                Data channel
- 2 - Shield            Gnd
- 3 - D2-                Data channel
- 4 - D1+                Data channel
- 5 - Shield            Gnd
- 6 - D1-                Data channel
- 7 - D0+                Data channel
- 8 - Shield            Gnd
- 9 - D0-                Data channel
- 10 - CLK+             Data channel
- 11 - Shield            Gnd
- 12 - CLK-             Data channel
- 13 - n.c.
- 14 - n.c.
- 15 - DDC\_SCL         DDC clock
- 16 - DDC\_SDA        DDC data
- 17 - Ground           Gnd
- 18 - +5V
- 19 - HPD             Hot Plug Detect
- 20 - Ground           Gnd

**Service Connector (UART)**

- 1 - Ground            Gnd
- 2 - UART\_TX          Transmit
- 3 - UART\_RX          Receive

**Aerial - In**

- - IEC-type (EU)    Coax, 75 ohm

**EXT3: Cinch: Video YPbPr - In**

Gn - Video Y	1 V <sub>PP</sub> / 75 ohm	⊖ ⊕
Bu - Video Pb	0.7 V <sub>PP</sub> / 75 ohm	⊖ ⊕
Rd - Video Pr	0.7 V <sub>PP</sub> / 75 ohm	⊖ ⊕

**EXT3: Mini Jack: Audio - In**

Bk - Audio Surround	0.5 V <sub>RMS</sub> / 10 kohm	⊖ ⊕
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**EXT 2 & 1: Video RGB/YC - In, CVBS - In/Out, Audio - In/Out**

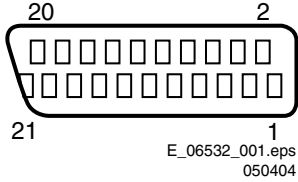


Figure 1-4 SCART connector

1 - Audio R	0.5 V <sub>RMS</sub> / 1 kohm	⊖ ⊕
2 - Audio L	0.5 V <sub>RMS</sub> / 10 kohm	⊖ ⊕

3 - Audio L	0.5 V <sub>RMS</sub> / 1 kohm	⊖ ⊕
4 - Ground Audio	Gnd	⊖ ⊕
5 - Ground Blue	Gnd	⊖ ⊕
6 - Audio L	0.5 V <sub>RMS</sub> / 10 kohm	⊖ ⊕
7 - Video Blue/C-out	0.7 V <sub>PP</sub> / 75 ohm	⊖ ⊕
8 - Function Select	0 - 2 V: INT 4.5 - 7 V: EXT 16:9 9.5 - 12 V: EXT 4:3	⊖ ⊕
9 - Ground Green	Gnd	⊖ ⊕
10 - Easylink P50	0 - 5 V / 4.7 kohm	⊖ ⊕
11 - Video Green	0.7 V <sub>PP</sub> / 75 ohm	⊖ ⊕
12 - n.c.		⊖ ⊕
13 - Ground Red	Gnd	⊖ ⊕
14 - Ground P50	Gnd	⊖ ⊕
15 - Video Red/C	0.7 V <sub>PP</sub> / 75 ohm	⊖ ⊕
16 - Status/FBL	0 - 0.4 V: INT 1 - 3 V: EXT / 75 ohm	⊖ ⊕
17 - Ground Video	Gnd	⊖ ⊕
18 - Ground FBL	Gnd	⊖ ⊕
19 - Video CVBS	1 V <sub>PP</sub> / 75 ohm	⊖ ⊕
20 - Video CVBS/Y	1 V <sub>PP</sub> / 75 ohm	⊖ ⊕
21 - Shield	Gnd	⊖ ⊕

1.3 Chassis Overview

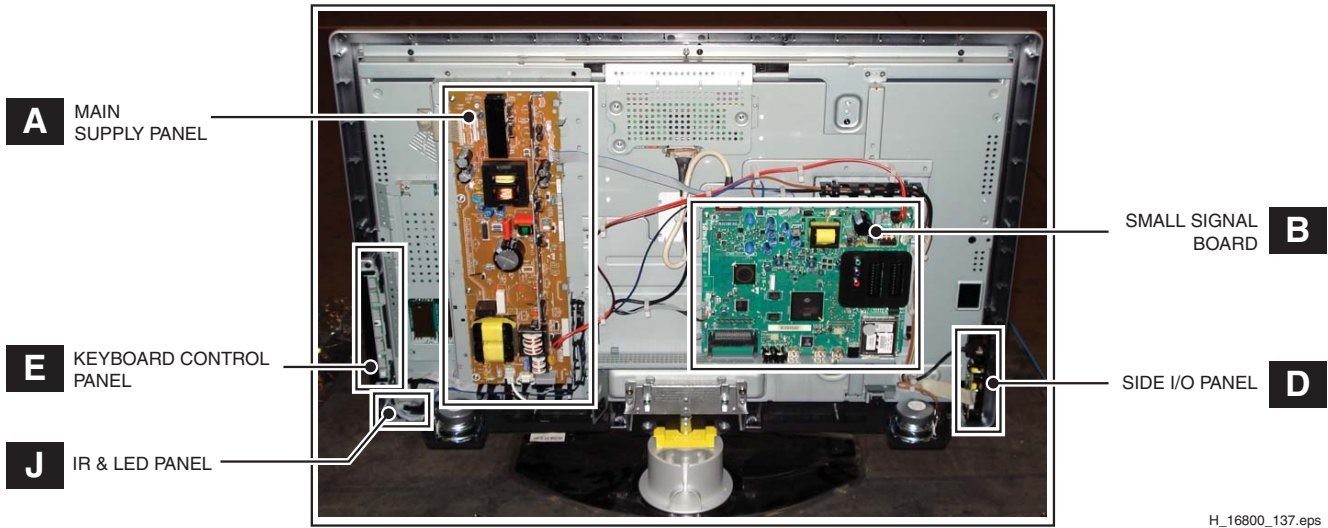


Figure 1-5 PWB/CBA locations 32" sets

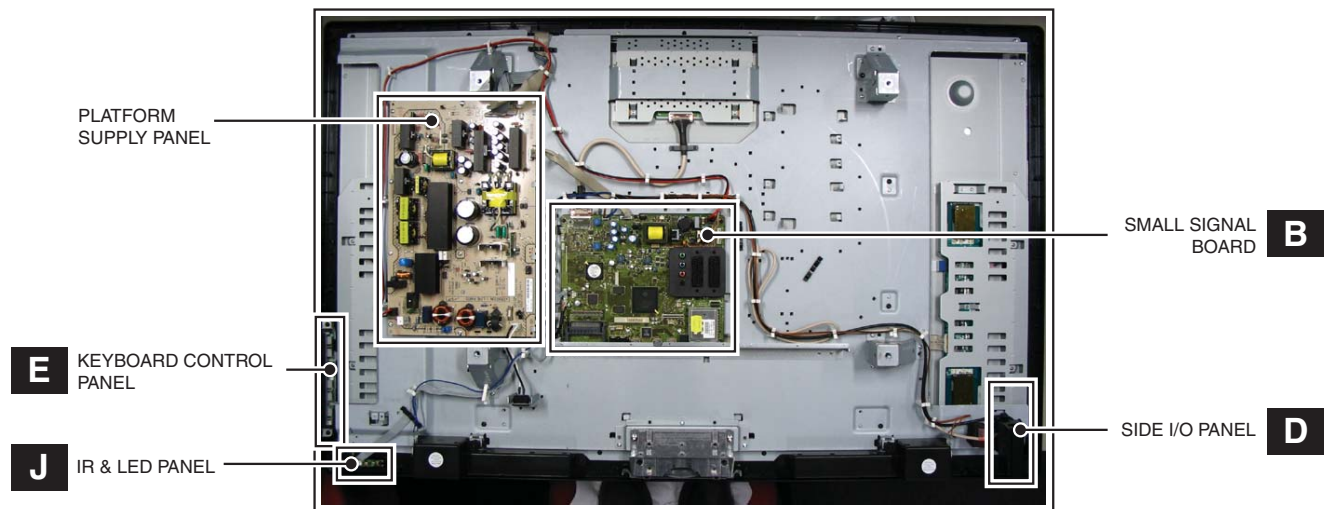


Figure 1-6 PWB/CBA locations 47" sets

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## 2. Safety Instructions, Warnings, and Notes

### Index of this chapter:

- 2.1 Safety Instructions
- 2.2 Warnings
- 2.3 Notes

### 2.1 Safety Instructions

Safety regulations require the following **during** a repair:

- Connect the set to the Mains/AC Power via an isolation transformer (> 800 VA).
- Replace safety components, indicated by the symbol ▲, only by components identical to the original ones. Any other component substitution (other than original type) may increase risk of fire or electrical shock hazard.

Safety regulations require that **after** a repair, the set must be returned in its original condition. Pay in particular attention to the following points:

- Route the wire trees correctly and fix them with the mounted cable clamps.
- Check the insulation of the Mains/AC Power lead for external damage.
- Check the strain relief of the Mains/AC Power cord for proper function.
- Check the electrical DC resistance between the Mains/AC Power plug and the secondary side (only for sets that have a Mains/AC Power isolated power supply):
  1. Unplug the Mains/AC Power cord and connect a wire between the two pins of the Mains/AC Power plug.
  2. Set the Mains/AC Power switch to the "on" position (keep the Mains/AC Power cord unplugged!).
  3. Measure the resistance value between the pins of the Mains/AC Power plug and the metal shielding of the tuner or the aerial connection on the set. The reading should be between 4.5 Mohm and 12 Mohm.
  4. Switch "off" the set, and remove the wire between the two pins of the Mains/AC Power plug.
- Check the cabinet for defects, to prevent touching of any inner parts by the customer.

### 2.2 Warnings

- All ICs and many other semiconductors are susceptible to electrostatic discharges (ESD ▲). Careless handling during repair can reduce life drastically. Make sure that, during repair, you are connected with the same potential as the mass of the set by a wristband with resistance. Keep components and tools also at this same potential.
- Be careful during measurements in the high voltage section.
- Never replace modules or other components while the unit is switched "on".
- When you align the set, use plastic rather than metal tools. This will prevent any short circuits and the danger of a circuit becoming unstable.

### 2.3 Notes

#### 2.3.1 General

- Measure the voltages and waveforms with regard to the chassis (= tuner) ground (⊥), or hot ground (↕), depending on the tested area of circuitry. The voltages and waveforms shown in the diagrams are indicative. Measure them in the Service Default Mode (see chapter 5) with a colour bar signal and stereo sound (L: 3 kHz, R: 1 kHz unless stated otherwise) and picture carrier at 475.25 MHz for PAL, or 61.25 MHz for NTSC (channel 3).

- Where necessary, measure the waveforms and voltages with (⊥) and without (↕) aerial signal. Measure the voltages in the power supply section both in normal operation (Ⓢ) and in stand-by (Ⓡ). These values are indicated by means of the appropriate symbols.
- Manufactured under license from Dolby Laboratories. "Dolby", "Pro Logic" and the "double-D symbol", are trademarks of Dolby Laboratories.

#### 2.3.2 Schematic Notes

- All resistor values are in ohms, and the value multiplier is often used to indicate the decimal point location (e.g. 2K2 indicates 2.2 kohm).
- Resistor values with no multiplier may be indicated with either an "E" or an "R" (e.g. 220E or 220R indicates 220 ohm).
- All capacitor values are given in micro-farads ( $\mu = \times 10^{-6}$ ), nano-farads ( $n = \times 10^{-9}$ ), or pico-farads ( $p = \times 10^{-12}$ ).
- Capacitor values may also use the value multiplier as the decimal point indication (e.g. 2p2 indicates 2.2 pF).
- An "asterisk" (\*) indicates component usage varies. Refer to the diversity tables for the correct values.
- The correct component values are listed in the Spare Parts List. Therefore, always check this list when there is any doubt.

#### 2.3.3 BGA (Ball Grid Array) ICs

##### Introduction

For more information on how to handle BGA devices, visit this URL: [www.atyourservice.ce.philips.com](http://www.atyourservice.ce.philips.com) (needs subscription, not available for all regions). After login, select "Magazine", then go to "Repair downloads". Here you will find Information on how to deal with BGA-ICs.

##### BGA Temperature Profiles

For BGA-ICs, you **must** use the correct temperature-profile, which is coupled to the 12NC. For an overview of these profiles, visit the website [www.atyourservice.ce.philips.com](http://www.atyourservice.ce.philips.com) (needs subscription, but is not available for all regions) You will find this and more technical information within the "Magazine", chapter "Repair downloads". For additional questions please contact your local repair help desk.

#### 2.3.4 Lead-free Soldering

Due to lead-free technology some rules have to be respected by the workshop during a repair:

- Use only lead-free soldering tin Philips SAC305 with order code 0622 149 00106. If lead-free solder paste is required, please contact the manufacturer of your soldering equipment. In general, use of solder paste within workshops should be avoided because paste is not easy to store and to handle.
- Use only adequate solder tools applicable for lead-free soldering tin. The solder tool must be able:
  - To reach a solder-tip temperature of at least 400°C.
  - To stabilize the adjusted temperature at the solder-tip.
  - To exchange solder-tips for different applications.
- Adjust your solder tool so that a temperature of around 360°C - 380°C is reached and stabilized at the solder joint. Heating time of the solder-joint should not exceed ~ 4 sec. Avoid temperatures above 400°C, otherwise wear-out of tips will increase drastically and flux-fluid will be destroyed. To avoid wear-out of tips, switch "off" unused equipment or reduce heat.
- Mix of lead-free soldering tin/parts with leaded soldering tin/parts is possible but PHILIPS recommends strongly to

**avoid** mixed regimes. If this cannot be avoided, carefully clear the solder-joint from old tin and re-solder with new tin.

### 2.3.5 Alternative BOM identification

The **third digit** in the serial number (example: AG2B0335000001) indicates the number of the alternative B.O.M. (Bill Of Materials) that has been used for producing the specific TV set. In general, it is possible that the same TV model on the market is produced with e.g. two different types of displays, coming from two different suppliers. This will then result in sets which have the same CTN (Commercial Type Number; e.g. 28PW9515/12) but which have a different B.O.M. number.

By looking at the third digit of the serial number, one can identify which B.O.M. is used for the TV set he is working with. If the third digit of the serial number contains the number "1" (example: AG1B0335000001), then the TV set has been manufactured according to B.O.M. number 1. If the third digit is a "2" (example: AG2B0335000001), then the set has been produced according to B.O.M. no. 2. **This is important for ordering the correct spare parts!**

For the third digit, the numbers 1...9 and the characters A...Z can be used, so in total: 9 plus 26= 35 different B.O.M.s can be indicated by the third digit of the serial number.

**Identification:** The bottom line of a type plate gives a 14-digit serial number. Digits 1 and 2 refer to the production center (e.g. AG is Bruges), digit 3 refers to the B.O.M. code, digit 4 refers to the Service version change code, digits 5 and 6 refer to the production year, and digits 7 and 8 refer to production week (in example below it is 2006 week 17). The 6 last digits contain the serial number.



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Figure 2-1 Serial number (example)

### 2.3.6 Board Level Repair (BLR) or Component Level Repair (CLR)

If a board is defective, consult your repair procedure to decide if the board has to be exchanged or if it should be repaired on component level.

If your repair procedure says the board should be exchanged completely, do not solder on the defective board. Otherwise, it cannot be returned to the O.E.M. supplier for back charging!

### 2.3.7 Practical Service Precautions

- **It makes sense to avoid exposure to electrical shock.** While some sources are expected to have a possible dangerous impact, others of quite high potential are of limited current and are sometimes held in less regard.
- **Always respect voltages.** While some may not be dangerous in themselves, they can cause unexpected reactions that are best avoided. Before reaching into a powered TV set, it is best to test the high voltage insulation. It is easy to do, and is a good service precaution.

### 3. Directions for Use

You can download this information from the following websites:

<http://www.philips.com/support>

<http://www.p4c.philips.com>

## 4. Mechanical Instructions

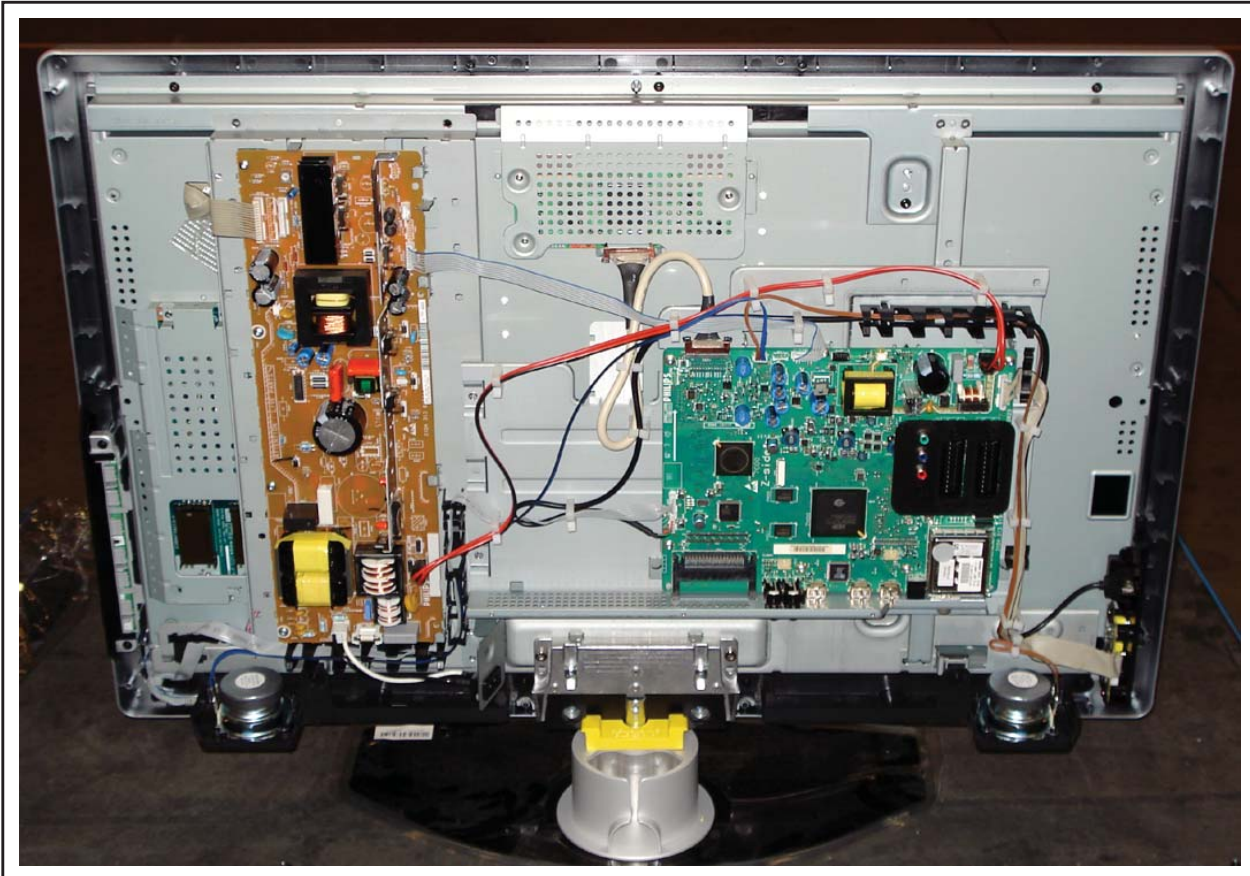
### Index of this chapter:

- 4.1 Cable Dressing
- 4.2 Service Positions
- 4.3 Assy/Panel Removal
- 4.4 Set Re-assembly

### Notes:

- Figures below can deviate slightly from the actual situation, due to the different set executions.
- Follow the disassemble instructions in described order. They apply to the 47PFL9532D/10 (47" with AmbiLight), unless stated otherwise.

### 4.1 Cable Dressing



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Figure 4-1 Cable dressing 32" sets





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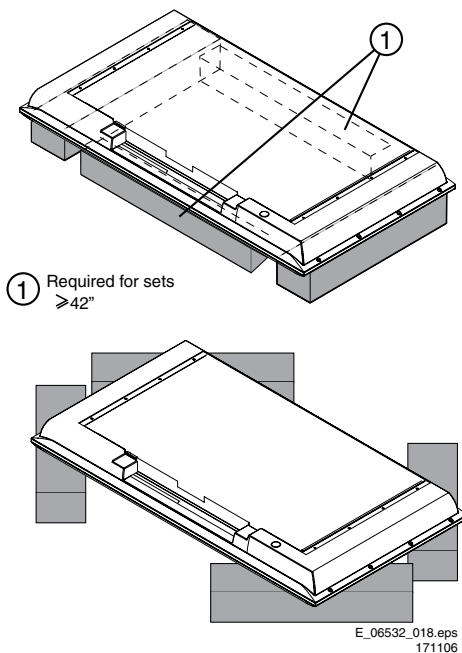
Figure 4-2 Cable dressing 47" sets

**4.2 Service Positions**

For easy servicing of this set, there are a few possibilities created:

- The buffers from the packaging (see figure "Rear cover").
- Foam bars (created for Service).
- Aluminium service stands (created for Service).

**4.2.1 Foam Bars**



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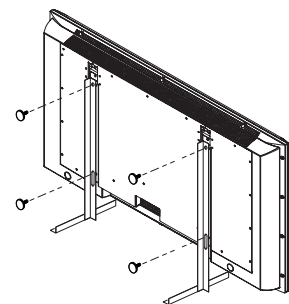
Figure 4-3 Foam bars

The foam bars (order code 3122 785 90580 for two pieces) can be used for all types and sizes of Flat TVs. See figure "Foam bars" for details. Sets with a display of 42" and larger, require **four** foam bars [1]. Ensure that the foam bars are always supporting the cabinet and **never** only the display.

**Caution:** Failure to follow these guidelines can seriously damage the display!

By laying the TV face down on the (ESD protective) foam bars, a stable situation is created to perform measurements and alignments. By placing a mirror under the TV, you can monitor the screen.

**4.2.2 Aluminium Stands**



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Figure 4-4 Aluminium stands (drawing of MkI)

The new MkII aluminium stands (not on drawing) with order code 3122 785 90690, can also be used to do measurements, alignments, and duration tests. The stands can be (dis)mounted quick and easy by means of sliding them in/out the "mushrooms". The new stands are backwards compatible with the earlier models.

**Important:** For (older) FTV sets without these "mushrooms", it is obligatory to use the provided screws, otherwise it is possible to damage the monitor inside!

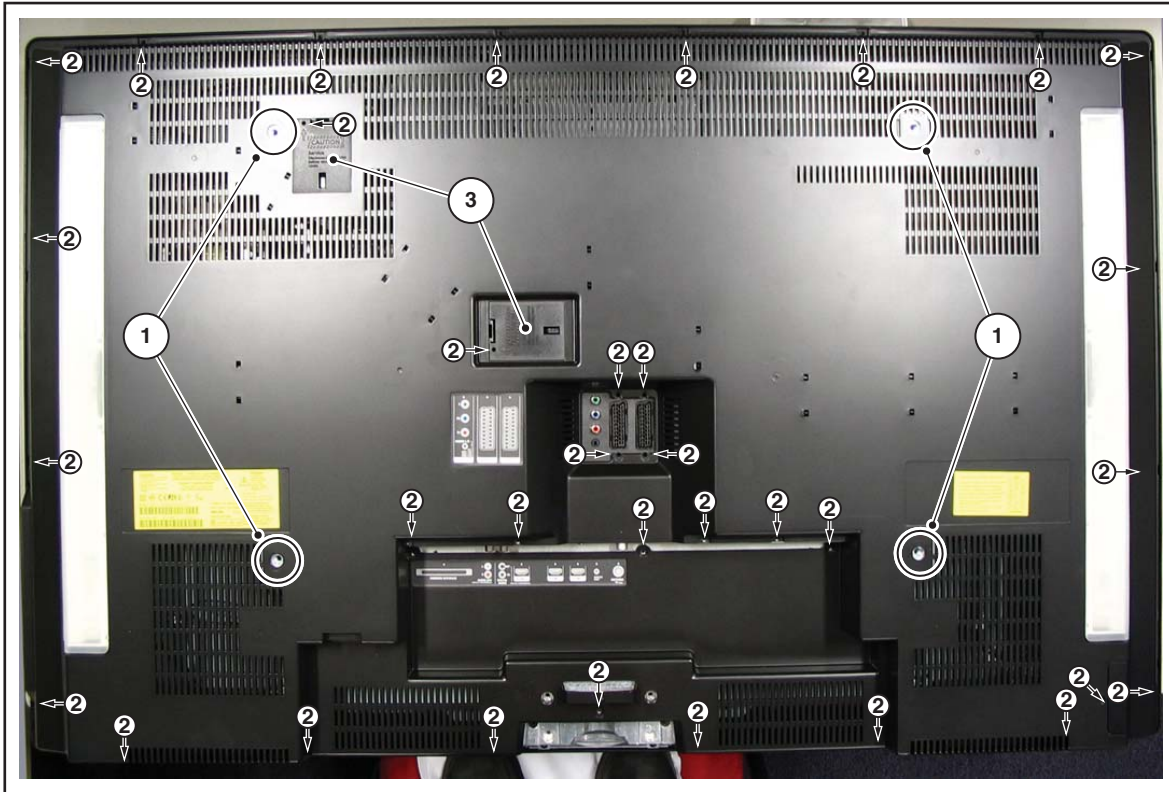
### 4.3 Assy/Panel Removal

#### 4.3.1 Rear Cover

**Warning:** Disconnect the mains power cord before you remove the rear cover.

**Note:** it is **not** necessary to remove the stand while removing the rear cover.

Refer to next figures for details.



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Figure 4-5 Rear Cover Removal

1. Place the TV set upside down on a table top, using the foam bars (see part "Foam Bars").  
**Caution:** do **not** put pressure on the display, but let the monitor lean on the Front cover!
2. Remove the mushrooms [1].
3. Remove the screws [2].
4. Open the covers [3] on the back cover.
5. Unplug connectors [4].
6. Lift the rear cover from the TV. Make sure that wires and flat coils are not damaged while lifting the rear cover from the set.



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Figure 4-6 Cover Removal -1-



4.3.2 Side I/O Board

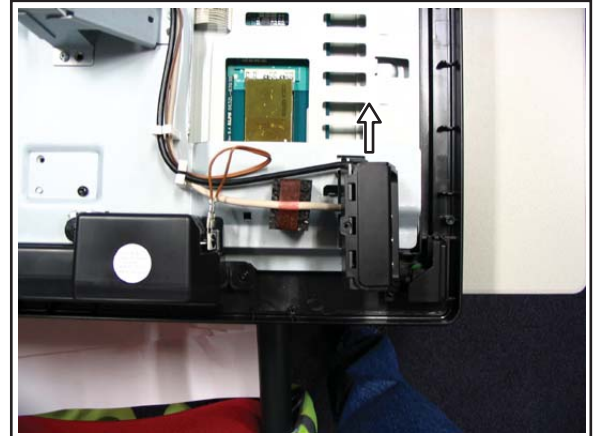
Refer to next figures for details.

1. Slide the unit upwards.
2. Unplug connector [1] and take the PWB out of its casing. When defective, replace the whole unit.



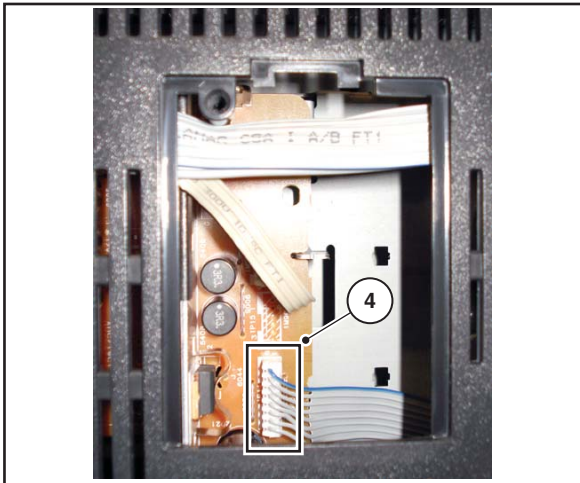
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090507

Figure 4-7 Cover Removal -2-



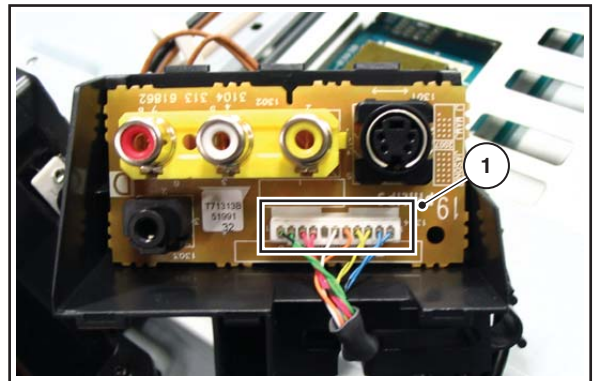
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Figure 4-10 Side I/O Board -1-



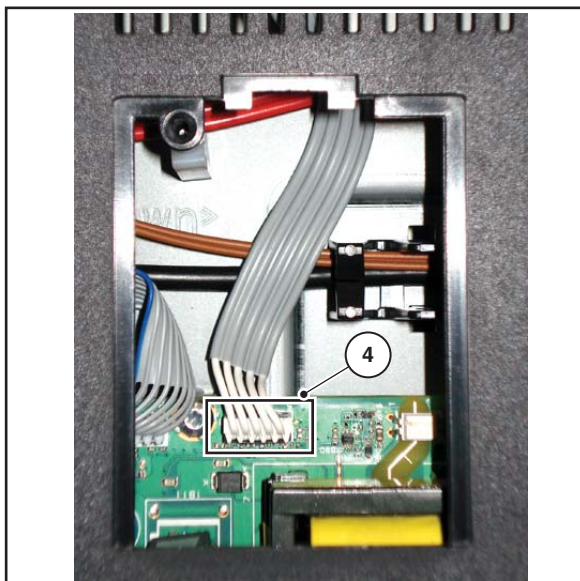
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Figure 4-8 Cover Removal -1- (32'')



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Figure 4-11 Side I/O Board -2-



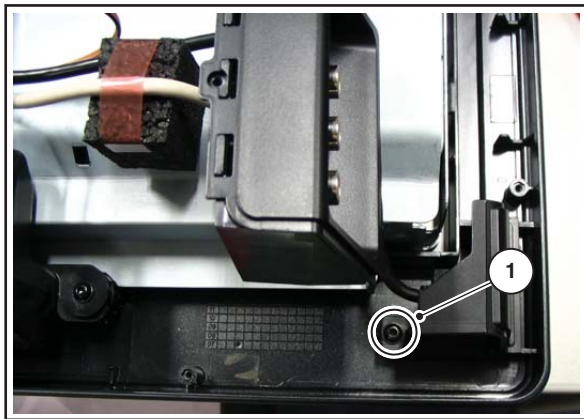
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Figure 4-9 Cover Removal -2- (32'')

4.3.3 USB I/O Board

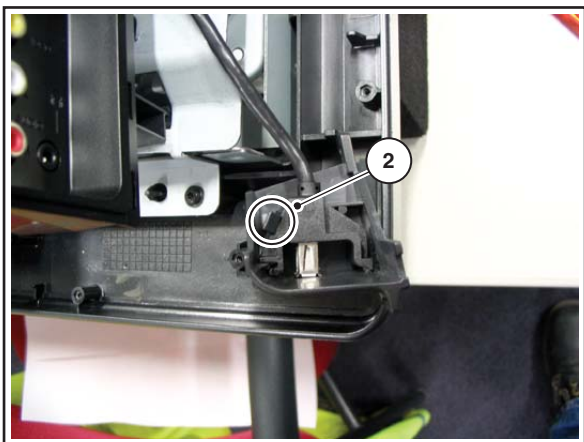
Refer to next figures for details.

1. Remove screw [1].
  2. Lift clamp [2] and take the unit out of its casing.
- When defective, replace the whole unit.



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Figure 4-12 USB I/O Board -1-



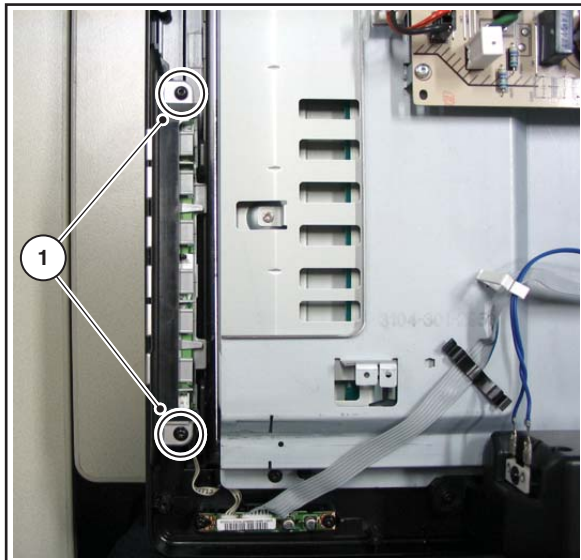
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Figure 4-13 USB I/O Board -2-

4.3.4 Keyboard Control Board

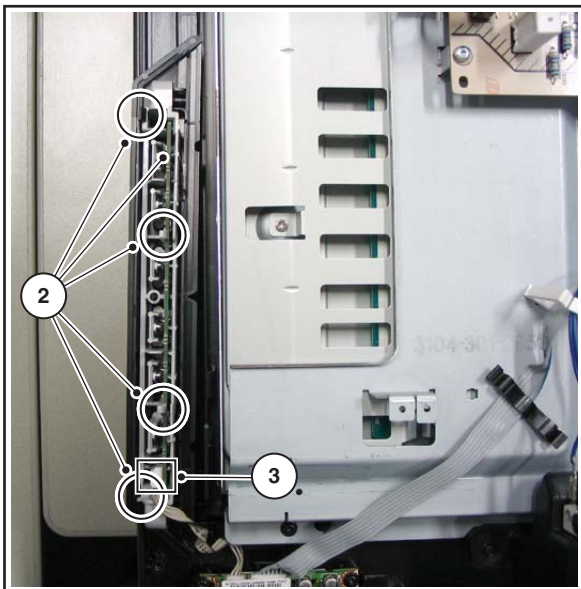
Refer to next figures for details.

1. Remove the screws [1] and turn the board upside-down.
  2. Lift the clamps [2].
  3. Take the PWB out of its casing.
  4. Unplug the connector [3] and remove the board.
- When defective, replace the whole unit.



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Figure 4-14 Keyboard Control Board -1-



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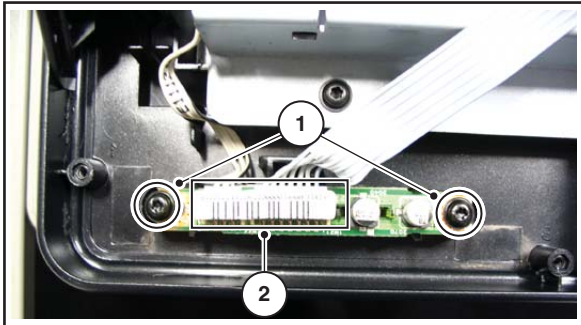
Figure 4-15 Keyboard Control Board -2-

#### 4.3.5 IR & LED Board

Refer to next figure for details.

1. Release clip [1], lift the board and take it out.
2. Unplug connectors [2].
3. Lift the board and take it out of the set.

When defective, replace the whole unit.



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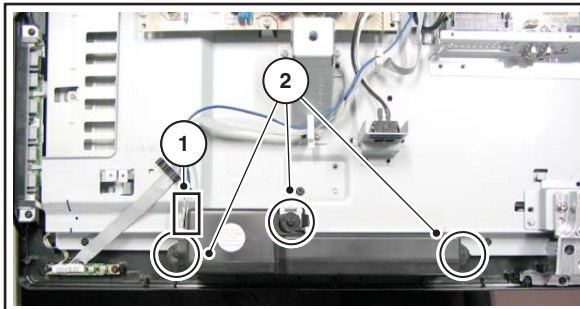
Figure 4-16 IR & LED Board

#### 4.3.6 Speakers

Refer to next figure for details.

1. Unplug connectors [1].
2. Remove screws [2].

Take the speakers out together with their casing. When defective, replace the whole unit.



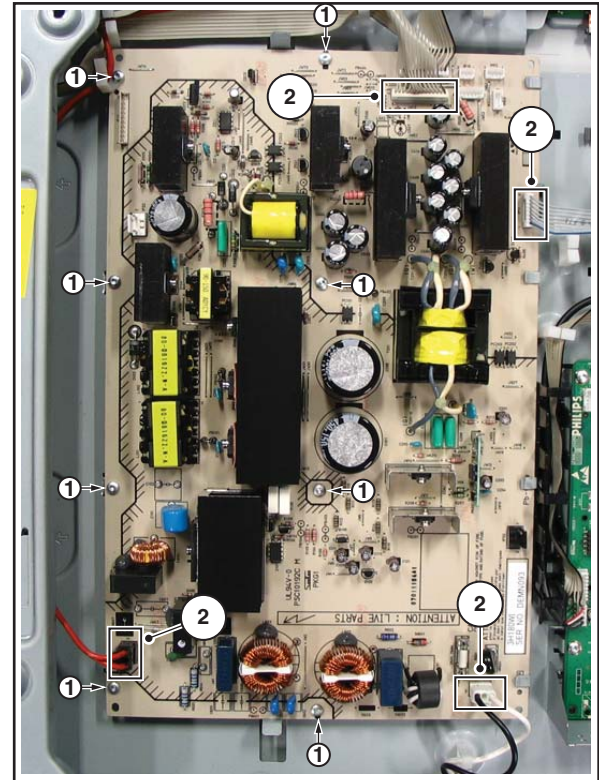
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Figure 4-17 Speakers

#### 4.3.7 Display Supply Panel - 47" sets

Refer to next figure for details.

1. Remove the fixation screws [1].
2. Unplug connectors [2].
3. Take the board out. It hinges on the right side.



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Figure 4-18 Display Supply Panel - 47" sets



#### 4.3.8 Small Signal Board (SSB)

**Caution:** it is mandatory to remount all different screws at their original position during re-assembly. Failure to do so may result in damaging the SSB.

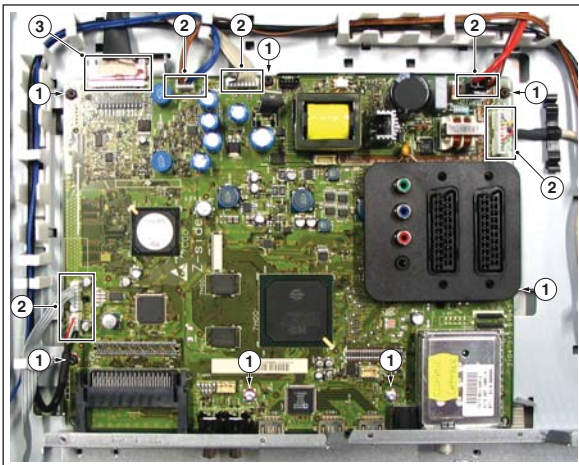
Refer to next figures or details.

1. Remove the tapping screws [1].
2. Unplug the connectors [2].
3. Unplug the LVDS connector [3]. **Caution:** be careful, as this is a very fragile connector!

The SSB can now be taken out of the set, together with the front shield.

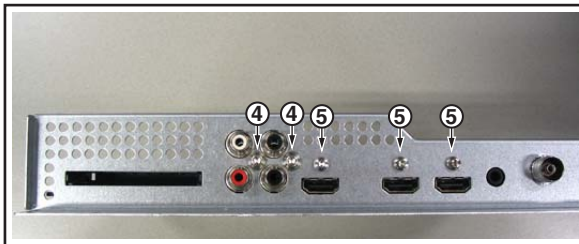
To remove the shield:

4. Remove the parker screws [4].
5. Remove the tapping screws [5].
6. Remove the shield from the SSB.



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Figure 4-19 Small Signal Board -1-



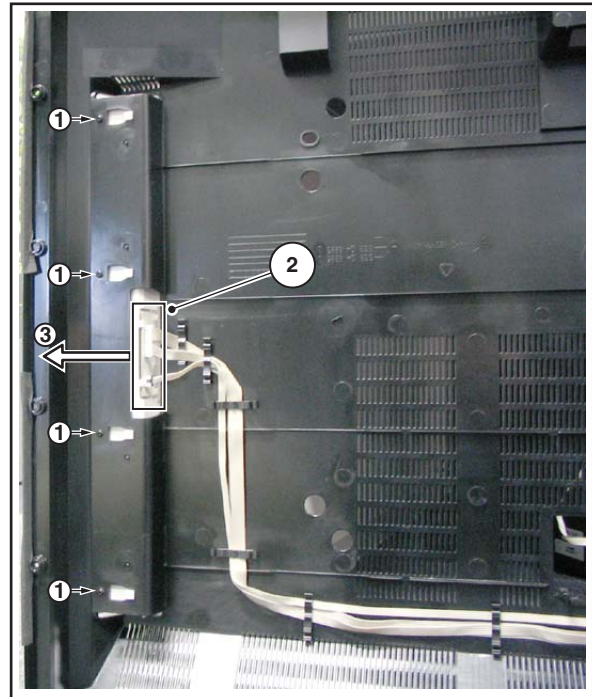
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Figure 4-20 Small Signal Board -2-

#### 4.3.9 AmbiLight Unit

The AmbiLight Units are located in the back cover. Refer to next figure for details.

1. Remove the screws [1].
2. Unplug the connectors [2].
3. Slide the unit sideways and take it out of the backcover.



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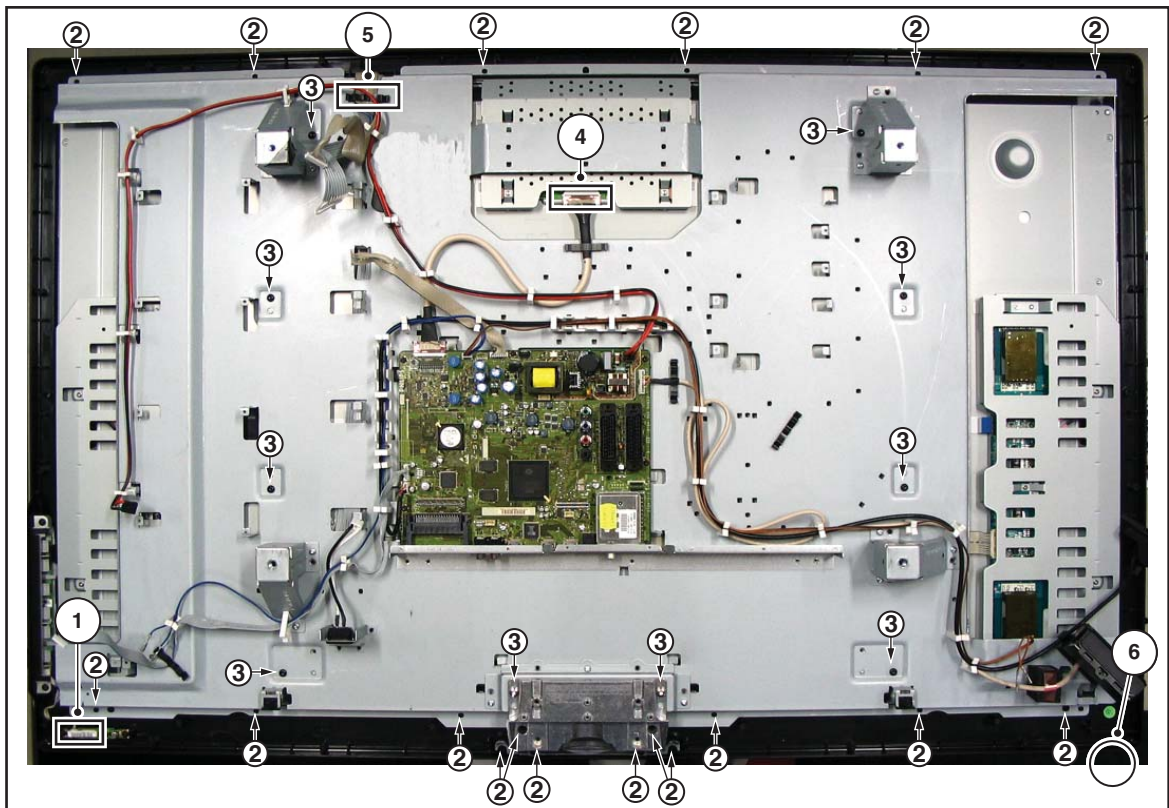
Figure 4-21 AmbiLight Unit

When defective, replace the whole unit.

#### 4.3.10 LCD Panel

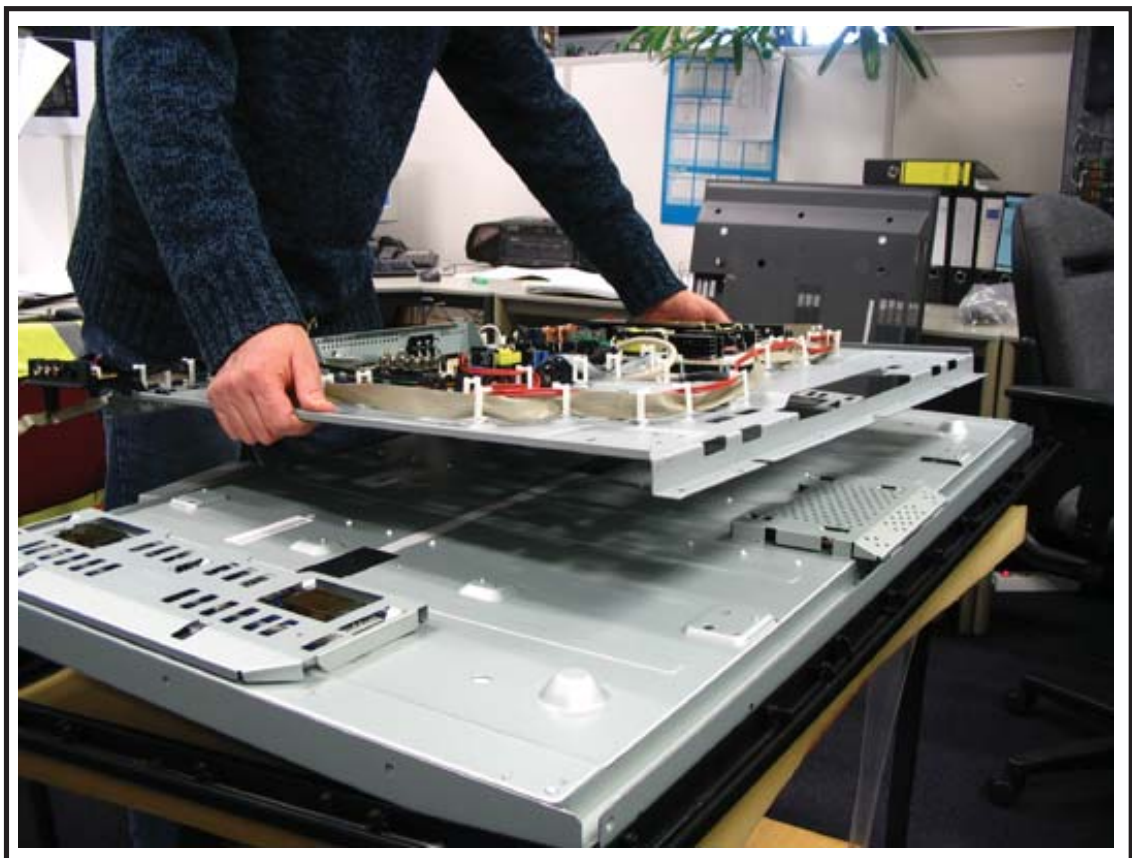
Refer to next figures for details.

1. Take the speakers out as earlier described.
2. Remove the LED/IR panel.
3. Unplug the connector [1].
4. Remove the Display Supply Panel, as earlier described.
5. Remove the parker screws [2].
6. Remove the tapping screws [3].
7. Unplug the LVDS connector [4] from the LCD panel. **Important:** Be careful, as this is a very fragile connector!
8. Remove the cables out of their bracket [5].
9. Remove the fixation screw [6] from the side I/O panel.
10. Lift the central sub-frame from the set.
11. Lift the LCD panel from the front cabinet.



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Figure 4-22 LCD Panel



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Figure 4-23 Central Sub-frame

#### 4.4 Set Re-assembly

To re-assemble the whole set, execute all processes in reverse order.

**Notes:**

- While re-assembling, make sure that all cables are placed and connected in their original position. See figure "Cable dressing".
- Pay special attention not to damage the EMC foams on the SSB shields. Ensure that EMC foams are mounted correctly.



# 5. Service Modes, Error Codes, and Fault Finding

**Index of this chapter:**

- 5.1 Test Points
- 5.2 Service Modes
- 5.3 Stepwise Start-up
- 5.4 Service Tools
- 5.5 Error Codes
- 5.6 The Blinking LED Procedure
- 5.7 Protections
- 5.8 Fault Finding and Repair Tips
- 5.9 Software Upgrading

- Picture mute (blue mute or black mute).
- Automatic volume levelling (AVL).
- Skip/blank of non-favourite pre-sets.

**How to Activate SDM**

For this chassis there are two kinds of SDM: an **analogue SDM** and a **digital SDM**. Tuning will happen according table "SDM Default Settings".

- **Analogue SDM:** use the standard RC-transmitter and key in the code "062596", directly followed by the "MENU" button.  
**Note:** It is possible that, together with the SDM, the main menu will appear. To switch it "off", push the "MENU" button again.
- **Digital SDM:** use the standard RC-transmitter and key in the code "062593", directly followed by the "MENU" button.  
**Note:** It is possible that, together with the SDM, the main menu will appear. To switch it "off", push the "MENU" button again.
- **Analogue SDM** can also be activated by shorting for a moment the two solder pads [1] (see figure "Service mode pads") on the SSB, with the indication "SDM". Activation can be performed in all modes, except when the set has a problem with the Stand-by Processor.

## 5.1 Test Points

As most signals are digital, it will be difficult to measure waveforms with a standard oscilloscope. However, several key ICs are capable of generating test patterns, which can be controlled via ComPair. In this way it is possible to determine which part is defective.

Perform measurements under the following conditions:

- Service Default Mode.
- Video: Colour bar signal.
- Audio: 3 kHz left, 1 kHz right.

## 5.2 Service Modes

Service Default mode (SDM) and Service Alignment Mode (SAM) offers several features for the service technician, while the Customer Service Mode (CSM) is used for communication between the call centre and the customer.

This chassis also offers the option of using ComPair, a hardware interface between a computer and the TV chassis. It offers the abilities of structured troubleshooting, error code reading, and software version read-out for all chassis. (see also paragraph "ComPair").

### 5.2.1 Service Default Mode (SDM)

**Purpose**

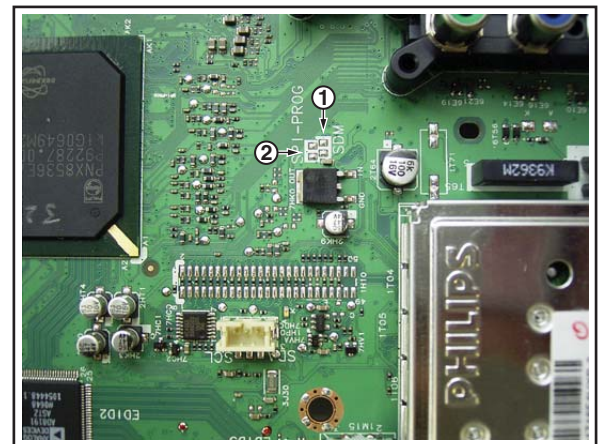
- To create a pre-defined setting, to get the same measurement results as given in this manual.
- To override SW protections detected by stand-by processor and make the TV start up to the step just before protection (a sort of automatic stepwise start up). See paragraph "Stepwise Start Up".
- To override SW protections detected by MIPS. See also paragraph "Error codes".
- To start the blinking LED procedure (not valid for protections detected by standby software).

**Specifications**

**Table 5-1 SDM default settings**

Region	Freq. (MHz)	Default system
Europe, AP(PAL/Multi)	475.25	PAL B/G
Europe, AP DVB-T	546.00 PID Video: 0B 06 PID PCR: 0B 06 PID Audio: 0B 07	DVB-T

- All picture settings at 50% (brightness, colour, contrast).
- All sound settings at 50%, except volume at 25%.
- All service-unfriendly modes (if present) are disabled, like:
  - (Sleep) timer.
  - Child/parental lock.



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**Figure 5-1 Service mode pads**

After activating this mode, "SDM" will appear in the upper right corner of the screen (if you have picture).

**How to Navigate**

When you press the "MENU" button on the RC transmitter, the set will toggle between the SDM and the normal user menu (with the SDM mode still active in the background).

**How to Exit SDM**

- Use one of the following methods:
- Switch the set to STAND-BY via the RC-transmitter.
  - Via a standard customer RC-transmitter: key in "00"-sequence.

### 5.2.2 Service Alignment Mode (SAM)

**Purpose**

- To perform (software) alignments.
- To change option settings.
- To easily identify the used software version.
- To view operation hours.
- To display (or clear) the error code buffer.

### How to Activate SAM

Via a standard RC transmitter: key in the code "062596" directly followed by the "INFO" button. After activating SAM with this method a service warning will appear on the screen, you can continue by pressing the red button on the RC.

### Contents of SAM:

#### • Hardware Info.

- **A. SW Version.** Displays the software version of the main software (**example:** Q581E-1.2.3.4\_12345 = AAAAB\_X.Y.W.Z\_NNNNN).
  - **AAAA**= the chassis name.
  - **B**= the region: A= AP, E= EU, L= LatAm, U = US. For AP sets it is possible that the Europe software version is used.
  - **X.Y.W.Z**= the software version, where X is the main version number (different numbers are not compatible with one another) and Y.W.Z is the sub version number (a higher number is always compatible with a lower number).
  - **NNNNN**= last five digits of 12nc code of the software.
- **B. SBY PROC Version.** Displays the software version of the stand-by processor.
- **C. Production Code.** Displays the production code of the TV, this is the serial number as printed on the back of the TV set. Note that if an NVM is replaced or is initialized after corruption, this production code has to be re-written to NVM. ComPair will foresee in a possibility to do this.
- **Operation Hours.** Displays the accumulated total of operation hours (not the stand-by hours). Every time the TV is switched "on/off", 0.5 hours is added to this number.
- **Errors** (followed by maximal 10 errors). The most recent error is displayed at the upper left (for an error explanation see paragraph "Error Codes").
- **Reset Error Buffer.** When you press "cursor right" (or the "OK" button) and then the "OK" button, the error buffer is reset.
- **Alignments.** This will activate the "ALIGNMENTS" sub-menu.
- **Dealer Options.** Extra features for the dealers.
- **Options.** Extra features for Service. For more info regarding option codes, see chapter 8 "Alignments". Note that if you change the option code numbers, you have to confirm your changes with the "OK" button before you store the options. Otherwise you will lose your changes.
- **Initialize NVM.** The moment the processor recognizes a corrupted NVM, the "initialize NVM" line will be highlighted. Now, you can do two things (dependent of the service instructions at that moment):
  - Save the content of the NVM via ComPair for development analysis, **before** initializing. This will give the Service department an extra possibility for diagnosis (e.g. when Development asks for this).
  - Initialize the NVM.

**Note:** When you have a corrupted NVM, or you have replaced the NVM, there is a high possibility that you will not have picture anymore because your display code is not correct. So, before you can initialize your NVM via the SAM, you need to have a picture and therefore you need the correct display option. Refer to chapter 8 for details. To adapt this option, you can use ComPair (the correct HEX values for the options can be found in chapter 8 "Alignments") or a method via a standard RC (described below).

**Changing the display option via a standard RC:** Key in the code "062598" directly followed by the "MENU" button and "XXX", where XXX is the 3 digit decimal display code (see table "Option code overview" in chapter 8 "Alignments", or sticker on the side/bottom of the cabinet). Make sure to key in all three digits, also the leading zero's. If the above action is successful, the front LED will go out as an indication that the RC sequence was correct. After the display option is changed in the NVM,

the TV will go to the Stand-by mode. If the NVM was corrupted or empty before this action, it will be initialized first (loaded with default values). This initializing can take up to 20 seconds.

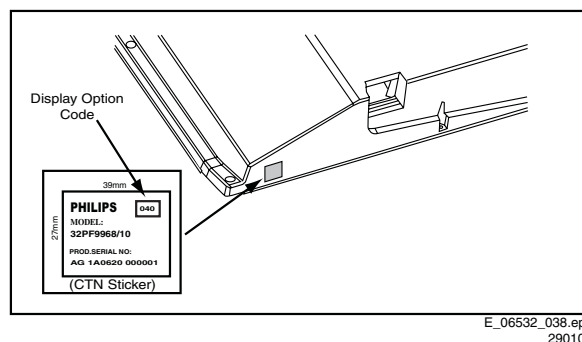


Figure 5-2 Location of Display Option Code sticker

- **Store.** All options and alignments are stored when pressing "cursor right" (or the "OK" button) and then the "OK"-button.
- **SW Maintenance.**
  - **SW Events.** Not useful for Service purposes. In case of specific software problems, the development department can ask for this info.
  - **HW Events.** Not useful for Service purposes. In case of specific software problems, the development department can ask for this info.
- **Test settings.** For development purposes only.
- **Upload to USB.** To upload several settings from the TV to a USB stick, which is connected to the Side I/O. The items are "Channel list", "Personal settings", "Option codes", "Display-related alignments" and "History list". First you have to create a directory "repair" in the root of the USB stick. To upload the settings you have to select each item separately, press "cursor right" (or the "OK" button), confirm with "OK" and wait until "Done" appears. In case the download to the USB stick was not successful "Failure" will appear. In this case, check if the USB stick is connected properly and if the directory "repair" is present in the root of the USB stick. Now the settings are stored onto your USB stick and can be used to download onto another TV or other SSB. Uploading is of course only possible if the software is running and if you have a picture. This method is created to be able to save the customer's TV settings and to store them into another SSB.
- **Download from USB.** To download several settings from the USB stick to the TV. Same way of working as with uploading. To make sure that the download of the channel list from USB to the TV is executed properly, it is necessary to restart the TV and tune to a valid preset if necessary. **Note:** The "History list item" can not be downloaded from USB to the TV. This is a "read-only" item. In case of specific problems, the development department can ask for this info.

### How to Navigate

- In SAM, you can select the menu items with the "CURSOR UP/DOWN" key on the RC-transmitter. The selected item will be highlighted. When not all menu items fit on the screen, move the "CURSOR UP/DOWN" key to display the next/previous menu items.
- With the "CURSOR LEFT/RIGHT" keys, it is possible to:
  - (De) activate the selected menu item.
  - (De) activate the selected sub menu.
- With the "OK" key, it is possible to activate the selected action.

### How to Exit SAM

Use one of the following methods:

- Press the "MENU" button on the RC-transmitter.
- Switch the set to STAND-BY via the RC-transmitter.



### 5.2.3 Customer Service Mode (CSM)

#### Purpose

When a customer is having problems with his TV-set, he can call his dealer or the Customer Helpdesk. The service technician can then ask the customer to activate the CSM, in order to identify the status of the set. Now, the service technician can judge the severity of the complaint. In many cases, he can advise the customer how to solve the problem, or he can decide if it is necessary to visit the customer.

The CSM is a read only mode; therefore, modifications in this mode are not possible.

When in this chassis CSM is activated, a colour bar test pattern will be visible for 5 seconds. This test pattern is generated by the Pacific3. So if you see this test pattern you can determine that the back end video chain (Pacific3, LVDS, and display) of the SSB is working. In case of a set with DFI panel, an extra test picture is generated. So you will see the Pacific3 test picture for 3 seconds and then the DFI EPLD test picture for another 3 seconds. With this extra test picture you can determine if the DFI board is working properly.

Also new in this chassis: when you activate CSM and there is a USB stick connected to the TV, the software will dump the complete CSM content to the USB stick. The file (Csm.txt) will be saved in the root of your USB stick. This info can be handy if you do not have picture.

Another new item in this chassis is when CSM is activated, the complete error-buffer content will be shown via the blinking LED procedure.

#### How to Activate CSM

Key in the code "123654" via the standard RC transmitter.

**Note:** Activation of the CSM is only possible if there is no (user) menu on the screen!

#### How to Navigate

By means of the "CURSOR-DOWN/UP" knob on the RC-transmitter, you can navigate through the menus.

#### Contents of CSM

- **Set Type.** This information is very helpful for a helpdesk/workshop as reference for further diagnosis. In this way, it is not necessary for the customer to look at the rear of the TV-set. Note that if an NVM is replaced or is initialized after corruption, this set type has to be re-written to NVM. ComPair will foresee in a possibility to do this.
- **Production Code.** Displays the production code (the serial number) of the TV. Note that if an NVM is replaced or is initialized after corruption, this production code has to be re-written to NVM. ComPair will foresee in a possibility to do this.
- **Code 1.** Gives the last five errors of the error buffer. As soon as the built-in diagnose software has detected an error, the buffer is adapted. The last occurred error is displayed on the leftmost position. Each error code is displayed as a 2-digit number. When less than 10 errors occur, the rest of the buffer is empty (00). See also paragraph "Error Codes" for a description.
- **Code 2.** Gives the first five errors of the error buffer. See also paragraph "Error Codes" for a description.
- **Options 1.** Gives the option codes of option group 1 as set in SAM (Service Alignment Mode).
- **Options 2.** Gives the option codes of option group 2 as set in SAM (Service Alignment Mode).
- **12NC SSB.** Gives an identification of the SSB as stored in NVM. Note that if an NVM is replaced or is initialized after corruption, this identification number has to be re-written to NVM. ComPair will foresee in a possibility to do this. This identification number consists of 14 characters and is built up as follows:
  - Eight last characters of the 12NC of the SSB itself.

- the serial number of the SSB, which consists of six digits. Both can be found on a sticker on the PWB of the SSB itself. The format of the identification number is then as follows: <last eight characters of 12NC of SSB><serial number of SSB> (total fourteen characters).

- **Installed date.** Indicates the date of the first installation of the TV. This date is acquired via time extraction.
- **Digital Natural Motion.** Gives the status of the Digital Natural Motion setting as set by the customer. Remark : a customer can choose between "OFF", "MINIMUM" and "MAXIMUM", but in CSM this item will only show "OFF" or "ON" ("ON" in case the customer has chosen "MINIMUM" or "MAXIMUM")
- **Pixel Plus.** Gives the last status of the Perfect Pixel HD setting, as set by the customer. Possible values are "ON" and "OFF". See DFU on how to change this item.
- **DNR.** Gives the last status of the Noise reduction setting, as set by the customer. Possible values are "OFF", "MINIMUM", "MEDIUM" and "MAXIMUM". See DFU on how to change this item.
- **Noise Figure.** Gives an indication of the signal quality for the selected transmitter. Possible values are "BAD", "AVERAGE", "GOOD" and "DIGITAL". In case of a digital channel, this item will never indicate : "BAD", "GOOD" or "AVERAGE" but only displays "DIGITAL".
- **12NC Display.** Shows the 12NC of the display.
- **Headphone Volume.** Gives the last status of the headphone volume, as set by the customer. The value can vary from 0 (volume is minimum) to 100 (volume is maximum). See DFU on how to change this item.
- **Surround Mode.** Indicates the by the customer selected sound mode (or automatically chosen mode). Possible values are "STEREO" and "VIRTUAL DOLBY SURROUND". It can also have been selected automatically by signalling bits (internal software). See DFU on how to change this item.
- **AVL.** Indicates the last status of AVL (Automatic Volume Level) as set by the customer: See DFU on how to change this item.
- **Delta Volume.** Indicates the last status of the delta volume for the selected preset as set by the customer: from "-12" to "+12". See DFU on how to change this item.
- **Volume.** Indicates the last status of the volume for the selected preset as set by the customer: from "0" to "100". See DFU on how to change this item.
- **Balance.** Indicates the last status of the balance for the selected preset as set by the customer: from "-10" to "+10". See DFU on how to change this item.
- **Preset Lock.** Indicates if the selected preset has a child lock: "LOCKED" or "UNLOCKED". See DFU on how to change this item.
- **Lock after.** Indicates at what time the channel lock is set: "OFF" or e.g. "18:45" (lock time). See DFU on how to change this item.
- **Parental rating lock.** Indicates the "Parental rating" as set by the customer. See DFU on how to change this item.
- **Parental rating status.** Indicates the "Parental rating" as transmitted by the broadcaster (if applicable). If the parental rating status is indicating a higher age then the parental rating lock as set by the customer, you will need to enter the child lock code.
- **TV ratings lock.** Only applicable for US.
- **Movie ratings lock.** Only applicable for US.
- **On timer.** Indicates if the "On timer" is set "ON" or "OFF" and when it is set to "ON", also start time, start day and program number is displayed. See DFU on how to change this item.
- **Location.** Gives the last status of the location setting as set via the installation menu. Possible values are "SHOP" and "HOME". If the location is set to "SHOP", several settings are fixed. So for a customer location must be set to "HOME". Can be changed via the installation menu (see also DFU).
- **HDMI key validity.** Indicates if the HDMI keys (or HDCP keys) are valid or not. In case these keys are not valid and

the customer wants to make use of the HDMI functionality, the SSB has to be replaced.

- **Tuner frequency.** Indicates the frequency the transmitter is tuned to.
- **TV System.** Gives information about the video system of the selected transmitter. In case a DVBT signal is received this item will also show ATSC.
  - BG: PAL BG signal received
  - DK: PAL DK signal received
  - L/La: SECAM L/La signal received
  - I: PAL I signal received
  - M: NTSC M signal received
  - ATSC: ATSC signal received
  - DVB : DVBT signal received
- **12NC one zip SW.** Displays the 12NC number of the one-zip file as it is used for programming software in production. In this one-zip file all below software versions can be found.
- **Initial main SW.** Displays the main software version which was initially loaded by the factory.
- **Current main SW.** Displays the built-in main software version. In case of field problems related to software, software can be upgraded. As this software is consumer upgradeable, it will also be published on the Internet. Example: Q581E\_1.2.3.4.
- **Flash utils SW.** Displays the software version of the software which contains all necessary components of the download application. To program this software, EJTAG tooling is needed. Example: Q581E\_1.2.3.4.
- **Standby SW.** Displays the built-in stand-by processor software version. Upgrading this software will be possible via ComPair or via USB (see chapter Software upgrade). Example: STDBY\_3.0.1.2.
- **MOP SW.** Only applicable for US. At the time of release of this manual, there was still a problem with this item, and some rubbish was displayed. Ignore this.
- **Pacific 3 Flash SW.** Displays the Pacific 3 software version.
- **NVM version.** Displays the NVM version as programmed by factory.
- **Display parameters.** for development purposes only.
- **Private PQ parameters.** for development purposes only.
- **Public PQ parameters.** for development purposes only.
- **Ambilight parameters.** for development purposes only.
- **Acoustics parameters.** for development purposes only.
- **DFI software (if applicable).** Displays the DFI EPLD software.
- **DFI ambilight software (if applicable).** Displays the DFI ambilight EPLD software.

#### **How to Exit CSM**

Press "MENU" on the RC-transmitter.

### 5.3 Stepwise Start-up

There are two possible situations: one for protections detected by the stand-by software and one for protections detected by the main software.

When the TV is in a protection state due to an error detected by stand-by software (and thus blinking an error) **and** SDM is activated via short-circuiting the pins on the SSB, the TV starts up until it reaches the situation just before protection. So, this is a kind of automatic stepwise start-up. In combination with the start-up diagrams below, you can see which supplies are present at a certain moment. Important to know is, that if e.g. the 3V3 detection fails (and thus error 8 is blinking) **and** the TV is restarted via SDM, the Stand-by Processor will enable the 3V3, but will not go to protection now. The TV will stay in this situation until it is reset (Mains/AC Power supply interrupted).

**Caution:** in case the start up in this mode with a faulty FET 7U01 is done, you can destroy all IC's supplied by the +3V3,

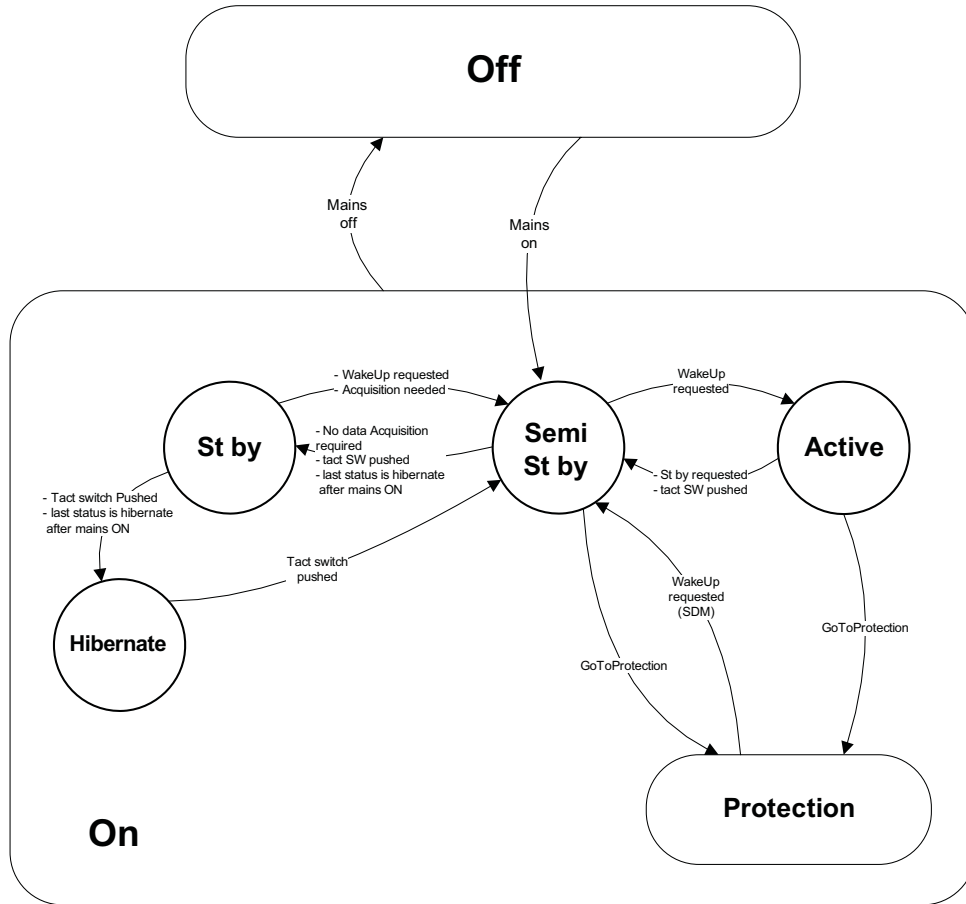
due to overvoltage. It is recommended to measure first the FET 7U01 on short-circuit before activating SDM via the service pads.

When the TV is in protection state due to an error detected by main software (MIPS protection) **and** SDM is activated via short cutting the service pads on the SSB, the TV starts up and ignores the error.

In this chassis, only error "63" (power-ok) is a MIPS protection and already displays the failure via blinking LED.

The abbreviations "SP" and "MP" in the figures stand for:

- SP: protection or error detected by the Stand-by Processor.
- MP: protection or error detected by the MIPS Main Processor.



In US region, hibernate state and tact switch are not available.  
Here the local keyboard tact switch acts as standby button.

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Figure 5-3 Transition diagram

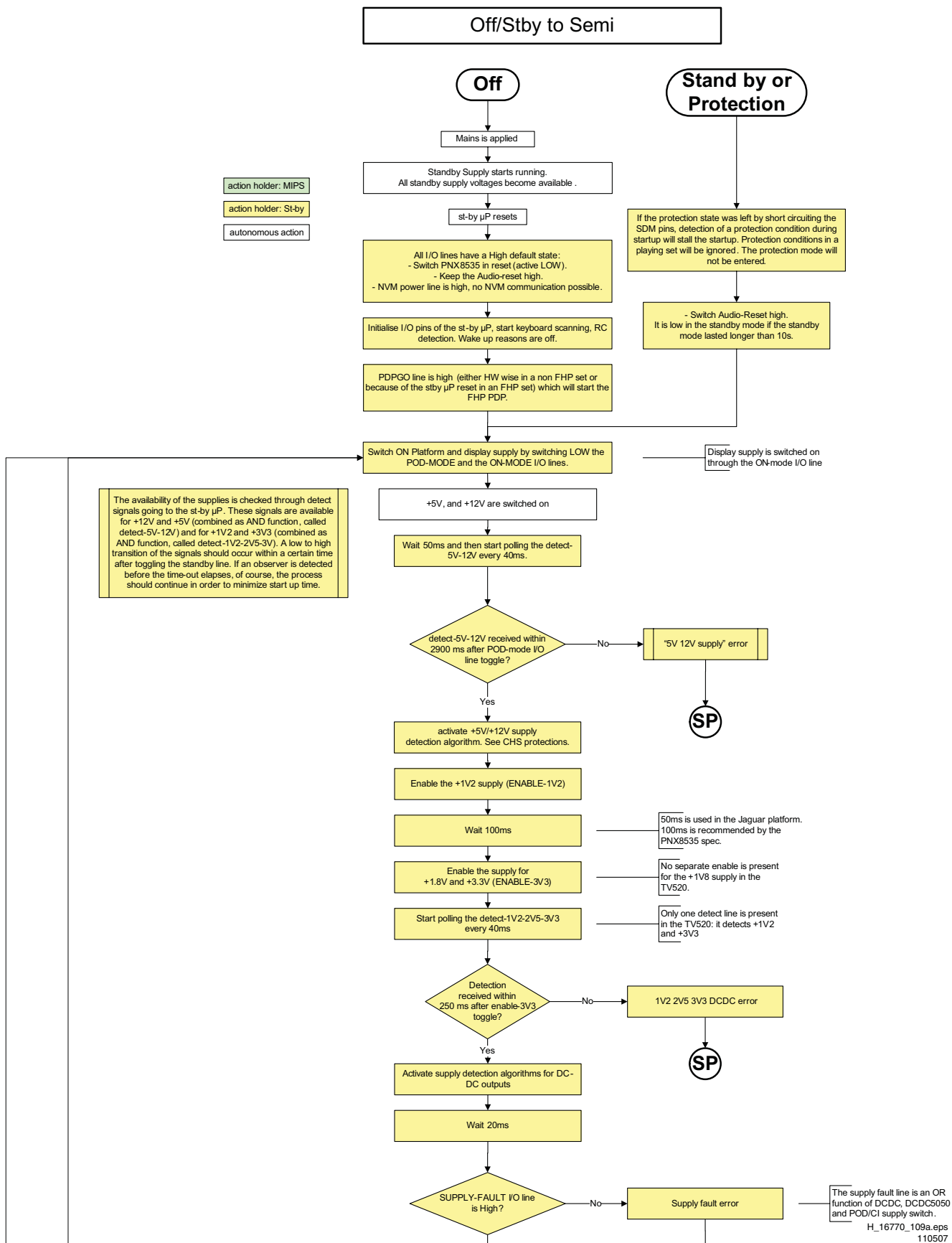


Figure 5-4 "Off" to "Semi Stand-by" flowchart (part 1)

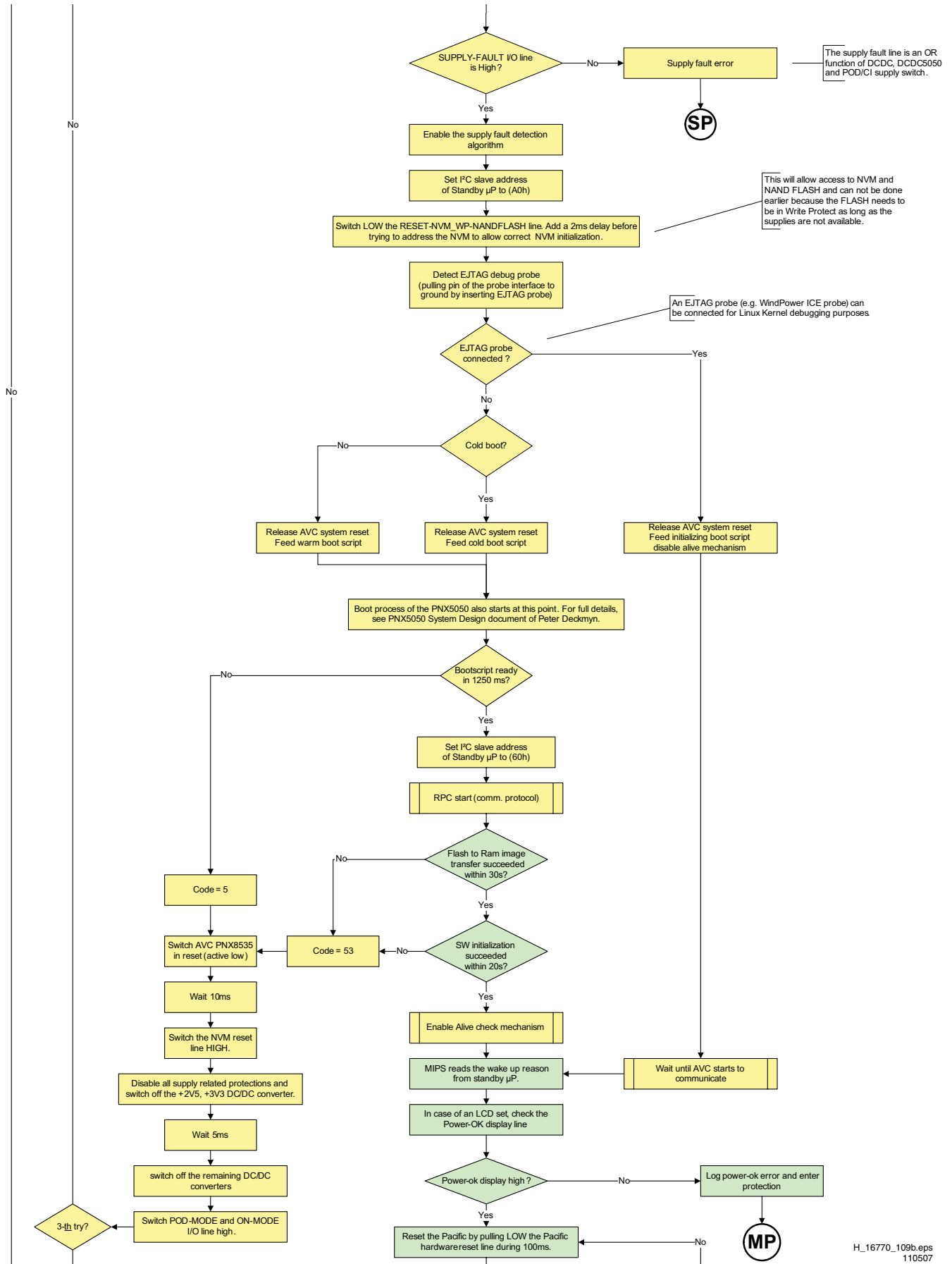


Figure 5-5 “Off” to “Semi Stand-by” flowchart (part 2)

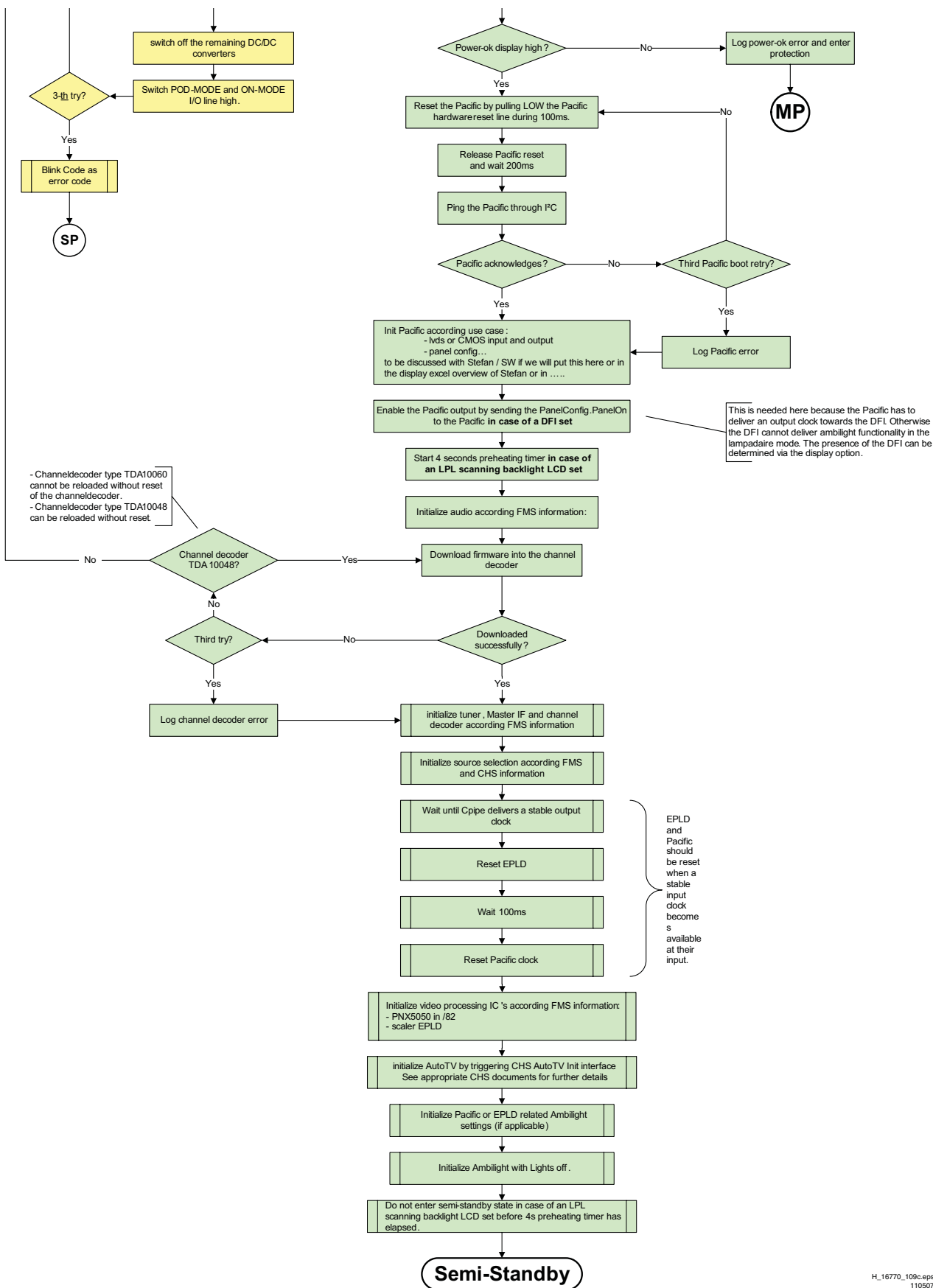
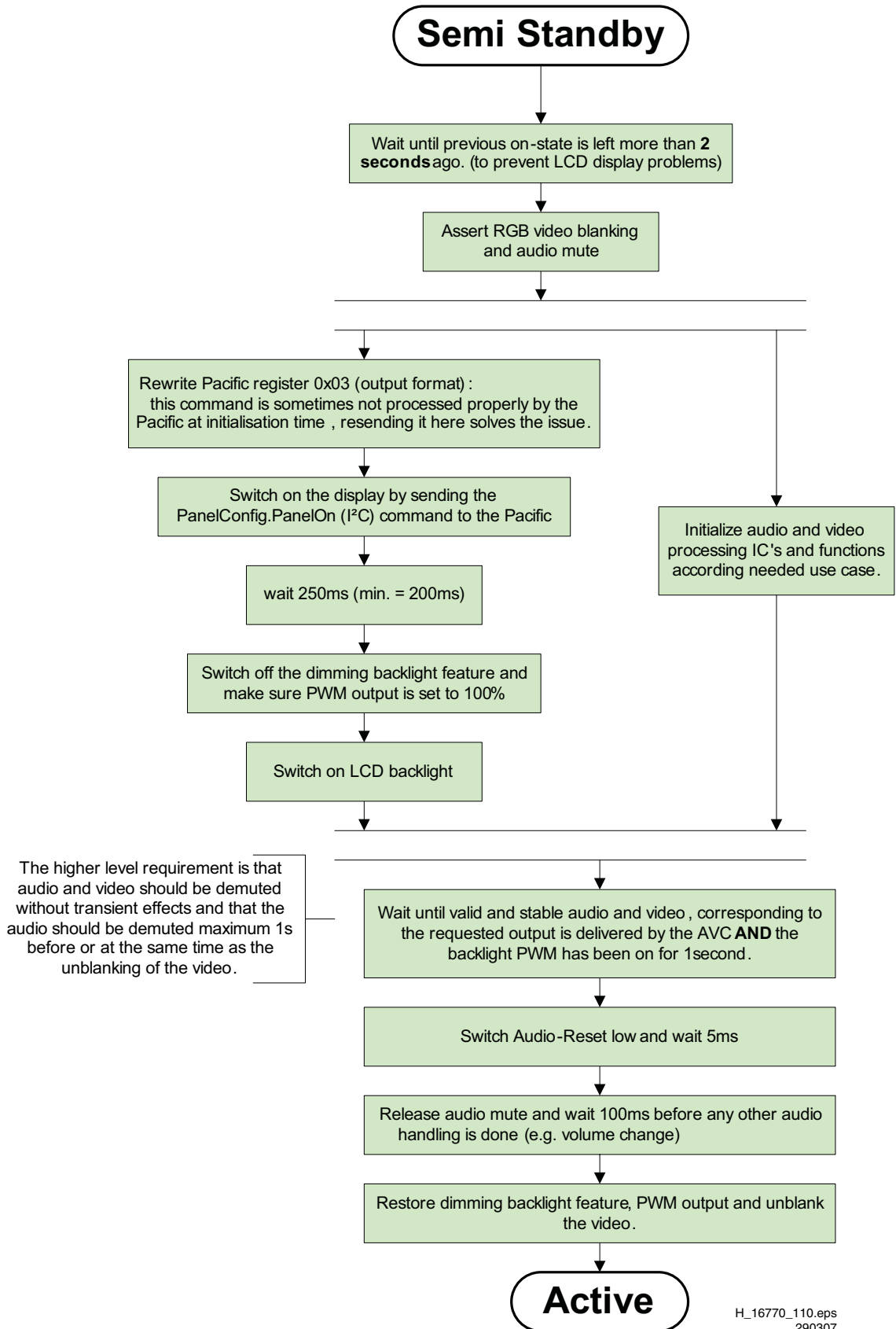


Figure 5-6 “Off” to “Semi Stand-by” flowchart (part 3)

action holder: AVC

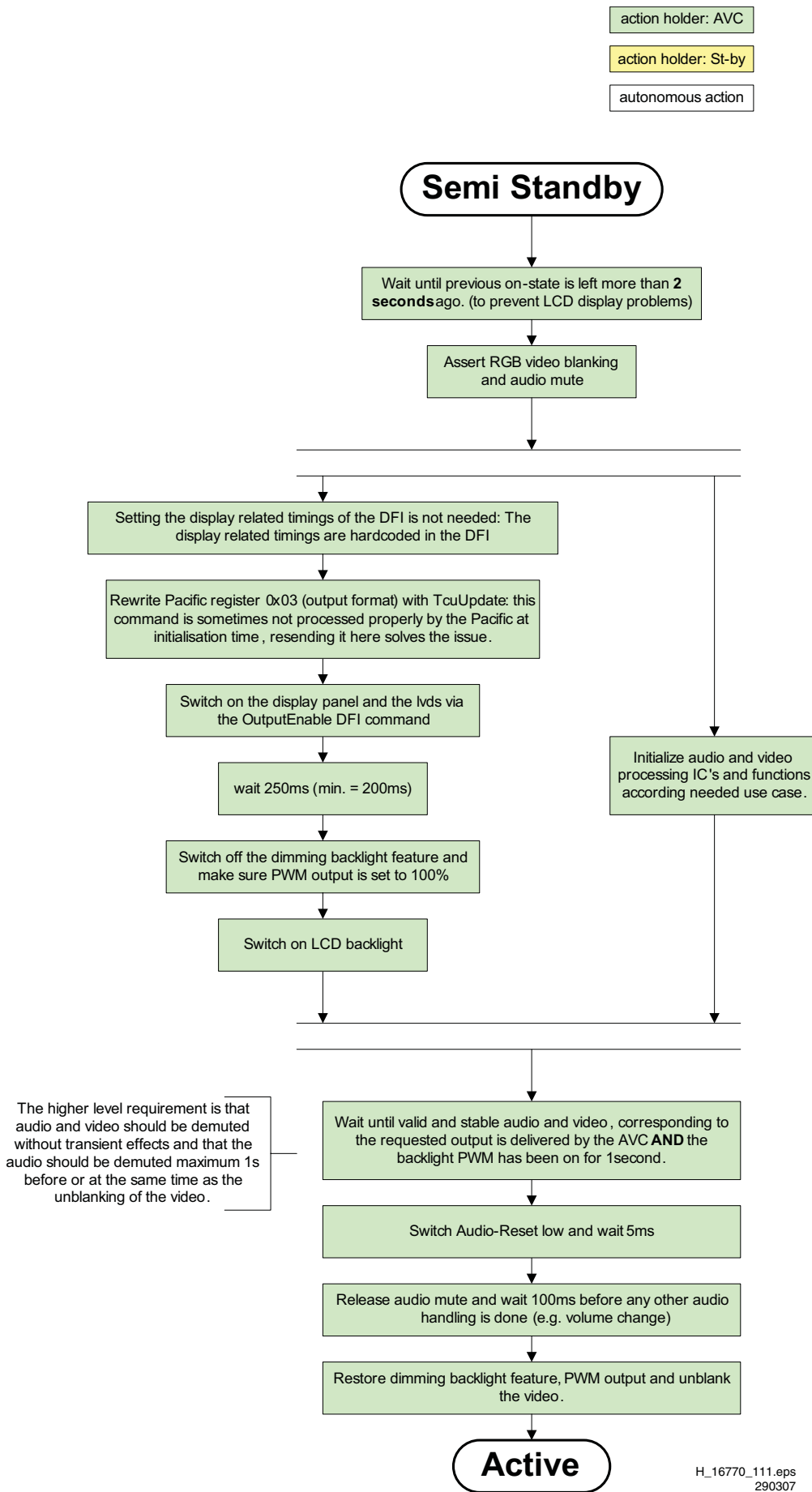
action holder: St-by

autonomous action



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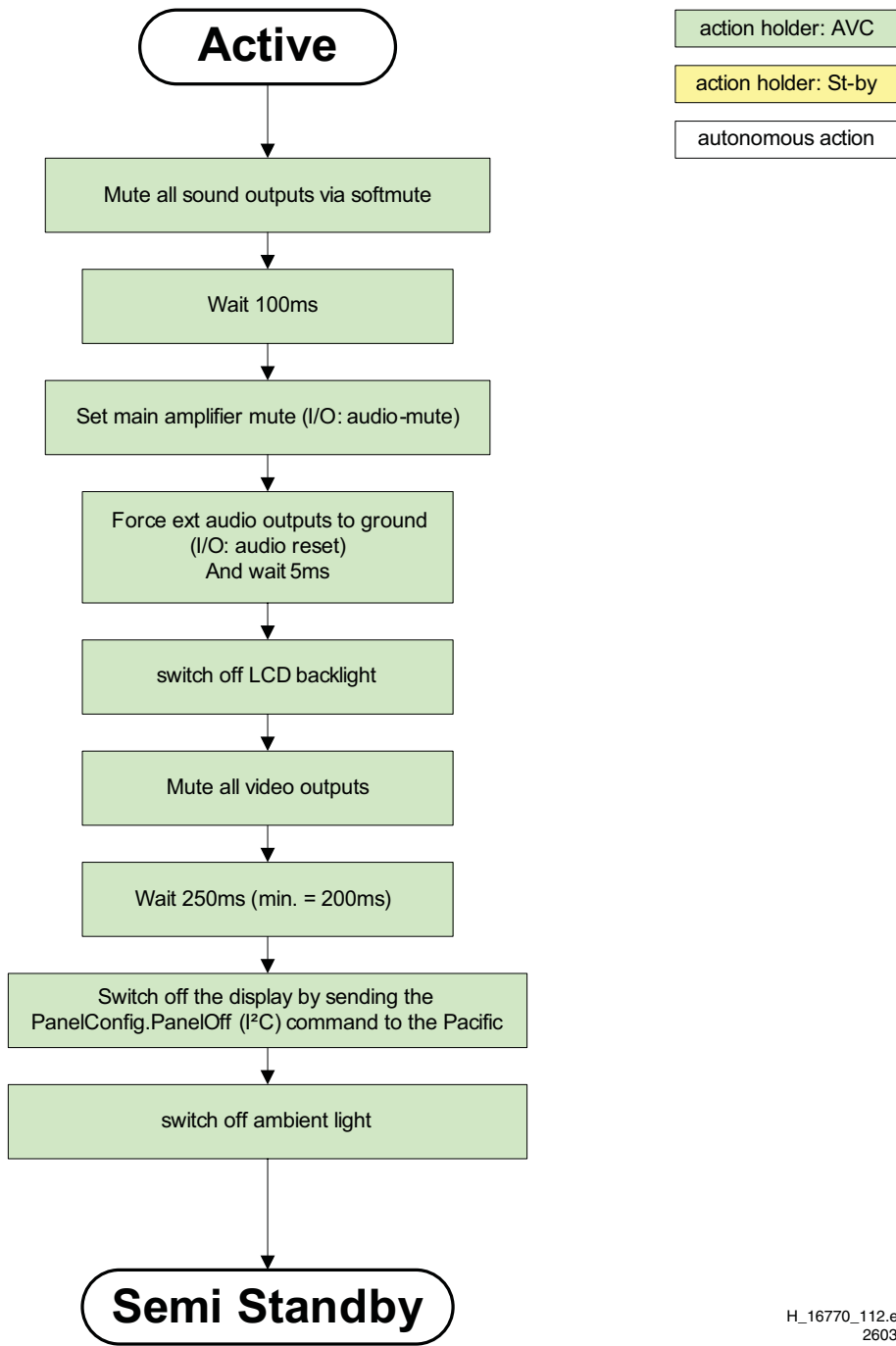
Figure 5-7 “Semi Stand-by” to “Active” flowchart non DFI



action holder: AVC  
 action holder: St-by  
 autonomous action

Figure 5-8 “Semi Stand-by” to “Active” flowchart DFI





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Figure 5-9 “Active” to “Semi Stand-by” flowchart (non-DFI)

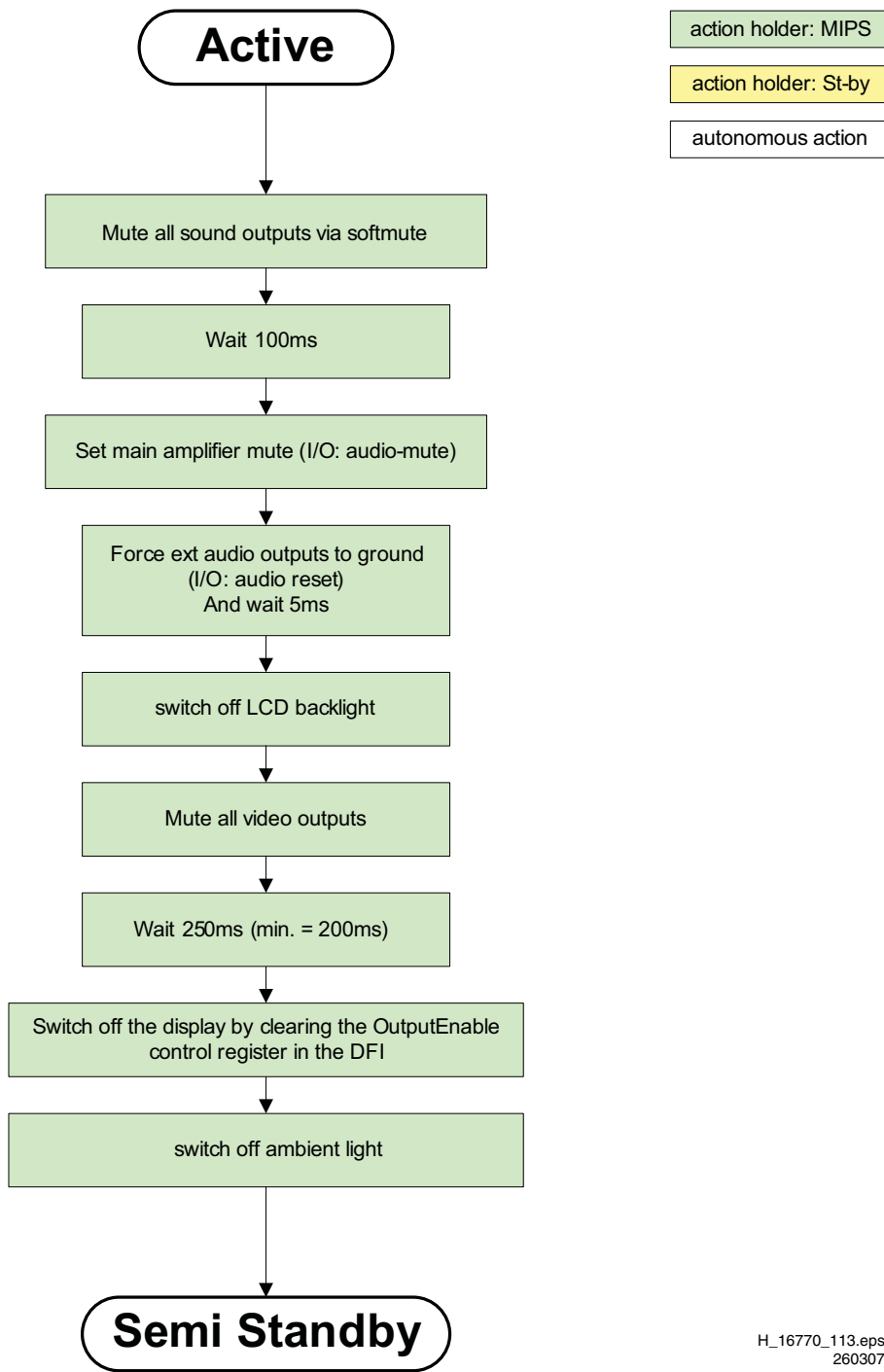


Figure 5-10 “Active” to “Semi Stand-by” flowchart (DFI)

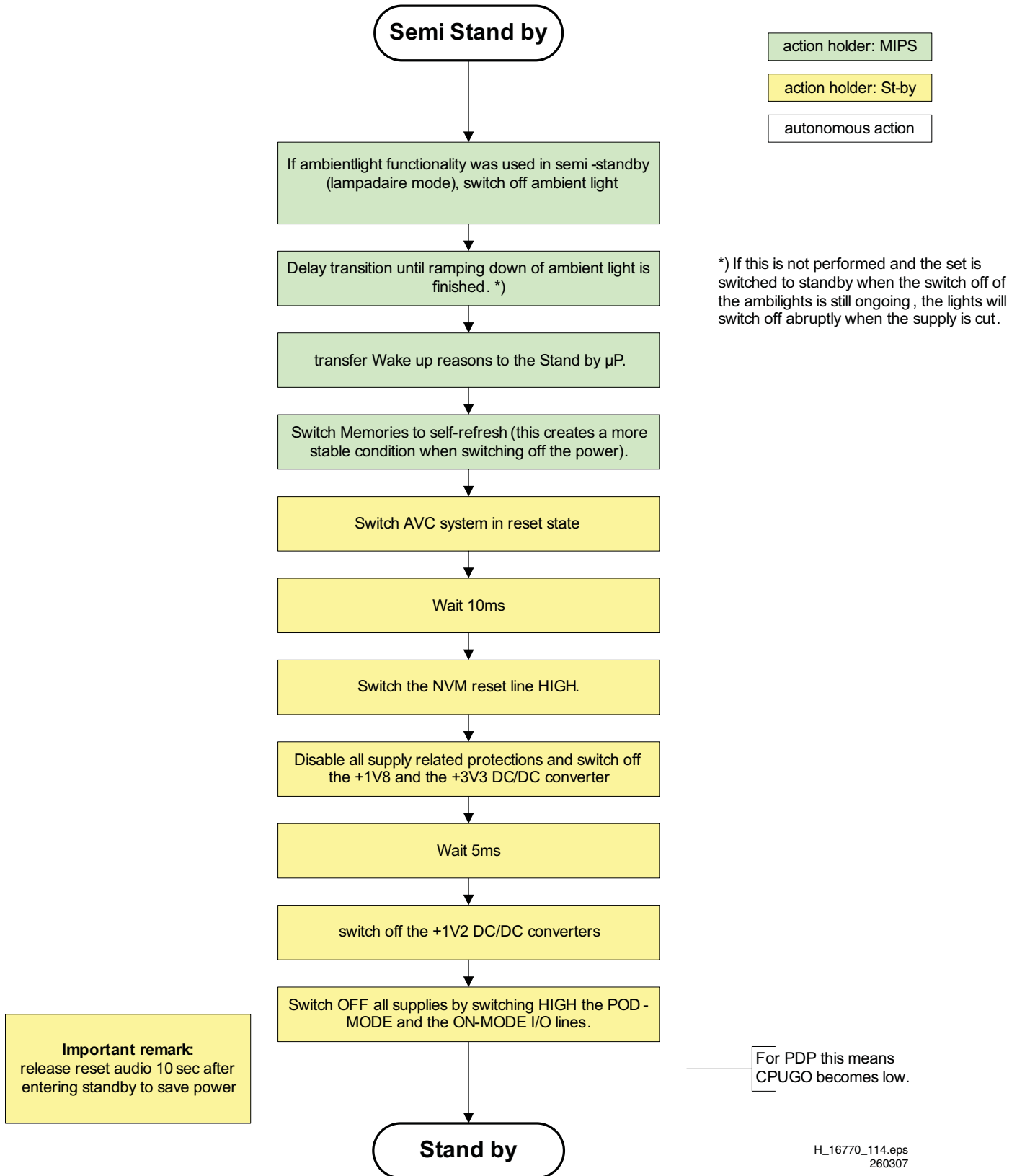


Figure 5-11 “Semi Stand-by” to “Stand-by” flowchart

- action holder: MIPS
- action holder: St-by
- autonomous action

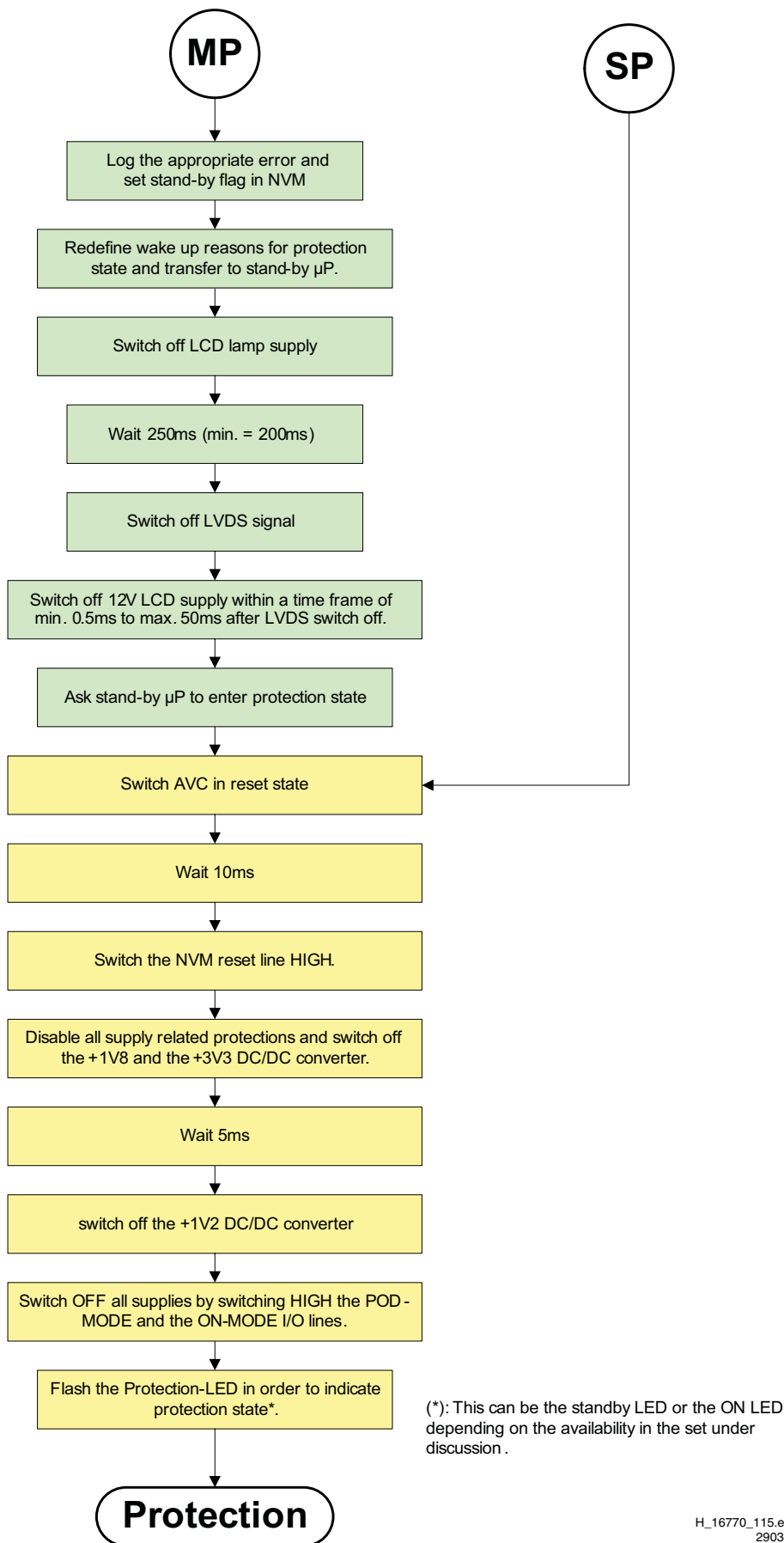


Figure 5-12 "Protection" flowchart

5.4 Service Tools

5.4.1 ComPair

Introduction

ComPair (Computer Aided Repair) is a Service tool for Philips Consumer Electronics products. and offers the following:

1. ComPair helps you to quickly get an understanding on how to repair the chassis in a short and effective way.
2. ComPair allows very detailed diagnostics and is therefore capable of accurately indicating problem areas. You do not have to know anything about I2C or UART commands yourself, because ComPair takes care of this.
3. ComPair speeds up the repair time since it can automatically communicate with the chassis (when the uP is working) and all repair information is directly available.
4. ComPair features TV software upgrade possibilities.

Specifications

ComPair consists of a Windows based fault finding program and an interface box between PC and the (defective) product. The (new) ComPair II interface box is connected to the PC via an USB cable. For the TV chassis, the ComPair interface box and the TV communicate via a bi-directional cable via the service connector(s).

The ComPair fault finding program is able to determine the problem of the defective television, by a combination of automatic diagnostics and an interactive question/answer procedure.

How to Connect

This is described in the chassis fault finding database in ComPair.

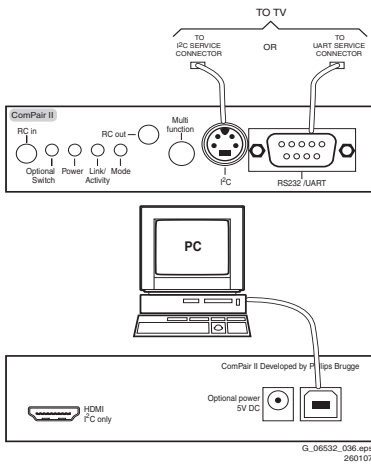


Figure 5-13 ComPair II interface connection

**Caution:** It is compulsory to connect the TV to the PC as shown in the picture above (with the ComPair interface in between), as the ComPair interface acts as a level shifter. If one connects the TV directly to the PC (via UART), ICs will be blown!

How to Order

ComPair II order codes:

- ComPair II interface: 3122 785 91020.
- ComPair32 CD (update): 3122 785 60160.
- ComPair interface cable: 3122 785 90004.
- ComPair interface extension cable: 3139 131 03791.
- ComPair UART interface cable: 3122 785 90630.
- ComPair UART interface cable for Q52x.x (using 3.5 mm Mini Jack connector): 3104 311 12742.

**Note:** If you encounter any problems, contact your local support desk

5.4.2 LVDS Tool

Introduction

This Service tool (also called “ComPair Assistant 1”) may help you to identify, in case the TV does not show any picture, whether the Small Signal Board (SSB) or the display of a Flat TV is defective. Thus to determine if LVDS, RGB, and sync signals are okay.

When operating, the tool will show a small (scaled) picture on a VGA monitor. Due to a limited memory capacity, it is not possible to increase the size when processing high-resolution LVDS signals (> 1280x960). Below this resolution, or when a DVI monitor is used, the displayed picture will be full size.

How to Connect

Connections are explained in the user manual, which is packed with the tool. The LVDS cables included in the package cover most chassis. For some chassis, a separate cable must be ordered.

**Note:** To use the LVDS tool, you must have ComPair release 2004-1 (or later) on your PC (engine version >= 2.2.05). For every TV type number and screen size, one must choose the proper settings via ComPair. The ComPair file will be updated regularly with new introduced chassis information.

How to Order

- LVDS tool (incl. two LVDS cables: 31p and 20p, covering chassis BJx, EJx, FJx and LC4.1): 3122 785 90671.
- LVDS tool Service Manual: 3122 785 00810.
- LVDS cable 20p/DF -> 20p/DF (standard with tool): 3122 785 90731.
- LVDS cable 31p/FI -> 31p/FI (standard with tool): 3122 785 90662.

For other chassis, a separate LVDS cable must be ordered. Refer to table “LVDS cable order number” for an overview of all available cables.

Table 5-2 LVDS cable order number

Chassis	LVDS cable order number	Remarks
BJ2.4	3122 785 90662 <sup>1</sup>	
BJ2.5	3122 785 90662 <sup>1</sup>	
BJ3.0	3122 785 90662 <sup>1</sup>	
BJ3.1	3122 785 90662 <sup>1</sup>	
EJ2.0	3122 785 90662 <sup>1</sup>	
EJ3.0	3122 785 90662 <sup>1</sup>	
EL1.1	3122 785 90662 <sup>1</sup> / 3122 785 90821	
FJ3.0	3122 785 90662 <sup>1</sup>	
FTL2.4	3122 785 90662 <sup>1, 2</sup>	
LC4.1	3122 785 90731 <sup>1</sup> / 3122 785 90851	
LC4.3	3122 785 90821	
LC4.31	3122 785 90821	
LC4.41	3122 785 90662 <sup>1, 2</sup> / 3122 785 90851	Only for 26 & 32" sets.
LC4.8	3122 785 90662 <sup>1, 2</sup> / 3122 785 90851	
LC4.9	3122 785 90662 <sup>1, 2</sup> / 3122 785 90851	MFD variant only.
LC7.x	t.b.d.	
JL2.1	3122 785 90861	
Q52x.x	t.b.d.	

Notes:

1. Included in LVDS tool package.
2. Pins “27” and “28” must be grounded or not connected.

5.5 Error Codes

5.5.1 Introduction

The error code buffer contains all detected errors since the last time the buffer was erased. The buffer is written from left to

right, new errors are logged at the left side, and all other errors shift one position to the right.

When an error occurs, it is added to the list of errors, provided the list is not full. When an error occurs and the error buffer is full, then the new error is not added, and the error buffer stays intact (history is maintained).

To prevent that an occasional error stays in the list forever, the error is removed from the list after more than 50 hrs. of operation.

When multiple errors occur (errors occurred within a short time span), there is a high probability that there is some relation between them.

Basically there are three kinds of errors:

- **Errors detected by the Stand-by software.** These errors will always lead to protection and an automatic start of the blinking LED for the concerned error (see paragraph "The Blinking LED Procedure"). In these cases SDM can be used to start up (see chapter "Stepwise Start-up"). Note that it can take up to 90 seconds before the TV goes to protection and starts blinking the error (e.g. error 53)
- **Errors detected by main software that lead to protection.** In this case the TV will go to protection and the front LED should also blink the concerned error. See also paragraph "Error Codes" -> "Error Buffer" -> "Extra Info". For this chassis only error 63 is a protection error detected by main software.
- **Errors detected by main software that do not lead to protection.** In this case the error will be logged into the error buffer and can be read out via ComPair, via blinking LED method, or in case you have picture, via SAM.

Take notice that some errors need more than 90 seconds before they start blinking or before they will be logged. So in case of problems wait 2 minutes from start-up onwards, and then check if the front LED is blinking or if an error is logged.

### 5.5.2 How to Read the Error Buffer

Use one of the following methods:

- On screen via the SAM (only if you have a picture). E.g.:
  - **00 00 00 00 00:** No errors detected
  - **06 00 00 00 00:** Error code 6 is the last and only detected error
  - **09 06 00 00 00:** Error code 6 was first detected and error code 9 is the last detected error
- Via the blinking LED procedure (when you have no picture). See next paragraph.
- Via ComPair.
- Via CSM. when CSM is activated the blinking LED procedure will start and the CSM content will be written to a USB stick (if present).

### 5.5.3 How to Clear the Error Buffer

Use one of the following methods:

- By activation of the "RESET ERROR BUFFER" command in the SAM menu.
- With a normal RC, key in sequence "MUTE" followed by "062599" and "OK".
- If the content of the error buffer has not changed for 50+ hours, it resets automatically.

### 5.5.4 Error Buffer

In case of non-intermittent faults, clear the error buffer before you begin the repair (**before** clearing the buffer, write down the content, as this history can give you significant information).

This to ensure that old error codes are no longer present.

If possible, check the entire contents of the error buffer. In some situations, an error code is only the result of another error code and not the actual cause (e.g., a fault in the protection detection circuitry can also lead to a protection).

There are several mechanisms of error detection:

- Via error bits in the status registers of ICs.
- Via polling on I/O pins going to the stand-by processor.
- Via sensing of analogue values on the stand-by processor or the PNX8535.
- Via a "not acknowledge" of an I<sup>2</sup>C communication.

Table 5-3 Error code overview

Error	Description	Error/Prot	Detected by	Device	Result
3	I <sup>2</sup> C3	E	MIPS	PNX8535	Error logged
5	PNX8535 does not boot (HW cause)	E	Stby P	PNX8535	Error blinking
6	5V, 12V supply	P	Stby P	/	Protection + Error blinking
8	1V2, 1V4, 2V5, 3V3 supply	P	Stby P	/	Protection + Error blinking
9	Supply fault	P	Stby P	/	Protection + Error blinking
11	I <sup>2</sup> C-MUX1	E	MIPS	PCA9540	Error logged
12	I <sup>2</sup> C-MUX2	E	MIPS	PCA9540	Error logged
22	PNX5050	E	MIPS	PNX5050	Error logged
23	HDMI mux	E	MIPS	AD8190/AD8191	Error logged
24	I <sup>2</sup> C switch	E	MIPS	PCA9540	Error logged
26	Master IF	E	MIPS	TDA9898/9897/9890	Error logged
28	MOP (Ambilight MOP on DFI panel) <sup>1)</sup>	E	MIPS	EP2CXXF484C7N	Error logged
34	Tuner	E	MIPS	TD1716	Error logged
37	Channel decoder	E	MIPS	TDA10060/ TDA10048	Error logged
46	Pacific3	E	MIPS	T6TF4	Error blinking + Error logged
53	PNX 8535 does not boot (SW cause)	E	Stby P	PNX8535	Error blinking
63	Power OK	E/P	MIPS	/	Error logged in case of a PDP set Protection in case of an LCD set
65	DFI (EPLD on DFI panel) <sup>1)</sup>	E	MIPS	/	Error blinking + Error logged

**Note**

1). Where applicable.

**Extra Info**

- **Rebooting.** When a TV is constantly rebooting due to internal problems, most of the time no errors will be logged or blinked. This rebooting can be recognized via a ComPair interface and Hyperterminal (for Hyperterminal settings, see paragraph "Stand-by software upgrade). You will see that the loggings which are generated by the main software keep continuing. In this case (rebooting) diagnose has to be done via ComPair.
- **Error 3 (I<sup>2</sup>C bus 3 blocked).** At the time of release of this manual, this error was not working as expected (error 3 is logged and can be read out). Current situation: when this error occurs, the TV will constantly reboot due to the blocked bus. The best way for further diagnosis here, is to use ComPair (e.g. read out the NVM content). Instead of error "3" it is possible you will see error "2" in the error buffer.
- **Error 5 (PNX8535 doesn't boot).** Indicates that the main processor was not able to read his bootscript. This error will point to a hardware problem around the PNX8535 (supplies not OK, PNX 8535 completely dead, I<sup>2</sup>C link between PNX and Stand-by Processor broken, etc...). When error 5 occurs it is also possible that I<sup>2</sup>C2 bus is blocked (NVM). I<sup>2</sup>C2 can be indicated in the schematics as follows : SCL-UP-MIPS, SDA-UP-MIPS, SCL-SLAVE, SDA-SLAVE, SCL-2 or SDA-2.
- **Error 11 (I<sup>2</sup>C MUX1).** Indicates a blocked (short-circuited) I<sup>2</sup>C-MUX1 bus. At the time of release of this manual, this error was not working as expected.
- **Error 12 (I<sup>2</sup>C MUX2).** Indicates a blocked (short-circuited) I<sup>2</sup>C-MUX2 bus. At the time of release of this manual, this error was not working as expected.
- **Error 24 (I<sup>2</sup>C switch).** As a side effect of error 24 it is possible that error 47 (no existing error) will also be logged.
- **Error 28 (DFI Ambilight MOP).** It can take up to 2 minutes or more before this error is logged. So if you suspect that this MOP is defective: clear the error buffer, restart the TV and wait for about 2 minutes before checking the error buffer.
- **Error 37 (Channel decoder).** When this error occurs, there probably will be no picture and sound from tuner input. As a side effect of error 37 it is possible that error 4 (no existing error) is also logged.

- **Error 46 (Pacific 3).** When there is an actual problem with or around the Pacific during start-up, you will have no picture and error 46 will be blinked via the blinking LED procedure. For further diagnosis you can always dump the CSM content on USB stick (see CSM) or use ComPair.
- **Error 53.** This error will indicate that the PNX8535 has read his bootscript (if this would have failed, error 5 would blink) but initialization was never completed because of hardware problems (NAND flash, ...) or software initialization problems. Possible cause could be that there is no valid software loaded (try to upgrade to the latest main software version). Note that it can take up to 2 minutes before the TV starts blinking error 53.
- **Error 63 (POWER OK).** When this error occurs, it means that the POWER-OK line did not become "high". This error is only applicable for TV's with an LCD display. For PDP displays there will be no protection during a POWER-OK line failure, but error 63 will be logged in the error buffer. **Caution:** in case a PDP TV ends up into power-ok protection, it can indicate that the display option code is set to "LCD". To change the display option code to "PDP" you need to activate SDM via the service pads (see figure "Service mode pads"). Then change the display option code blindly via a standard RC : key in the code "062598" directly followed by the "MENU" button and "XXX" (where XXX is the 3 digit decimal display option code as mentioned in figure "Display option code overview").
- **Error 65 (DFI EPLD error).** When this error occurs it means that there is a problem with the I<sup>2</sup>C communication towards the EPLD (picture processing EPLD, not the Ambilight EPLD) on the DFI panel.

## 5.6 The Blinking LED Procedure

### 5.6.1 Introduction

The blinking LED procedure can be split up into two situations:

- Blinking LED procedure in case of a protection. In this case the error is automatically blinked. This will be only one error, namely the one that is causing the protection. Therefore, you do not have to do anything special, just read out the blinks. A long blink indicates the decimal digit, a short blink indicates the units.
- Blinking LED procedure in the "on" state. Via this procedure, you can make the contents of the error buffer

visible via the front LED. This is especially useful for fault finding, when there is no picture.

When the blinking LED procedure is activated in the "on" state, the front LED will show (blink) the contents of the error-buffer. Error-codes > 10 are shown as follows:

1. "n" long blinks (where "n" = 1 - 9) indicating decimal digit,
2. A pause of 1.5 s,
3. "n" short blinks (where "n" = 1 - 9),
4. A pause of approx. 3 s,
5. When all the error-codes are displayed, the sequence finishes with a LED blink of 3 s,
6. The sequence starts again.

**Example:** Error 12 8 6 0 0.

After activation of the SDM, the front LED will show:

1. 1 long blink of 750 ms (which is an indication of the decimal digit) followed by a pause of 1.5 s,
2. 2 short blinks of 250 ms followed by a pause of 3 s,
3. 8 short blinks followed by a pause of 3 s,
4. 6 short blinks followed by a pause of 3 s,
5. 1 long blink of 3 s to finish the sequence,
6. The sequence starts again.

### 5.6.2 How to Activate

Use one of the following methods:

- **Activate the SDM or CSM.** The blinking front LED will show the entire contents of the error buffer (this works in "normal operation" mode).
- **Transmit the commands "MUTE" - "062500" - "OK" with a normal RC.** The complete error buffer is shown. Take notice that it takes some seconds before the blinking LED starts.
- **Transmit the commands "MUTE" - "06250x" - "OK" with a normal RC** (where "x" is a number between 1 and 5). When x= 1 the last detected error is shown, x= 2 the second last error, etc.... Take notice that it takes some seconds before the blinking LED starts.

## 5.7 Protections

### 5.7.1 Software Protections

Most of the protections and errors use either the stand-by microprocessor or the MIPS controller as detection device. Since in these cases, checking of observers, polling of ADCs, and filtering of input values are all heavily software based, these protections are referred to as software protections.

There are several types of software related protections, solving a variety of fault conditions:

- **Protections related to supplies:** check of the 12V, +5V, +1V2, +1V4, 2V5 and +3V3.
- **Protections related to breakdown of the safety check mechanism.** E.g. since the protection detections are done by means of software, failing of the software will have to initiate a protection mode since safety cannot be guaranteed any more.

#### **Remark on the Supply Errors**

The detection of a supply dip or supply loss during the normal playing of the set does not lead to a protection, but to a cold reboot of the set. If the supply is still missing after the reboot, the TV will go to protection.

#### **Protections during Start-up**

During TV start-up, some voltages and IC observers are actively monitored to be able to optimise the start-up speed, and to assure good operation of all components. If these monitors do not respond in a defined way, this indicates a malfunction of the system and leads to a protection. As the observers are only used during start-up, they are described in the start-up flow in detail (see paragraph "Stepwise Start-up").

### 5.7.2 Hardware Protections

The only real hardware protection in this chassis is (in case of an audio problem) the audio protection circuit that will switch "off" immediately the supply of the SSB. The supply will buzz during the protection and +12VS drops to approx. 5V5 and +5V Stand-by to approx. to 1V9. Other indication of the audio protection is that the red LED lights up with an intensity of 50%.

#### **Repair Tips**

- It is also possible that you have an audio DC protection because of an interruption in one or both speakers (the DC voltage that is still on the circuit cannot disappear through the speakers). **Caution:** (dis)connecting the speaker wires during the ON state of the TV at high volume can damage the audio amplifier.

## 5.8 Fault Finding and Repair Tips

Read also paragraph "Error Codes" -> "Error Buffer" -> "Extra Info".

### 5.8.1 Ambilight

Due to a degeneration process of the ambilights, it is recommended to change both ambilight units in case one unit needs to be repaired.

### 5.8.2 Audio Amplifier

It is recommended to replace all components at once (7D06, 7D09, 3D01, 3D25) when a defective FET 7D10 needs to be fixed. For the left channel (defective FET 7D35) of the audio amplifier replace all components like 7D31, 7D34, 3D11, 3D28.

### 5.8.3 CSM

When you activate CSM and there is a USB stick connected to the TV, the software will dump the complete CSM content to the USB stick. The file (Csm.txt) will be saved in the root of your USB stick. If this mechanism works you can conclude that a large part of the operating system is already working (MIPS, USB...)

### 5.8.4 DC/DC Converter

#### **Introduction**

- The best way to find a failure in the DC-DC converters is to check their starting-up sequence at "power-on via the mains cord", presuming that the standby microprocessor is operational.
- If the input voltage of DC-DC converters is around 12.7V (measured on decoupling capacitors 2U03/2U93/2U6S and 2U6R) and the enable signals are "low" (active) then the output voltages should have their normal values. +12V and +5V-POD supplies start-up first (enabled by POD-MODE signal from the standby microprocessor). There is a supplementary condition for +12V to start-up: if +5V-POD does not start up due to a local defect, then +12V will not be available as well. +5V-ON supply is enabled by the ON-MODE signal (coming also from the standby microprocessor) and is coming up a little bit later (20 ms) due to the slower rise time needed to charge the USB decoupling capacitor 2N31. +1V2 supply starts-up when +12V appears, then at least 100 ms later, +1V8, +2V5 and +3V3 will be activated via the ENABLE-3V3 signal from the standby microprocessor. If +12V value is less than 10 V then the last enumerated voltages will not show-up due to the under-voltage detection circuit 6U10 + 7U10 and surrounding components. Furthermore, if +12V is less than 8V then also +1V2 will not be available. The third DC-DC convertor that delivers +1V4 out of +12V is started up when



the ENABLE-1V2 becomes active (low) and +12V is present. The +Vtun generator (present only for the analogue version of China platforms) will generate +33V for the analogue tuner as soon as the 12V/3.3V DC-DC converter will start to operate.

- The consumption of controller IC 7U00 is around 19 mA (that means almost 200 mV drop voltage across resistor 3U01) and the consumption of controller IC 7U64 is around 12 mA.
- The current capability of DC-DC converters is quite high (short-circuit current is 7 to 10 A), therefore if there is a linear integrated stabiliser that, for example, delivers 1.8V from +3V3 with its output overloaded, the +3V3 stays usually at its normal value even though the consumption from +3V3 increases significantly.
- The +1V8 and +2V5 supply voltages are obtained via linear stabilizer made with discrete components that can deliver a lot of current, therefore in case +1V8 or +2V5 are short-circuited to GND then +3V3 will not have the normal value but much less.
- The SUPPLY-FAULT signal (active low) is an internal protection (error 9) of the DC-DC convertor and will occur if the output voltage of any DC-DC convertor is out of limits (10% of the normal value).

#### Fault Finding

- **Symptom:** +1V2 not present (even for a short while ~10 ms)
  1. Check 12 V availability (resistor 3U01, MOS-FETs 7U03 and 7U08), value of +12 V, +12 V switch (7U14 + 7U16 and surrounding components) and +5V-POD.
  2. Check the voltage on pin 9 (1.5 V),
  3. Check for +1V2 output voltage short-circuit to GND that can generate pulsed over-currents 7...10 A through coil 5U02.
  4. Check the over-current detection circuit (2U20 or 3U40 interrupted).
- **Symptom:** +1V4 not present (even for a short while ~10ms) while +12V is okay (also across input capacitors 2U6S and 2U6R).
  1. Check resistor 3U7B and power MOS-FETs 7U61-1/2
  2. Check the voltage on pin 4 (4 V)
  3. Check enable signal ENABLE-1V2 (active "low")
  4. Check for +1V4 output voltage short-circuit to GND that can generate pulsed over-currents 7...10 A through coil 5U60
  5. Check the over-current detection reference(2U65 + 3U7C) and the boot components (2U66 + 6U60).
- **Symptom:** +1V2 present for about 100ms, +1V8, +2V5 and +3V3 not rising.
  1. Check the ENABLE-3V3 signal (active "low"),
  2. Check the voltage on pin 8 (1.5 V),
  3. Check the under-voltage detection circuit (the voltage on collector of transistor 7U10-1 should be less than 0.8 V),
  4. Check for output voltages short-circuits to GND (+3V3, +2V5 and +1V8) that can generate pulsed over-currents 7...10 A through coil 5U01,
  5. Check the over-current detection circuit (2U18 or 3U31 interrupted).
- **Symptom:** +1V2 OK, +2V5 and +3V3 present for about 100 ms. **Possible cause:** SUPPLY-FAULT line stays "low" even though the +3V3 and +1V2 is available - the standby microprocessor is detecting that and switching "off" all supply voltages.
  1. Check the drop voltage across resistor 3U01 or 3U7B (they could be too high, meaning a defective controller IC or MOS-FETs),
  2. Check if the boost voltage on pin 4 of controller IC 7U00 is less than 14 V (should be 19 V),
  3. Check if +1V2 or +3V3 are higher than their normal values - that can be due to defective DC feedback of the respective DC-DC convertor (ex. 3U47, 3U77, 3U7L, 3U7J or 3U70).

- **Symptom:** +1V2, +1V4, +1V8, +2V5 or +3V3 shows a high level of ripple voltage (audible noise can come from the filtering coils 5U01, 5U02 or 5U60). **Possible cause:** instability of the frequency and/or duty cycle of a DC-DC converter or stabilizer.

1. Check the resistor 3U32 and 3U7D, capacitors 2U17 and 2U19, input and output decoupling capacitors.
2. Check a.c. feedback circuits (2U23+2U24+3U55+3U63 for +1V2, 2U6D+2U6E+2U6G+3U6A+3U7E for +1V4 and 2U07+2U08+3U17+3U24 for +3V3), compensation capacitors 2U25, 2U34, 2U36, 2U37, 2U40, 2U43, 2U68 and 2U6B.

- **Symptom:** +1V2, +1V4, +2V5 and +3V3 ok, no +Vtun (analogue sets only). **Possible cause:** the "+VTUN GENERATOR" circuit (7U24+7U26+surroundings components) is defective: check transistor 7U24 (it has to have gate voltage pulses of about 10 V amplitude and drain voltage pulses of about 35 V amplitude) and surroundings components. A high consumption (more than 6 mA) from +Vtun voltage can cause also +Vtun voltage to be too low or zero.

**Note:** when a pair of power MOSFETs(7U01+7U06, 7U03+7U08 or 7U61-1/2) becomes defective the controller IC 7U00 or 7U64 should be replaced as well.

#### 5.8.5 Exit "Factory Mode"

When an "F" is displayed in the screen's right corner, this means that the set is in "Factory" mode, and it normally happens after a new SSB has been mounted.

To exit this mode, push the "VOLUME minus" button on the TV's keyboard control for 5 seconds and restart the set.

#### 5.8.6 Sanken display supply

The 47" sets in this chassis come with a Sanken buy-in supply. The result of a failing Sanken display supply, e.g. one of the following voltages is missing : +400V, +12V or +24V, will lead to protection (blinking error 63). The set is switched off. When the primary circuit of the on-board platform supply fails, there is a high possibility that the main fuse of the Sanken display supply will break. In this case the Sanken display supply must not be replaced completely, just replace the Sanken main value fuse and repair the on-board platform supply. For safety reasons, make sure to use the correct fuse type.

#### 5.8.7 SSB service supply connector

Connector 1B40 can be used to apply external supply (+12V and +5V). In this way you do not need mains supply for troubleshooting on the SSB. e.g. component level repair.

#### 5.8.8 Tuner

For access to the components located under the tuner itself, you need to desolder the ground connections and bend the tuner up. To maintain a good performing tuner you must make a new ground connection to the SSB, for instance by use of a wire.

**Caution:** In case you replace the tuner, always check the tuner option!

#### 5.8.9 UI over PCI bus

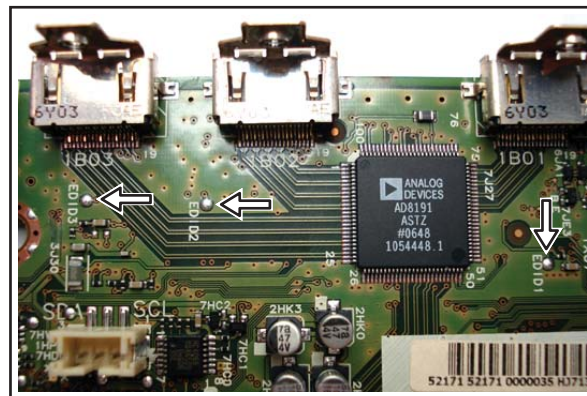
The UI is not integrated in the RGB signal but is sent from PNX8535 to PNX5050 via the PCI bus. TXT and MHEG are integrated in the RGB signal. So if you have TXT signal but no UI, check the PCI bus.

5.8.10 Display option code

**Caution:** In case you have replaced the SSB, always check the display option code in SAM, even if you have picture. With a wrong display option code it is possible that you have picture, but that in certain conditions you have unwanted side-effects.

5.8.11 Upgrade EDID NVM

To upgrade the EDID NVM you must short circuit pin 7 of the EDID NVM to ground. Therefore some test points (EDID1, EDID2 and EDID3) are foreseen (figure “EDID-NVM pins”). See ComPair for further instructions.



H\_16800\_122.eps  
090507

Figure 5-14 EDID-NVM pins

5.9 Software Upgrading

5.9.1 Introduction

The set software and security keys are stored in a NAND-Flash, which is connected to the PNX8535 via the PCI bus.

It is possible **for the user** to upgrade the **main** software via the USB port. This allows replacement of a software image in a stand alone set, without the need of an E-JTAG debugger. A description on how to upgrade the main software can be found in the DFU.

**Important:** When the NAND-Flash must be replaced, a new SSB must be ordered, due to the presence of the security keys!!! (copy protection keys, MAC address, ...).

Perform the following actions after SSB replacement:

1. Set the correct option codes (see sticker inside the TV).
2. Update the TV software (see the DFU for instructions).
3. Perform the alignments as described in chapter 8 (section “Reset of Repaired SSB”).
4. Check in CSM if the HDMI keys are valid.

For the correct order number of a new SSB, always refer to the Spare Parts list!

5.9.2 Main Software Upgrade

The software image resides in the NAND-Flash, and is formatted in the following way (refer to figure “NAND-flash content”):

Nand Flash content		ONE ZIP					
		FUS UPG		UpgradeAll UPG		FlashUtils UPG	
Partition	Content	erase	program	erase	program	erase	program
JFFS2 partition 1 (application read write data)	Channel table, EPG data, ...		X	X	X		
JFFS2 partition 0 (Application read only once data)	HDMI keys, back up display file, ...			X	X		
JFFS2 partition 0 (Application read only upgradable data)	wizard pictures, display file, cabinet file, upgrade assistant, ...		X	X	X		
SQUASHFS partition	Main software (Mips) Linux structure (root file system)	X	X	X	X		
BFFS partition 2 (DVD OK)	Default software upgrade application TriMedia software boot batch file 2	X	X	X	X		
BFFS partition 1 (DVD cursor down)	Back up software upgrade application boot batch file 1 Linux kernel JETT : needed for ComPair			X	X	X	X
BFFS partition 0	Jaguar Boot loader boot batch file 0					X	X
Block 0	µBTM partition table						X

H\_16770\_099.eps  
220307

Figure 5-15 NAND-Flash content

- The above overview of the NAND Flash shows the content of the different partitions. It also shows which part of the one-zip file erases and programs which part of the NAND Flash.
- Remark: the above does not mean that you can reprogram your HDMI keys with the "UpgradeAll.upg" file from the one zip file. This can only be done in a secure environment (e.g. the factory).
- The "UpgradeAll.upg" file is only used in the factory.
- The "FlashUtils.upg" file is only used by service centra which are allowed to do component level repair on the SSB.

#### Automatic Software Upgrade

In "normal" conditions, so when there is no major problem with the TV, the main software and the default software upgrade application can be upgraded with the "AUTORUN.UPG" (FUS part of the one-zip file: e.g. 3104 337 03801 \_FUS\_Q581E\_0.37.0.0\_commercial.zip). This can also be done by the consumers themselves, but they will have to get their software from the commercial Philips website or via the Software Update Assistant in the user menu (see DFU). The "autorun.upg" file must be placed in the root of your USB stick.

How to upgrade:

1. Copy "AUTORUN.UPG" to the root of your USB stick.
2. Insert USB stick in the side I/O while the set is in ON MODE. The set will restart and the upgrading will start automatically. As soon as the programming is finished, you will get the message that you can remove your USB stick and restart the set.

#### Manual Software Upgrade

In case that the software upgrade application does not start automatically, you can also start it manually.

How to start the software upgrade application manually:

1. Disconnect the TV from the Mains/AC Power.
2. Press the "OK" button on a Philips DVD RC-6 remote control (it is also possible to use the TV remote in "DVD" mode). Keep the "OK" button pressed while reconnecting the TV to the Mains/AC Power.
3. The software upgrade application will start.

#### Attention!

In case that you have started the download application **manually**, the "autorun.upg" will maybe not be recognized. What to do in this case:

1. Create a directory "UPGRADES" on your USB stick.
2. Rename the "autorun.upg" to something else, e.g. to "software.upg". Do not use long or complicated names, keep it simple. Make sure that "AUTORUN.UPG" is no longer present in the root of your USB stick.
3. Copy the renamed "upg" file into this directory.
4. Insert USB stick in the side I/O.
5. The renamed "upg" file will be visible and selectable in the upgrade application.

#### Back-up Software Upgrade Application

If the default software upgrade application does not start (could be due to a corrupted boot 2 sector) via the above described method, you can try to activate the "back-up software upgrade application".

How to start the "back-up software upgrade application" manually:

1. Disconnect the TV from the Mains/AC Power.
2. Press the "CURSOR DOWN" button on a Philips DVD RC-6 remote control (it is also possible to use the TV remote in "DVD" mode). Keep the "cursor down" button pressed while reconnecting the TV to the Mains/AC Power.
3. The software upgrade application will start.

### 5.9.3 Stand-by Software Upgrade

There are two methods now to upgrade stand-by software:

#### Upgrade via USB

In this chassis it is possible to upgrade the Stand-by software via a USB stick. The method is similar to upgrading the main software via USB.

Use the following steps:

1. Create a directory "UPGRADES" on your USB stick.
2. Copy the Stand-by software (part of the one-zip file, e.g. StandbySW\_CFT01\_9.0.0.0.upg) into this directory.
3. Insert the USB stick into the TV.
4. Start the download application manually (see paragraph "Manual start of the Software Upgrade Application").
5. Select the appropriate file and press the "red" button to upgrade.

#### Upgrade via PC and ComPair interface

It will be possible to upgrade the Stand-by software via a PC and the ComPair interface. Check paragraph "ComPair" on how to connect the interface. To upgrade the Stand-by software, use the following steps:

1. Disconnect the TV from the Mains/AC Power.
  2. Short circuit the SPI pins [2] on the SSB (see figure "Service mode pads" earlier in this chapter).
  3. Keep the SPI pins shorted while connecting the TV to the Mains/AC Power.
  4. Release the short circuit after approx. two seconds.
  5. Start up HyperTerminal (can be found in every Windows application via Programs -> Accessories -> Communications -> HyperTerminal). Use the following settings:
    - COM1
    - Bits per second = 38400 (9600)\*
    - Data bits = 8
    - Parity = none
    - Stop bits = 1
    - Flow control = None
  6. Press "Shift U" on your PC keyboard. You should now see the following info:
    - PNX2015 Loader V1.0
    - 19-09-2003
    - DEVID=0x05
    - Erasing
    - MCSUM=0x0000
    - =
  7. If you do not see the above info, restart the above procedure, and check your HyperTerminal settings and the connections between PC and TV.
  8. Via "Transfer" -> "Send text file ...", you can send the proper upgrade file to the TV (e.g. \*.hex).
  9. After successful programming, you must see the following info (this can take several minutes!):
    - DCSUM=0xC67E
    - :Ok
    - MCSUM=0xC67E
    - Programming
    - PCSUM=0xC67E
    - Finished
  10. If you do not see this info, restart the complete procedure.
  11. Close HyperTerminal.
  12. Disconnect and connect Mains/AC Power again.
- (\*) When having problems with upgrading, use the values between brackets.

### 5.9.4 Content and Usage of the One-Zip Software File

Below you find a content explanation of the One-Zip file, and instructions on how and when to use it.

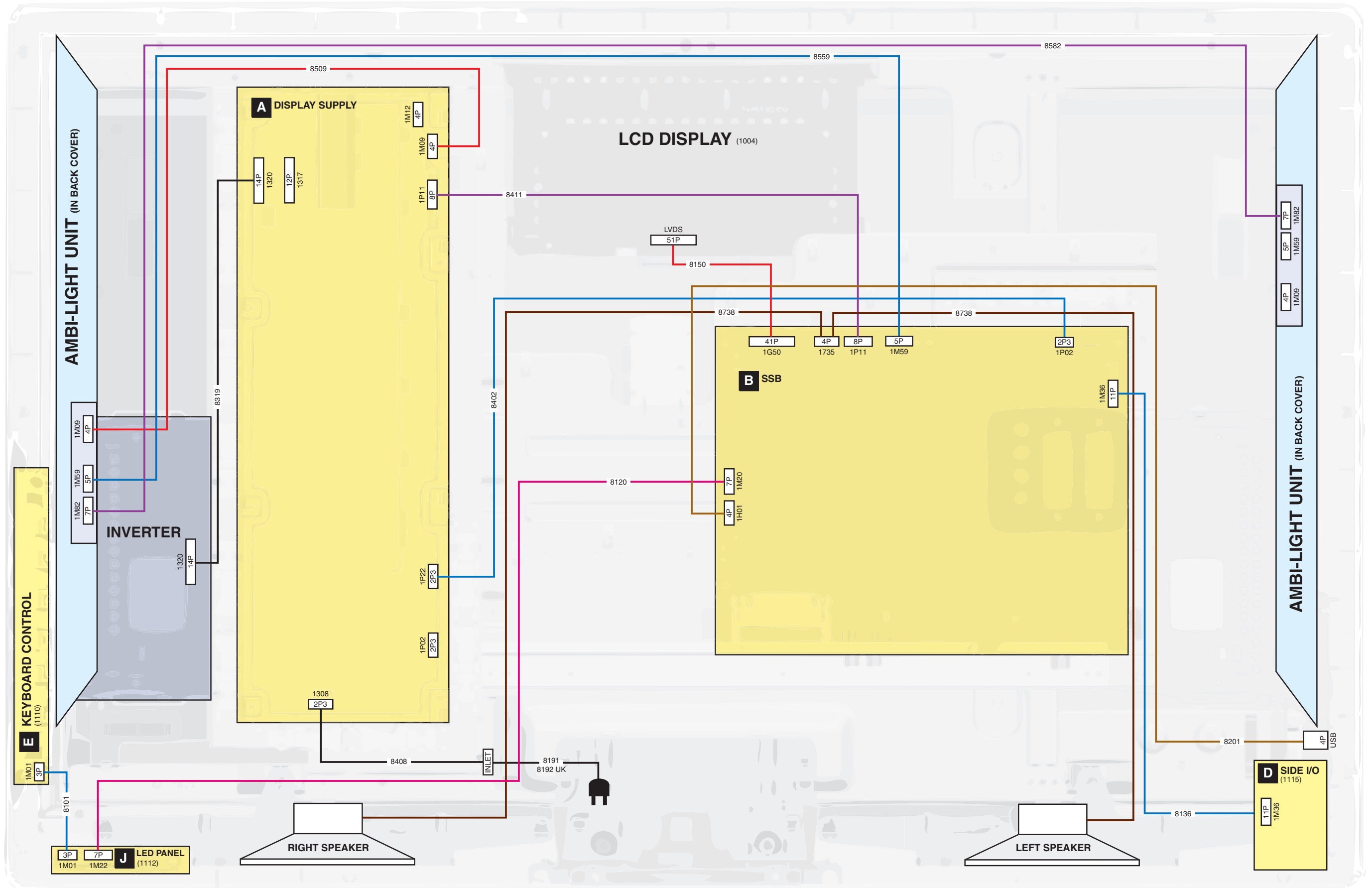
- **1.1 Ambilight\_PR FAM\_x.x.x.x.zip**. Not to be used by Service technicians.
- **1.2 Cabinet\_ACOUS\_x.x.x.x.zip**. Not to be used by Service technicians.
- **1.3 Ceisp2padII\_P2PAD\_x.x.x.x.zip**. Not to be used by Service technicians. For ComPair development only.

- **1.4 Display\_DISPT\_x.x.x.x.zip.** Not to be used by Service technicians.
  - **1.5 EDID\_Q581X\_x.x.x.x.zip.** Contains the EDID content of the different EDID NVM's. See ComPair for further instructions.  
For sets with three HDMI connectors.
    - For **HDMI 1** NVM, use "port 1\*.bin
    - For **HDMI 2** NVM, use "port 2\*.bin
    - For **HDMI 3** NVM, use "port 3\*.bin
  - **1.6 EJTAGDownload\_Q581X\_x.x.x.x.zip.** Only used by service centra which are allowed to do component level repair.
  - **1.7 Factory\_Q581X\_x.x.x.x\_commercial.zip.** Only for production purposes, not to be used by Service technicians.
  - **1.8 FlashUtils\_Q581X\_x.x.x.x\_commercial.zip.** Not to be used by Service technicians.
  - **1.9 FUS\_Q581X\_x.x.x.x\_commercial.zip.** Contains the "autorun.upg" which is needed to upgrade the TV main software and the software download application.
  - **2.0 MOP\_IACXX\_x.x.x.x.zip.** Not to be used by Service technicians. A programmed MOP device can be ordered via your regional Service organization.
  - **2.1 OpenSourceFile\_Q581X\_x.x.x.x.zip.** Not to be used by Service technicians.
  - **2.2 Pacific3\_P3FW0\_x.x.x.x.zip.** Not to be used by Service technicians. A programmed PACIFIC device can be ordered via your regional Service organization.
  - **2.3 PQPrivate\_U5207\_x.x.x.x.zip.** Not to be used by Service technicians.
  - **2.4 PQPublic\_U5207\_x.x.x.x.zip.** Not to be used by Service technicians.
  - **2.5 ProcessNVM\_Q531X\_x.x.x.x.zip.** Default NVM content. Must be programmed via ComPair.
  - **2.6 StandbySW\_CFTxx\_x.x.x.x\_commercial.zip.** Contains the Stand-by software in "upg" and "hex" format.
    - The "StandbySW\_xxxxx\_prod.upg" file can be used to upgrade the Stand-by software via USB.
    - The "StandbySW\_xxxxx.hex" file can be used to upgrade the Stand-by software via ComPair.
    - The files "StandbySW\_xxxxx\_exhex.hex" and "StandbySW\_xxxxx\_dev.upg" may not be used by Service technicians (only for development purposes).
  - **2.7 UpgradeAll\_Q531X\_x.x.x.x\_commercial.zip.** Only for production purposes, not to be used by Service technicians.
- Caution : Never try to use this file, because it will overwrite the HDCP keys !!!**

### 6. Block Diagrams, Test Point Overview, and Waveforms

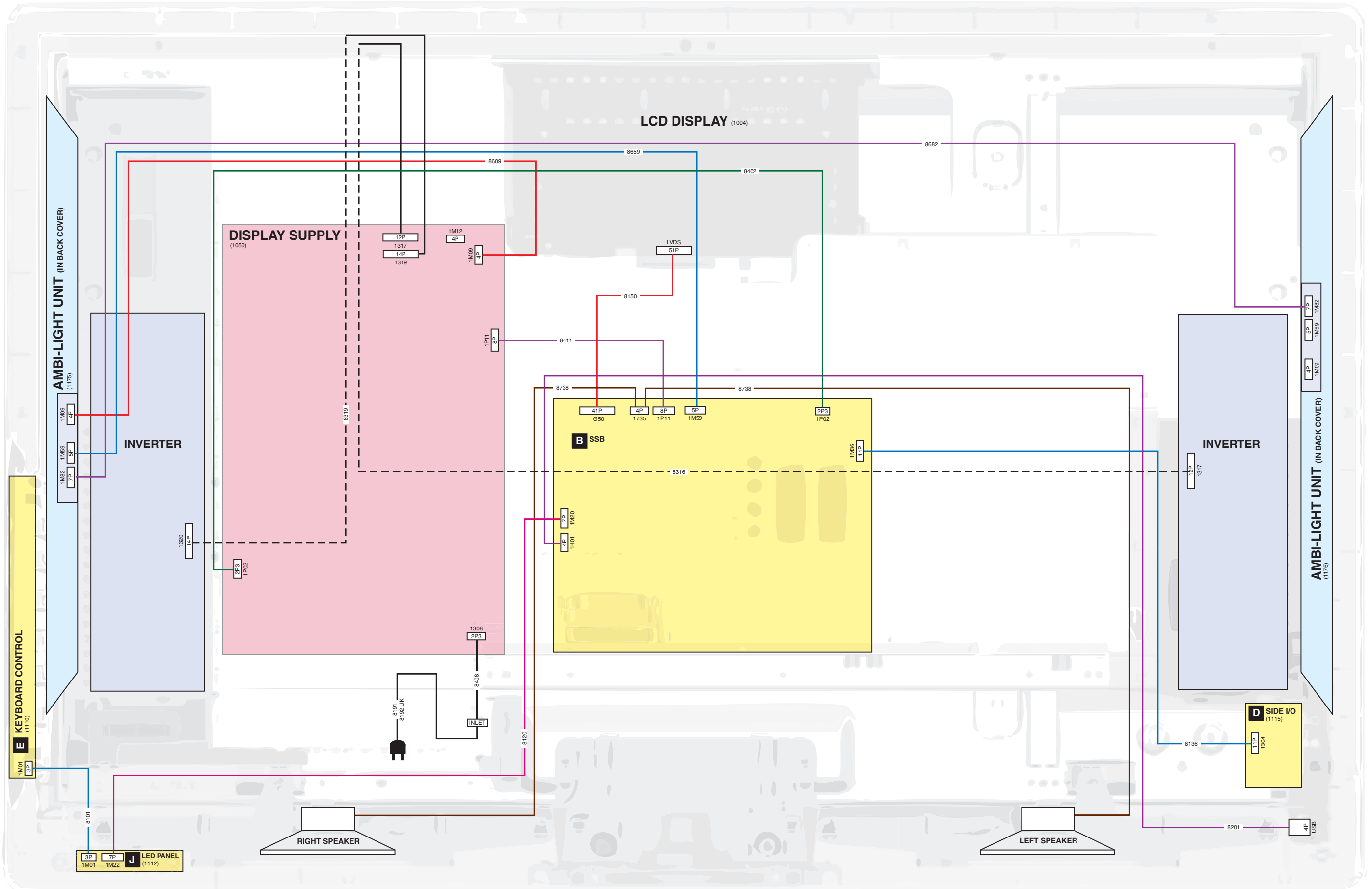
#### Wiring Diagram 32''

WIRING 32'' Styling MS7



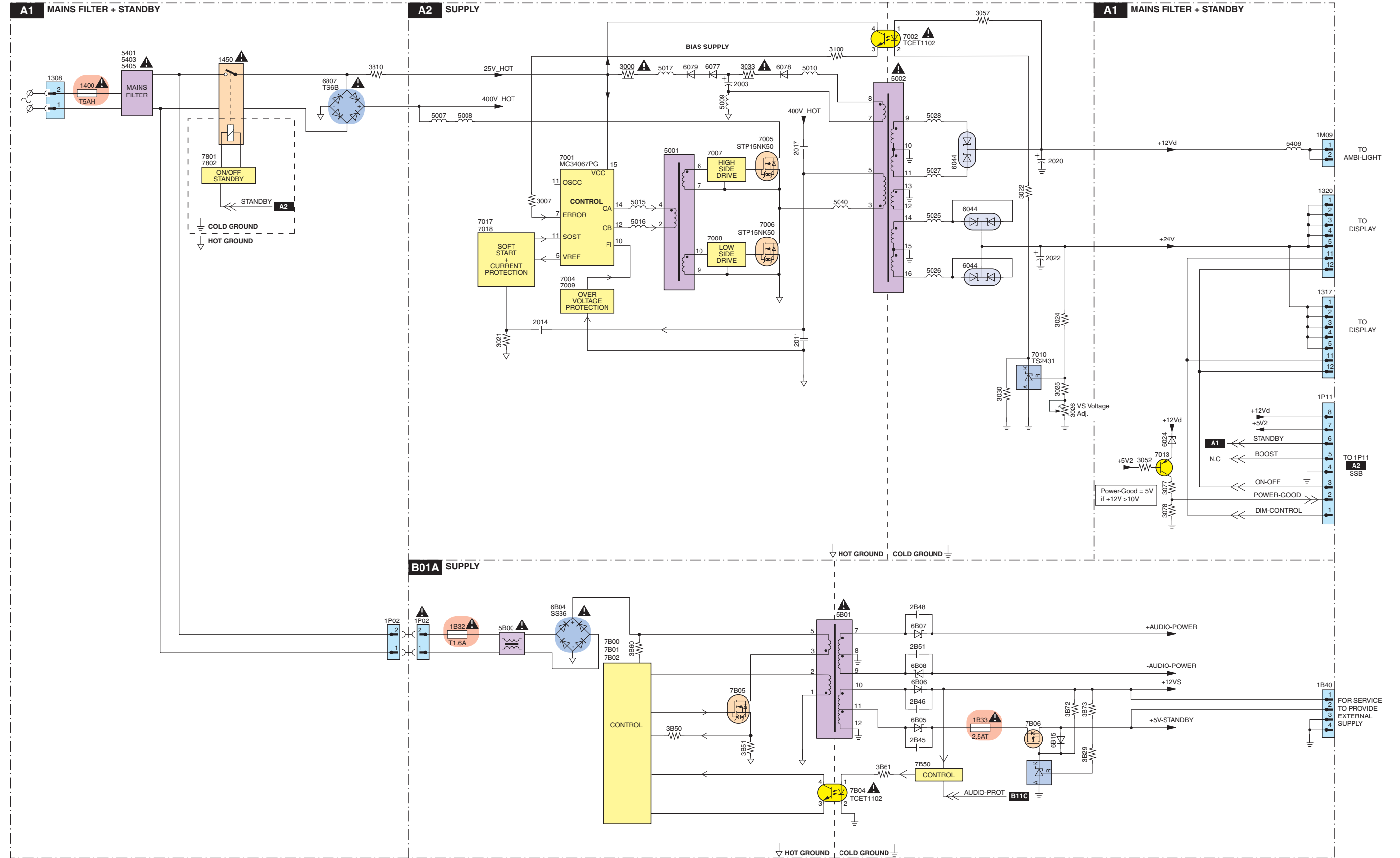
### Wiring Diagram 47"

WIRING 47" Styling MS7



### Block Diagram Display & Platform Supply 32"

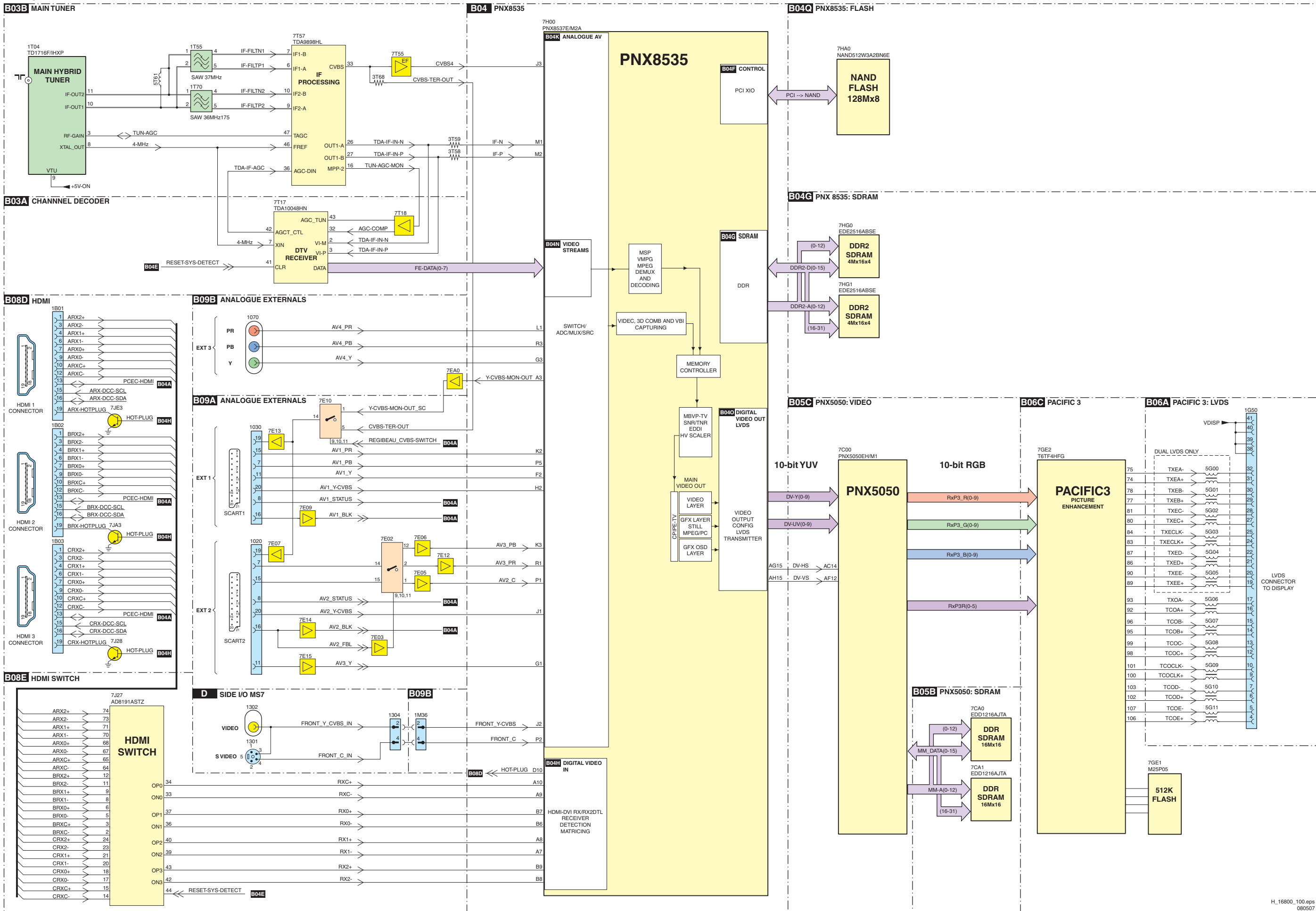
#### DISPLAY SUPPLY + PLATFORM SUPPLY 32"





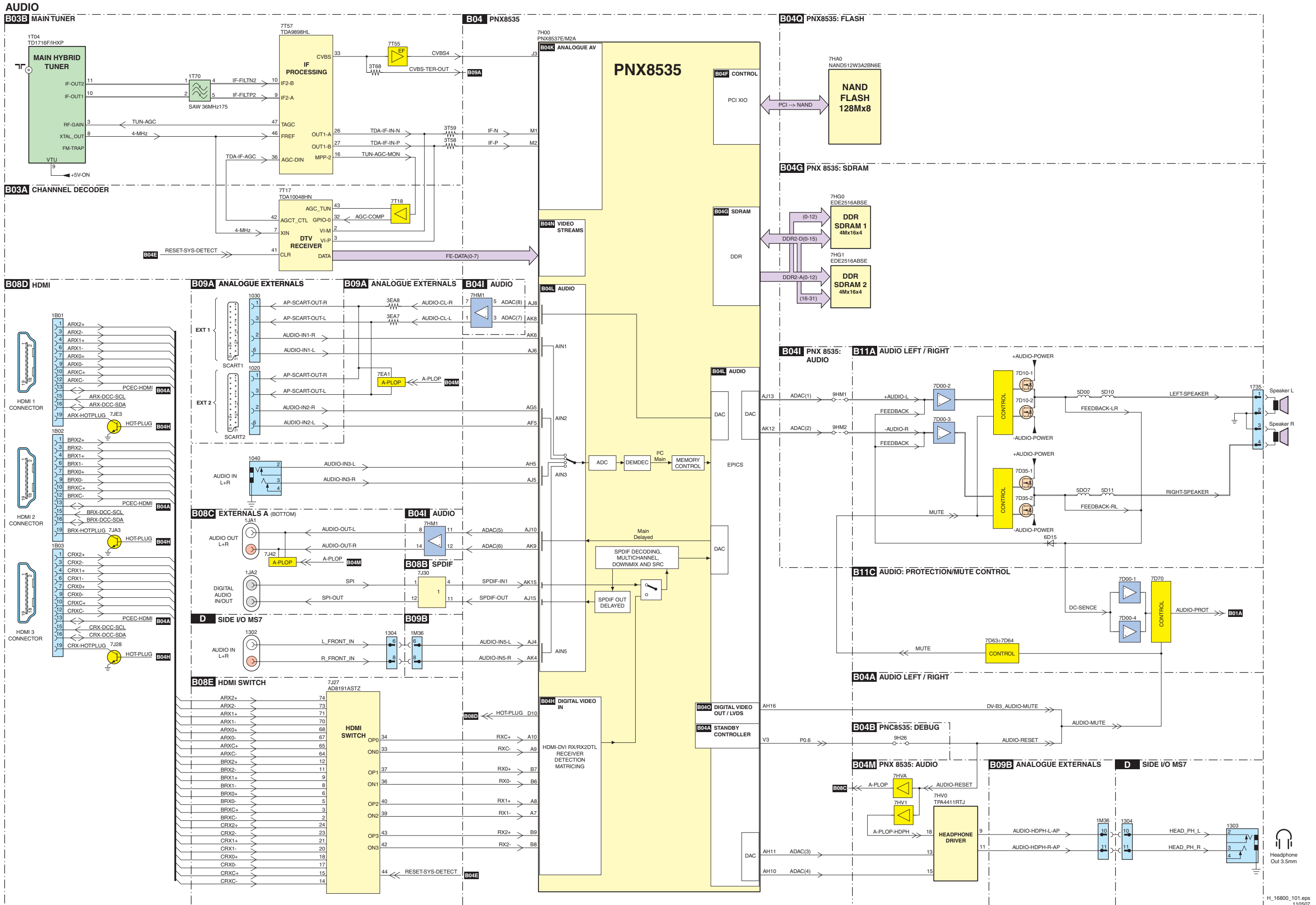
### Block Diagram Video

#### VIDEO



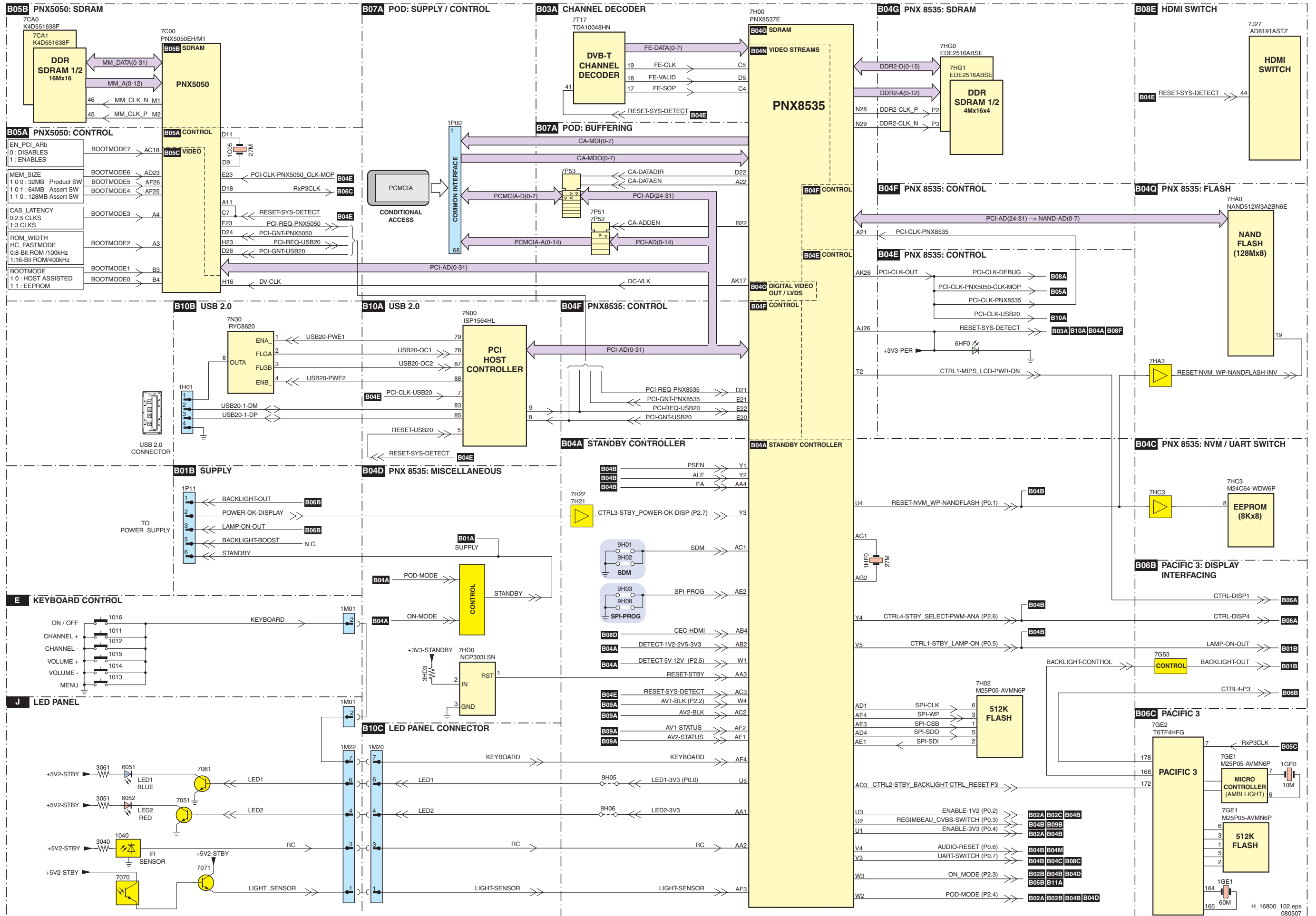


### Block Diagram Audio

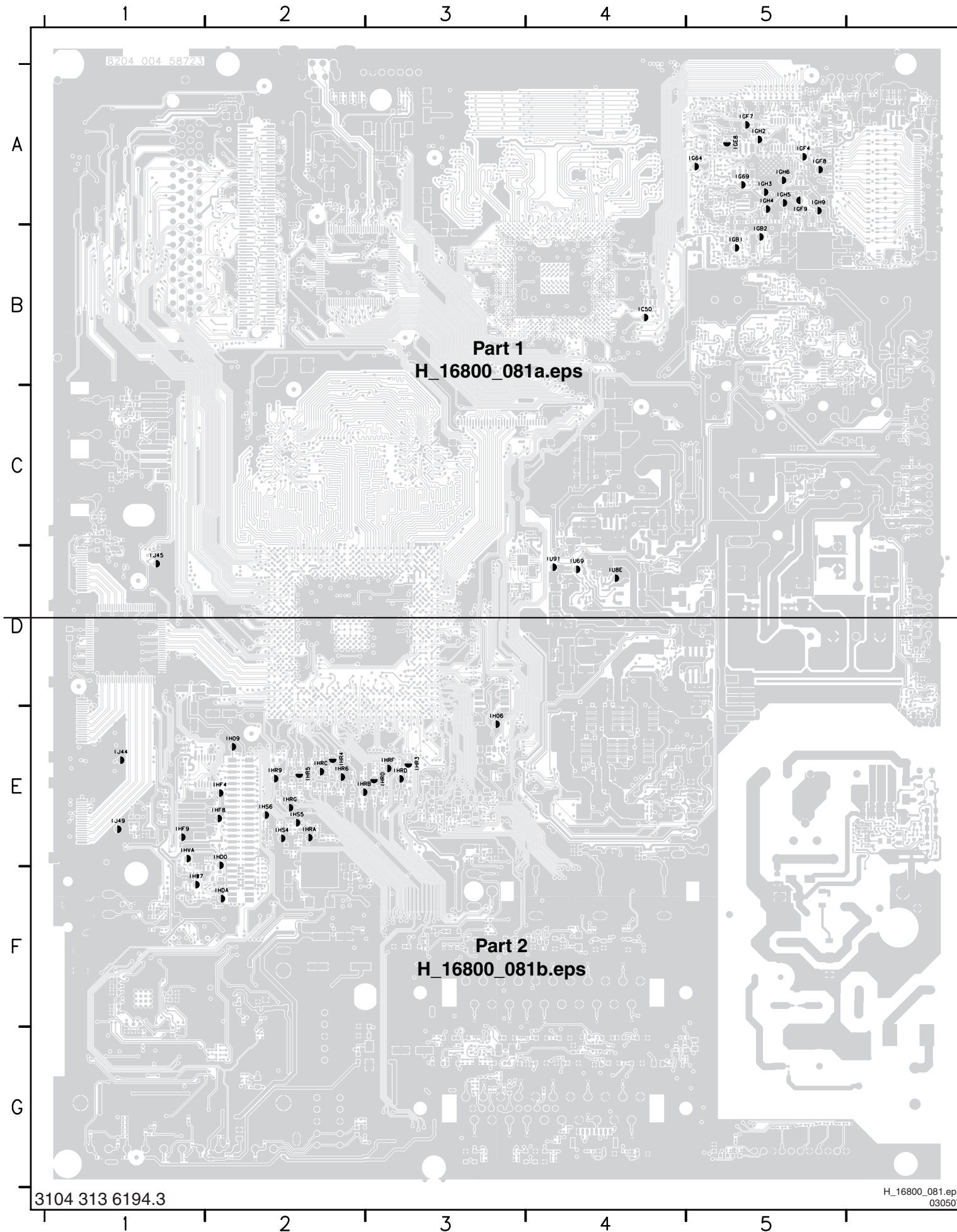


# Block Diagram Control & Clock Signals

## CONTROL + CLOCK SIGNALS



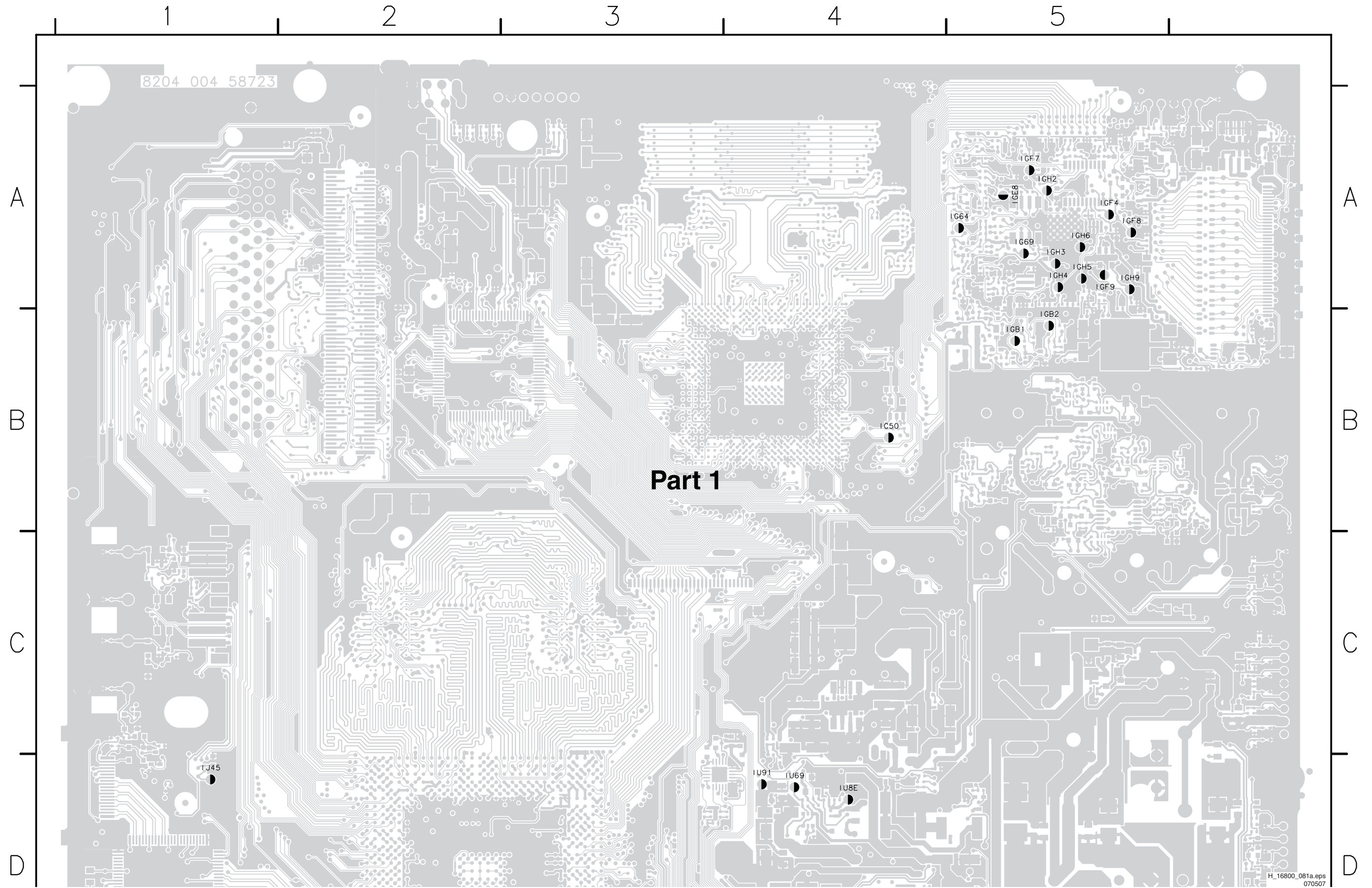
SSB: Test Points (Overview Top Side)



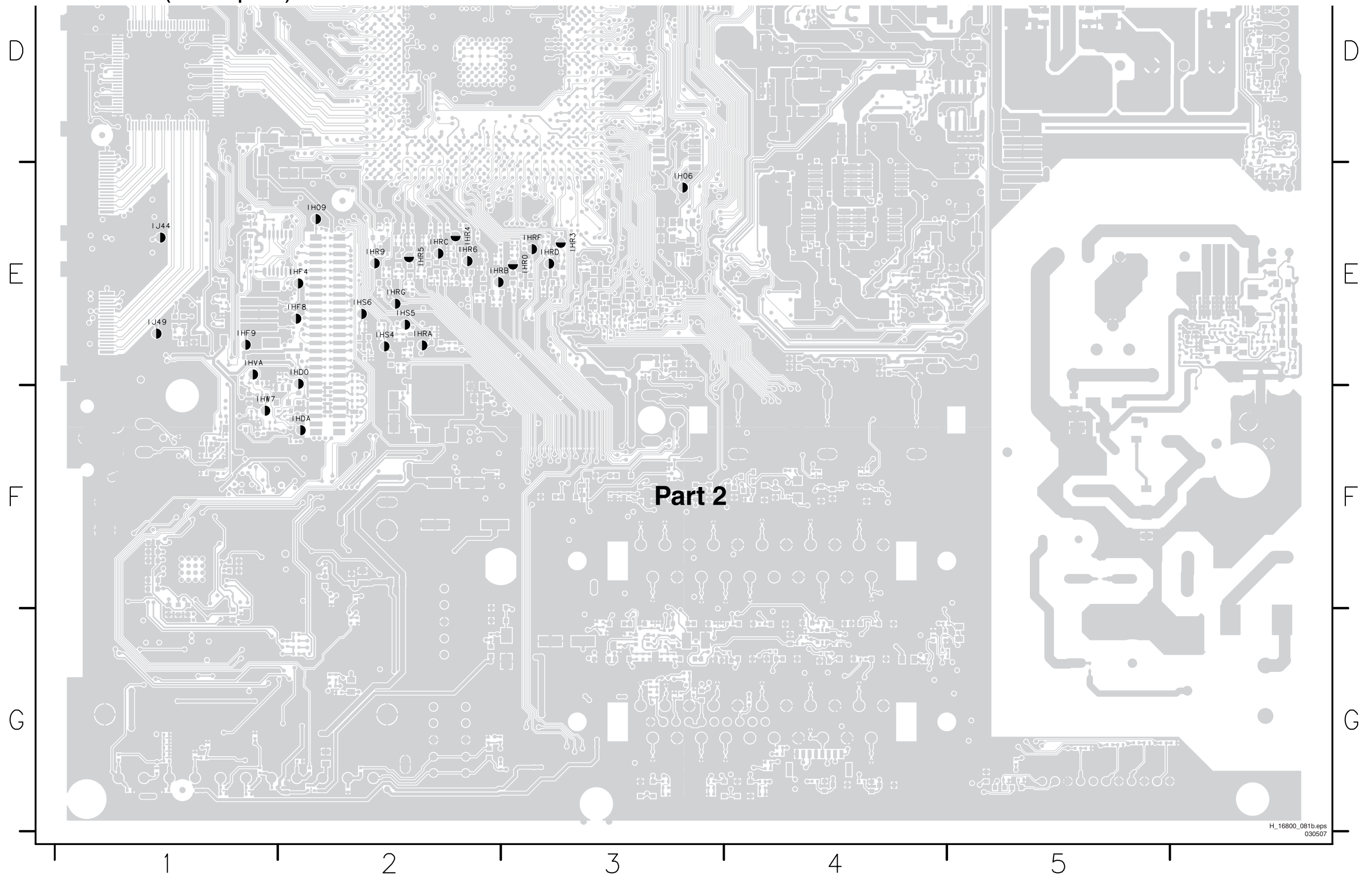
- I60 A5
- I65 A5
- I66 A5
- I67 A5
- I68 A5
- I69 A5
- I133 D4
- I148 D3
- I238 G2
- I240 G2
- I737 D4
- I738 E2
- I739 E3
- I740 E2
- I741 E2
- I742 E3
- I743 E1
- I744 F2
- I745 E2
- I746 A5
- I747 A5
- I748 E2
- I749 E2
- I750 E2
- IC50 B4
- IC50 B4
- IG64 A5
- IG64 A5
- IG69 A5
- IG69 A5
- IG69 A5
- IGB1 B5
- IGB1 B5
- IGB2 B5
- IGB2 B5
- IGB2 B5
- IGB8 A5
- IGB8 A5
- IGF4 A5
- IGF7 A5
- IGF7 A5
- IGF8 A5
- IGF9 A5
- IGH2 A5
- IGH3 A5
- IGH4 A5
- IGH5 A5
- IGH6 A5
- IGH6 A5
- IGH9 A5
- IH06 E3
- IH06 E3
- IH09 E2
- IH09 E2
- IH09 E2
- IH09 E2
- IHDA F2
- IHF4 E2
- IHF4 E2
- IHF8 E2
- IHF8 E2
- IHF9 E1
- IHR0 E3
- IHR3 E3
- IHR3 E3
- IHR4 E2
- IHR4 E2
- IHR5 E2
- IHR5 E2
- IHR6 E2
- IHR9 E2
- IHR9 E2
- IHR9 E2
- IHRA E2
- IHRB E2
- IHRC E2
- IHRD E3
- IHRF E3
- IHRF E3
- IHRG E2
- IHS4 E2
- IHS4 E2
- IHS5 E2
- IHS6 E2
- IHS6 E2
- IHVA E1
- IHVA E1
- IHW7 F1
- IHW7 F1
- IJ44 E1
- IJ44 E1
- IJ45 D1
- IJ45 D1
- IJ49 E1
- IJ49 E1
- IU69 D4
- IU8E D4
- IU91 D4
- IU91 D4



SSB: Test Points (Part 1 Top Side)



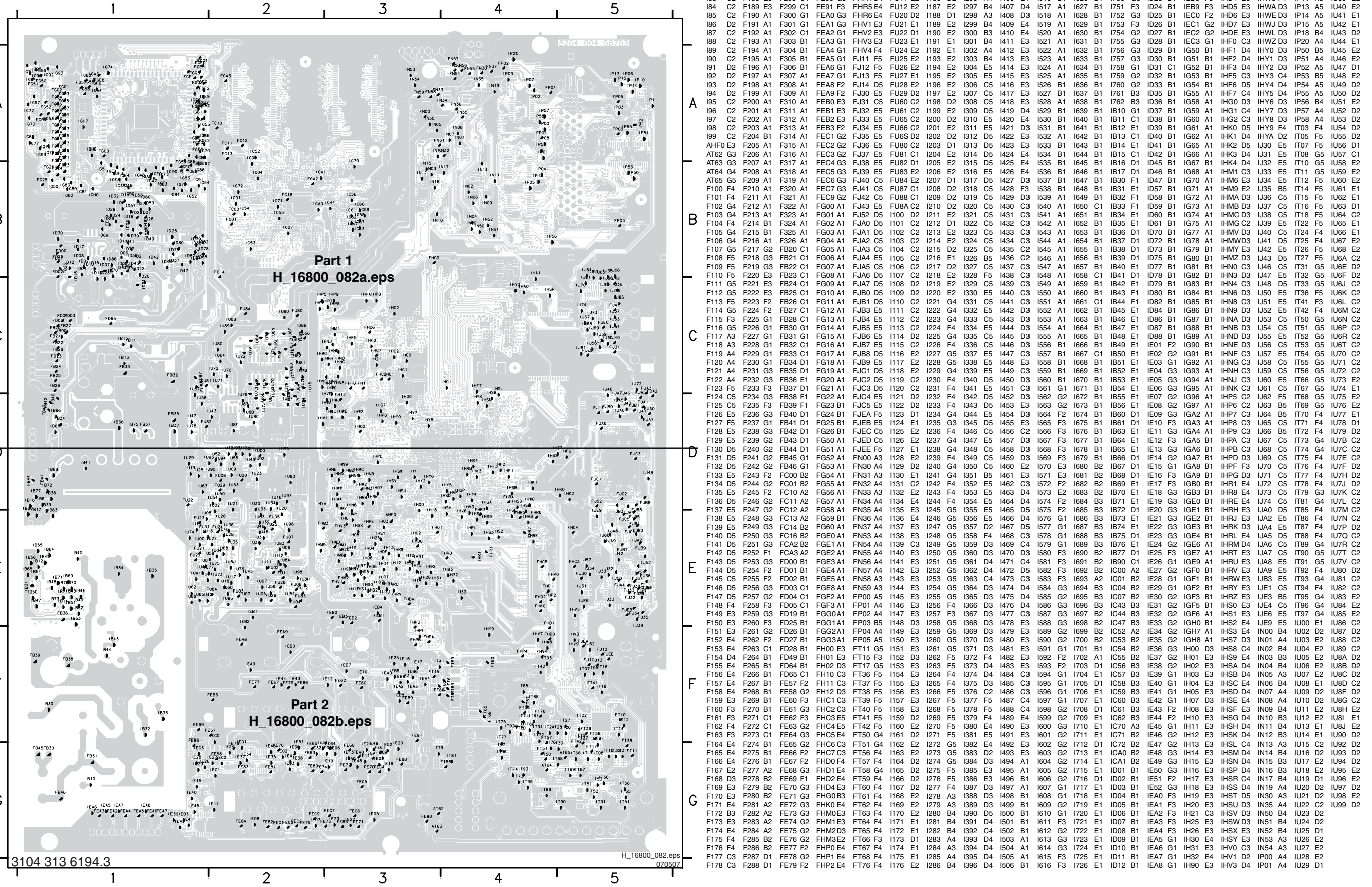
SSB: Test Points (Part 2 Top Side)





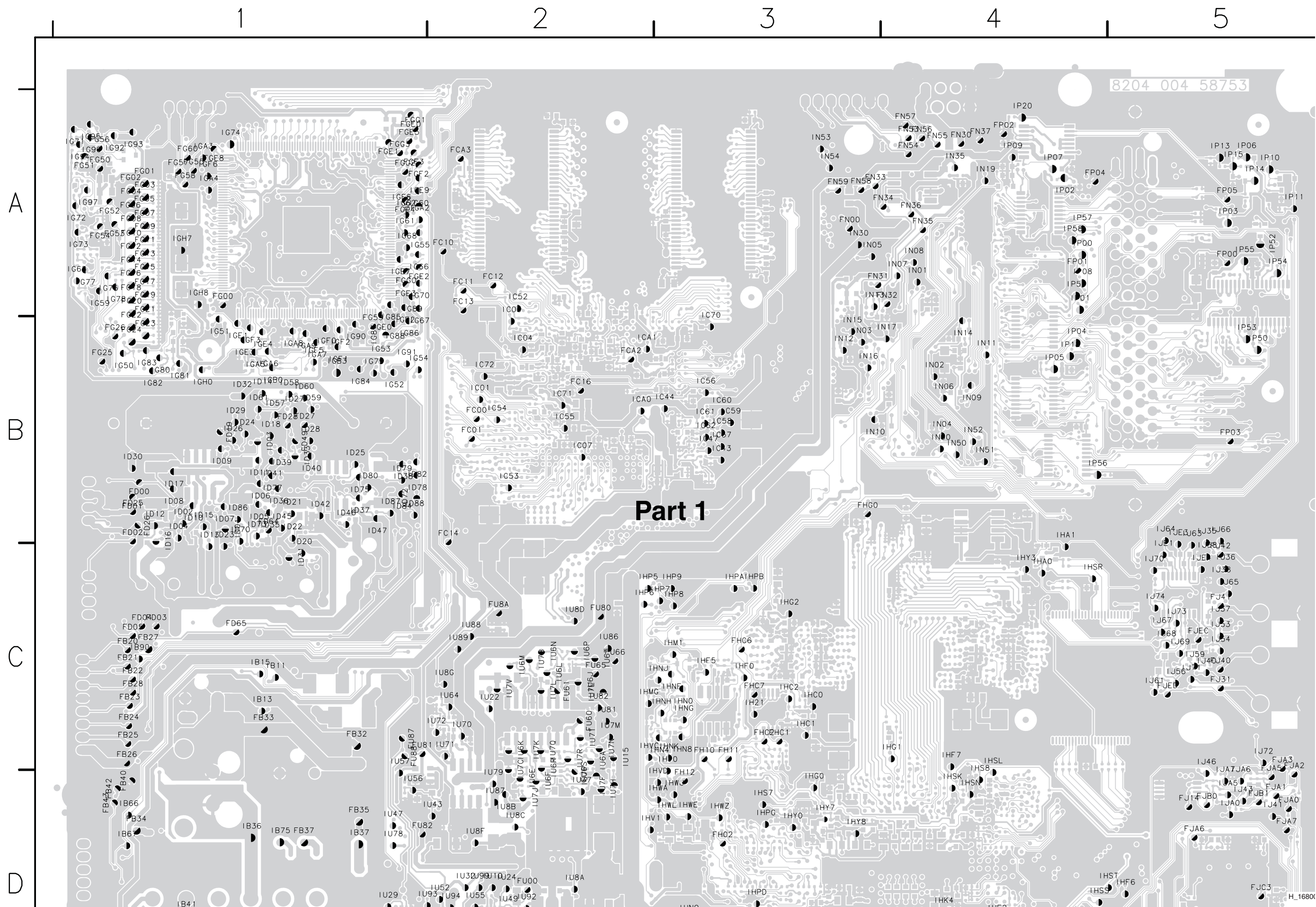
### SSB: Test Points (Overview Bottom Side)

A1	E3	F5	A4	I5	B4	F14	A4	F23	C1	F32	E5	F41	E5	F50	G2	F59	C1	F68	C2	F77	D1	F86	E2	F95	F4	I14	A5	I23	A5	I32	D1	I41	D1	I50	C1	I59	E2	I74	C2	F179	E4	F289	D1	FE80	G2	FHP3	E3	FU00	D2	I177	E2	I287	B3	I397	D4	I507	B1	I617	G3	I727	E1	ID13	B1	IEA9	F2	IHH1	E3	IHV5	E5	IP02	A4	IU30	D2	
A2	G5	F7	A4	I7	A5	F16	A4	F24	C1	F33	C5	F42	E5	F51	G2	F60	E2	F69	C2	F78	E1	F87	E2	F96	F4	I15	A5	I24	A4	I33	D1	I42	C1	I51	E1	I61	E1	I76	D2	F181	E5	F291	G1	FE82	G2	FHP4	F3	FU02	E2	I179	D1	I289	B4	I399	D4	I509	B1	I619	G2	I728	E1	ID14	B1	IEB0	F2	IHH2	E3	IHV6	E4	IP03	A4	IU31	E2	
A3	G5	F7	A4	I7	A5	F16	A4	F24	C1	F33	C5	F42	E5	F52	F2	F61	E2	F70	D2	F79	D2	F88	E2	F97	F4	I16	A4	I25	B5	I34	D1	I43	D1	I52	C1	I62	E1	I77	D2	F182	C3	F292	F1	FE83	G2	FHP5	F3	FU03	E2	I180	D1	I290	A3	I400	E1	I510	B1	I620	B1	I730	G3	ID16	B1	IEB2	E2	IHA0	C4	IHV7	F5	IP05	B4	IU32	D2	
A4	G4	F8	A4	I8	A4	F17	D1	F26	C1	F35	F5	F44	C5	F53	G2	F62	D1	F71	E2	F80	E2	F89	E1	F98	F3	I17	A4	I26	A4	I35	E1	I44	D1	I53	G1	I63	E1	I78	D2	F183	C3	F293	F1	FE84	G2	FHP6	F3	FU04	E2	I181	D2	I291	B3	I401	E1	I511	B1	I621	B1	I731	G3	ID17	B1	IEB3	E3	IHA1	B4	IHV8	F5	IP06	A5	IU33	D2	
A5	G3	F9	A4	I9	B5	F18	D1	F27	E5	F36	C5	F45	E5	F54	G2	F63	C1	F72	E1	F81	E2	F90	D2	F99	F4	I18	A5	I27	E1	I36	F1	I45	D1	I54	B5	I64	E1	I79	E1	F184	D3	F294	D1	FE85	G3	FHR0	E4	FU05	E2	I182	D2	I292	B4	I402	D4	I512	B1	I622	B1	I732	G3	ID18	B1	IEB4	E3	IHC0	C3	IHV9	C2	IP07	A4	IU34	E2	
F1	A5	I1	A5	F10	A4	F19	C1	F28	D5	F37	E5	F46	F5	F55	G2	F64	C2	F73	E2	F82	D2	F91	F4	I19	A4	I19	B4	I28	F1	I37	E1	I46	E1	I55	C5	I70	E1	D2	I80	E2	F185	C3	F295	E1	FE86	F1	FHR1	E4	FU06	E2	I183	D2	I293	B3	I403	E4	I513	B1	I623	B1	I733	G3	ID20	B1	IEB5	F3	IHC1	C3	IHV10	D3	IP08	A4	IU35	E2
F2	A4	I2	A4	F11	A4	F20	C1	F29	D5	F38	E5	F47	G3	F56	E5	F65	D2	F74	E2	F83	E2	F92	F4	I11	B4	I20	B4	I29	F1	I38	E1	I47	E1	I56	C5	I71	D2	I81	C2	F186	C3	F296	D1	FE87	F3	FHR2	E4	FU07	E2	I184	D2	I294	B4	I404	E4	I514	B1	I624	B1	I734	G1	ID21	B1	IEB6	F3	IHC2	C3	IHV11	D4	IP09	A4	IU37	D2	
F3	B5	I3	A4	F12	A3	F21	C1	F30	D5	F39	E5	F48	G3	F57	C2	F66	C2	F75	E2	F84	E2	F93	F4	I12	A5	I21	A5	I30	E1	I39	F1	I48	C1	I57	E1	I72	C2	I82	C2	F187	D3	F297	D1	FE88	F3	FHR3	D4	FU08	E2	I185	E2	I295	A4	I405	D4	I515	B1	I625	B1	I735	G2	ID22	B1	IEB7	F3	IHD2	E3	IHW0	D4	IP10	A5	IU38	E2	
F4	A4	I4	A4	F13	A3	F22	C1	F31	C5	F40	E5	F49	G2	F58	C1	F67	C2	F76	E1	F85	E2	F94	F3	I13	A5	I22	A4	I31	E1	I40	E1	I49	E1	I58	E2	I73	C2	I83	C2	F188	E3	F298	C1	FE89	G3	FHR4	D4	FU11	E2	I186	D2	I296	A4	I406	E4	I516	B1	I626	B1	I736	G3	ID23	B1	IEB8	F3	IHD3	E3	IHW1	D3	IP11	A5	IU39	E2	





SSB: Test Points (Part 1 Bottom Side)



1

2

3

4

5

A

B

C

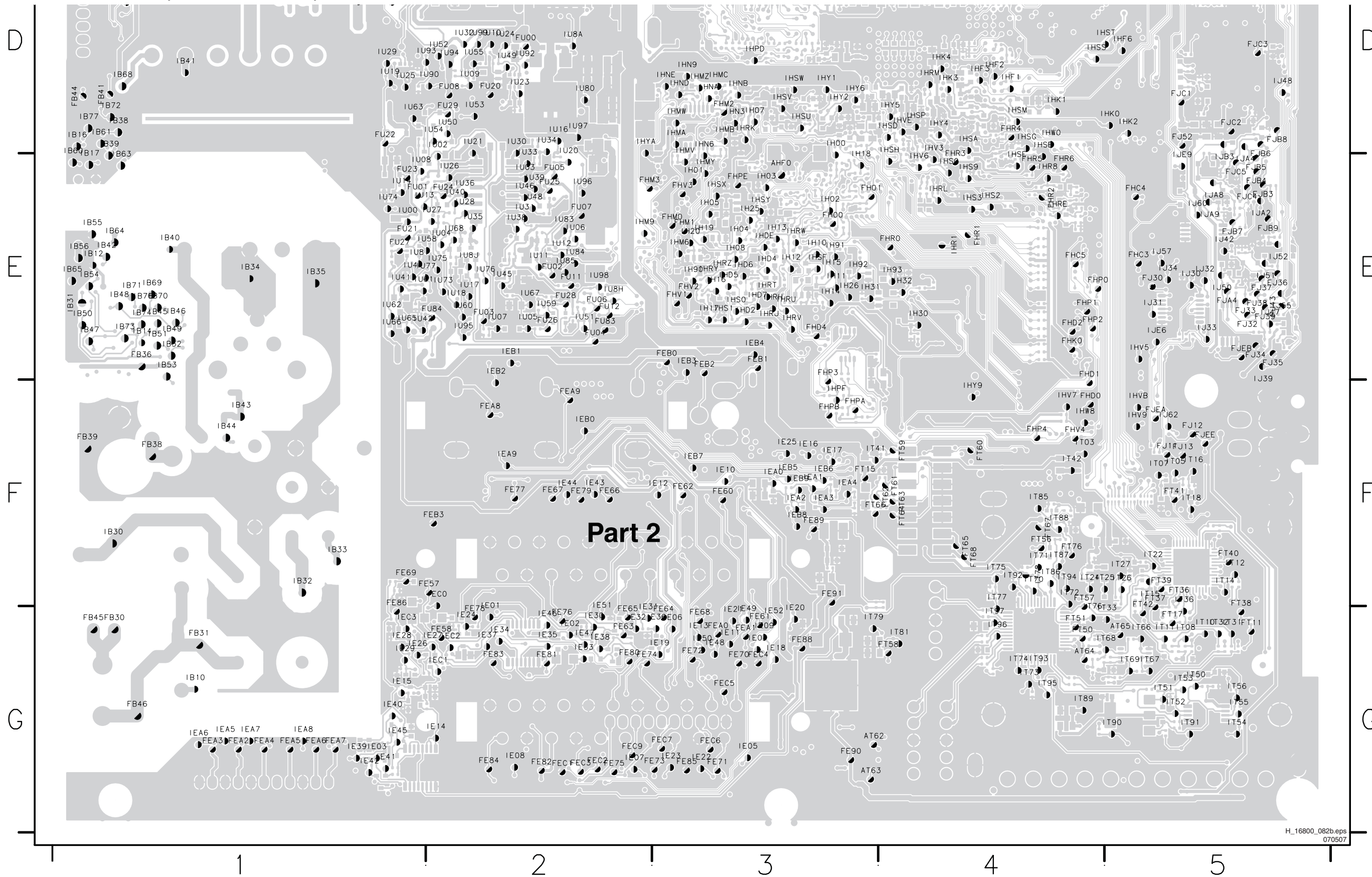
D

Part 1

8204 004 58753

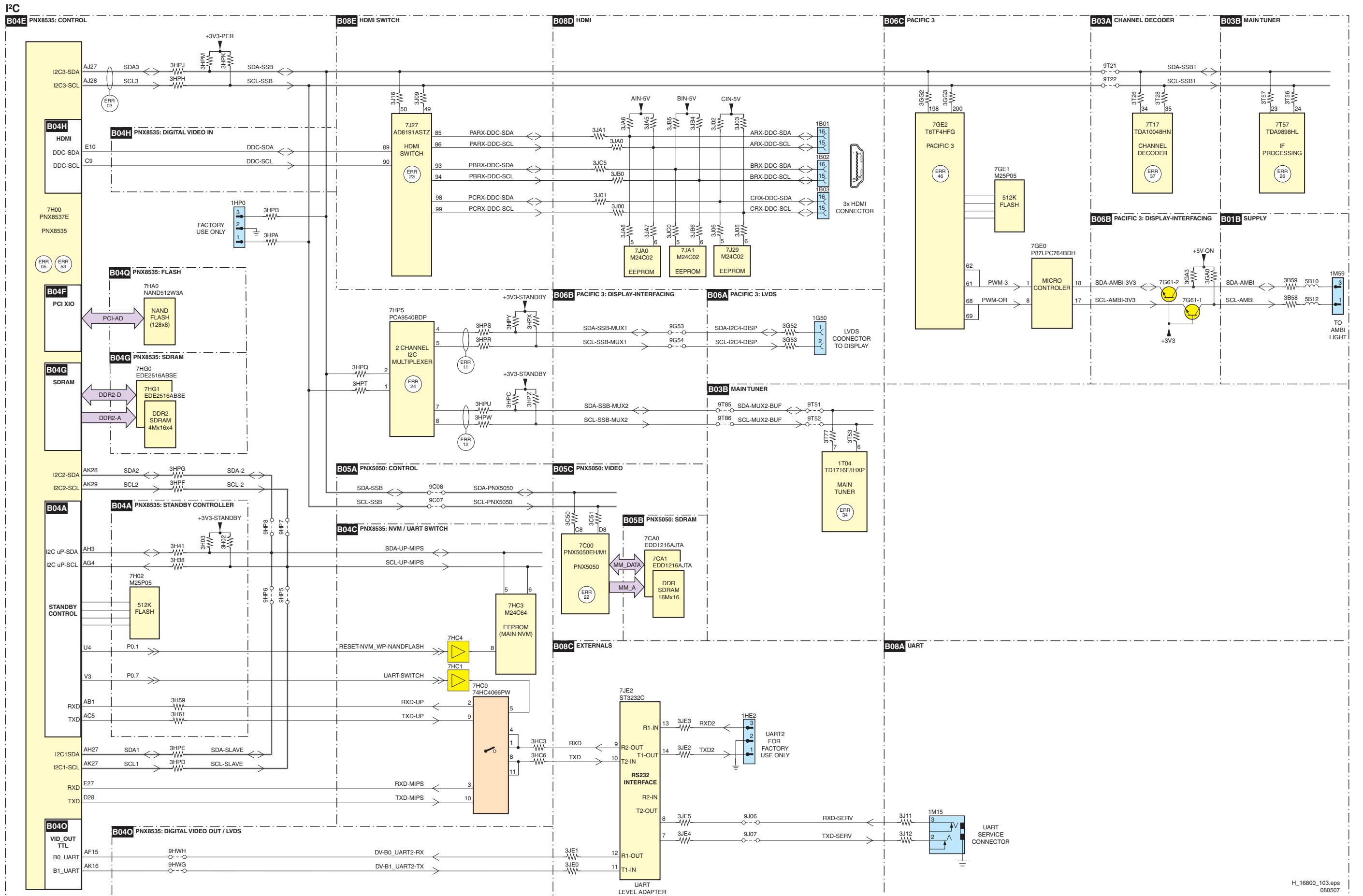
H\_1680C

SSB: Test Points (Part 2 Bottom Side)



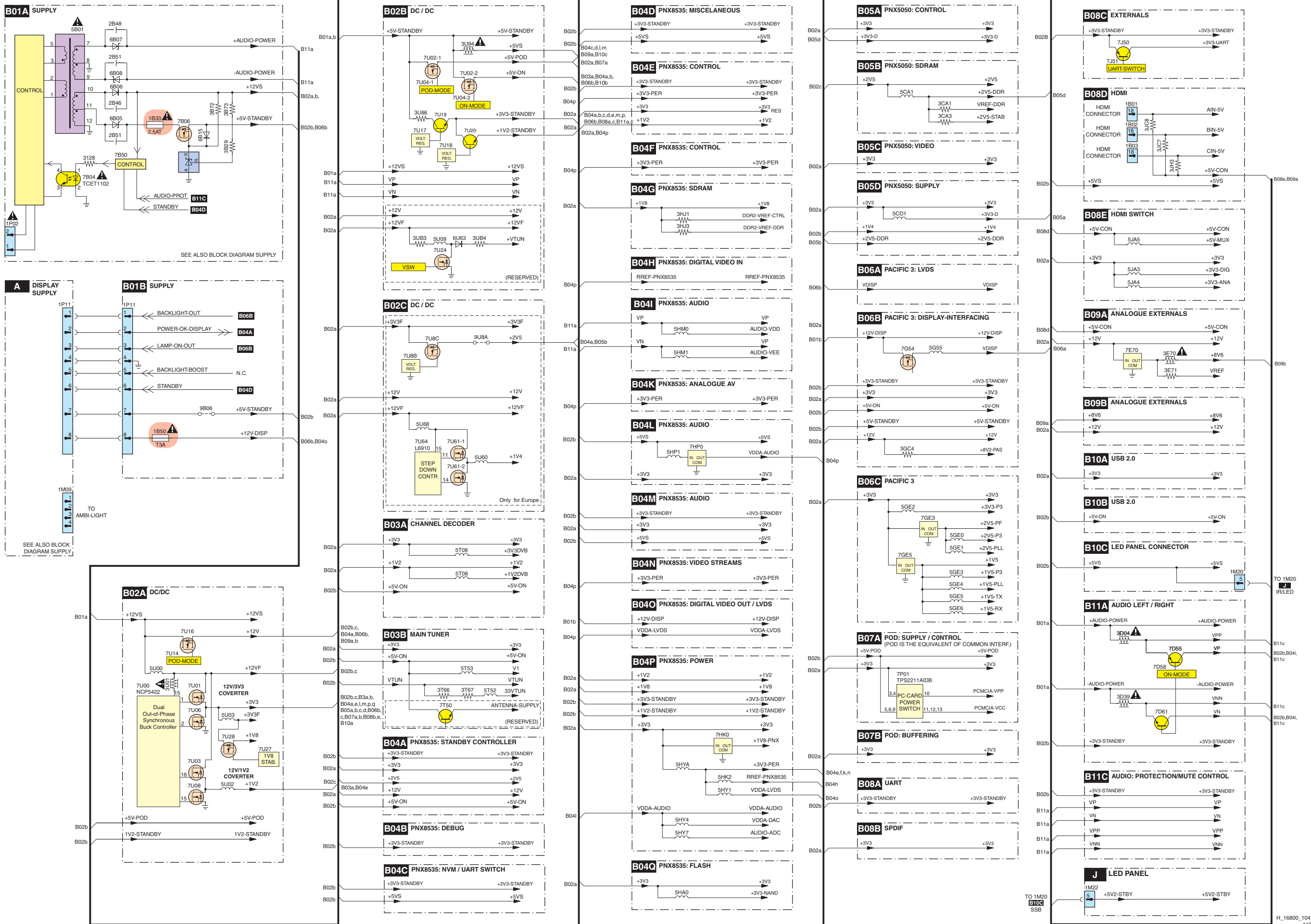


### I2C IC Overview



# Supply Lines Overview

## SUPPLY LINES OVERVIEW

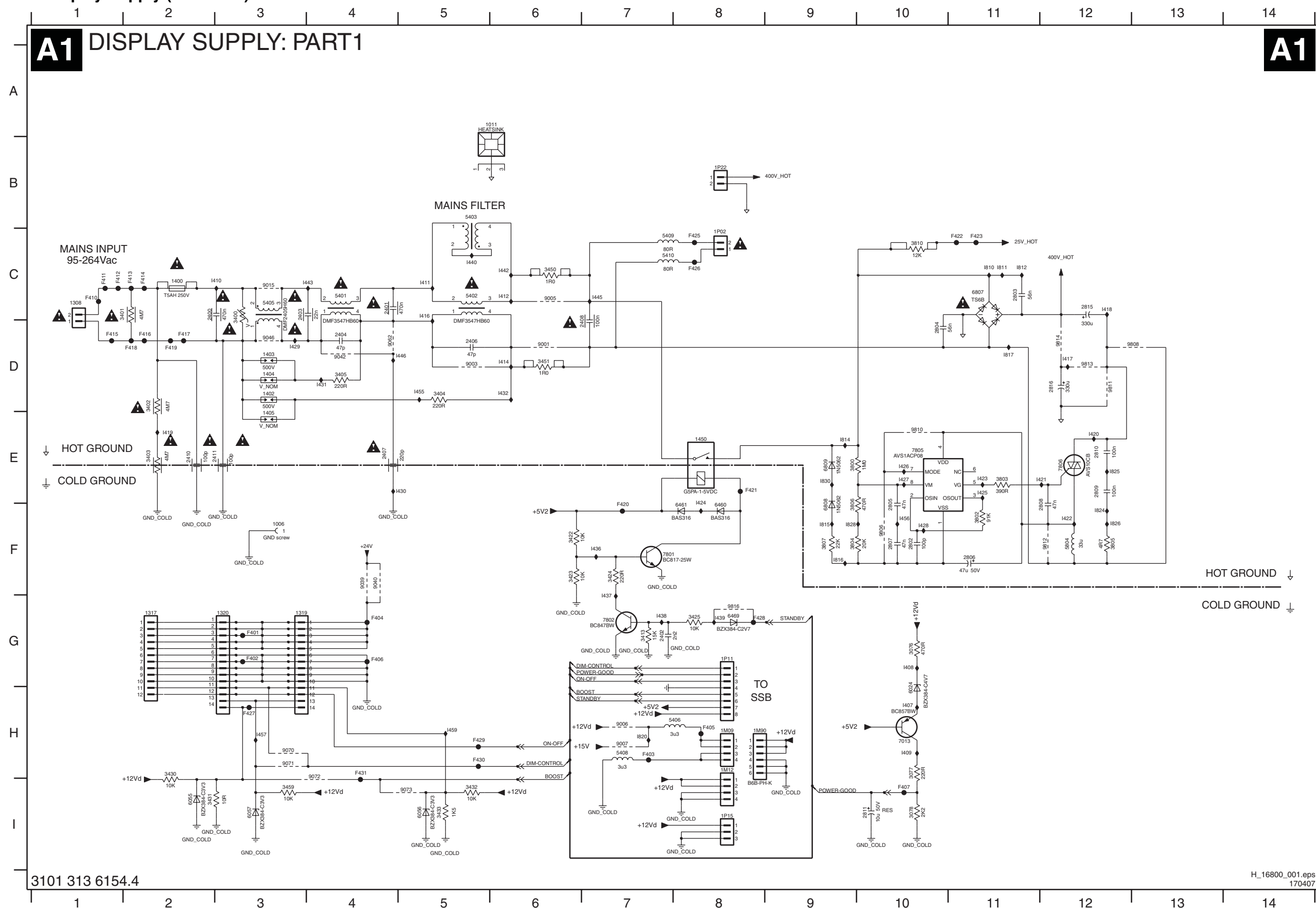


# 7. Circuit Diagrams and PWB Layouts

## Display Supply (32" & 37"): Part 1

### A1 DISPLAY SUPPLY: PART 1

### A1



- 1006 F3
- 1011 A6
- 1308 C1
- 1317 G2
- 1319 G3
- 1320 G3
- 1400 C2
- 1402 D3
- 1403 D3
- 1404 D3
- 1405 E3
- 1450 E8
- 1M09 H8
- 1M12 H8
- 1M90 H8
- 1P02 C8
- 1P11 G8
- 1P15 I8
- 1P22 B8
- 2400 C2
- 2401 C4
- 2402 G7
- 2403 C3
- 2404 D4
- 2406 D5
- 2407 E4
- 2408 D7
- 2410 E2
- 2411 E3
- 2802 F10
- 2803 C11
- 2804 D10
- 2805 F10
- 2806 F11
- 2807 F10
- 2808 F12
- 2809 E12
- 2810 E12
- 2811 I10
- 2815 C12
- 2816 D12
- 3076 G10
- 3077 H10
- 3078 I10
- 3400 C3
- 3401 C2
- 3402 D2
- 3403 E2
- 3404 D5
- 3405 D4
- 3413 G7
- 3422 F6
- 3423 F6
- 3424 F7
- 3425 G8
- 3430 H2
- 3431 I2
- 3432 I5
- 3433 I5
- 3450 C6
- 3451 D6
- 3459 I3
- 3800 E9
- 3802 F11
- 3803 E11
- 3804 F9
- 3805 F12
- 3806 F9
- 3807 F9
- 3810 C10
- 5401 C4
- 5402 C5
- 5403 B5
- 5406 C3
- 5406 H8
- 5408 H7
- 5409 C7
- 5410 C7
- 5804 F12
- 6055 I2
- 6056 I5
- 6057 I3
- 6460 F8
- 6461 F8
- 6469 G8
- 6807 C11
- 6808 F9
- 6809 E9
- 7013 H10
- 7801 F7
- 7802 G7
- 7805 E10
- 7806 E12
- 9001 D6
- 9003 D5
- 9006 C6
- 9006 H7
- 9007 H7
- 9015 C3
- 9039 F4
- 9040 F4
- 9042 D4
- 9046 D3
- 9062 D4
- 9070 H3
- 9071 H3
- 9072 H4
- 9073 I5
- 9806 F10
- 9808 D13
- 9810 E10
- 9811 D12
- 9812 F12
- 9813 D12
- 9814 D12
- 9816 G8
- F401 G3
- F402 G3
- F403 H7
- F404 G4
- F405 H8
- F406 G4
- F407 I10
- F410 C1
- F411 C1
- F412 C1
- F413 C2
- F414 C2
- F415 D1
- F416 D2
- F417 D2
- F418 D2
- F419 D2
- F420 F7
- F421 E8
- F422 C11
- F423 C8
- F425 C8
- F426 C8
- F427 H3
- F428 G8
- F429 H5
- F430 H5
- F431 H4
- I408 G10
- I409 H10
- I410 C3
- I411 C5
- I412 C6
- I414 D6
- I416 C5
- I417 D12
- I418 C12
- I419 E2
- I420 E12
- I421 E12
- I422 F12
- I423 E11
- I424 E8
- I425 E11
- I426 E10
- I427 E10
- I428 F10
- I429 D3
- I430 E5
- I431 D4
- I432 D6
- I436 F7
- I437 G7
- I438 G7
- I439 G8
- I440 C5
- I442 C6
- I443 C4
- I445 C7
- I446 D5
- I455 D5
- I457 H3
- I459 H5
- I810 C11
- I811 C11
- I812 C11
- I814 E9
- I815 F9
- I816 F9
- I817 D11
- I820 H7
- I824 F12
- I825 E12
- I826 F12
- I828 F9
- I830 E9

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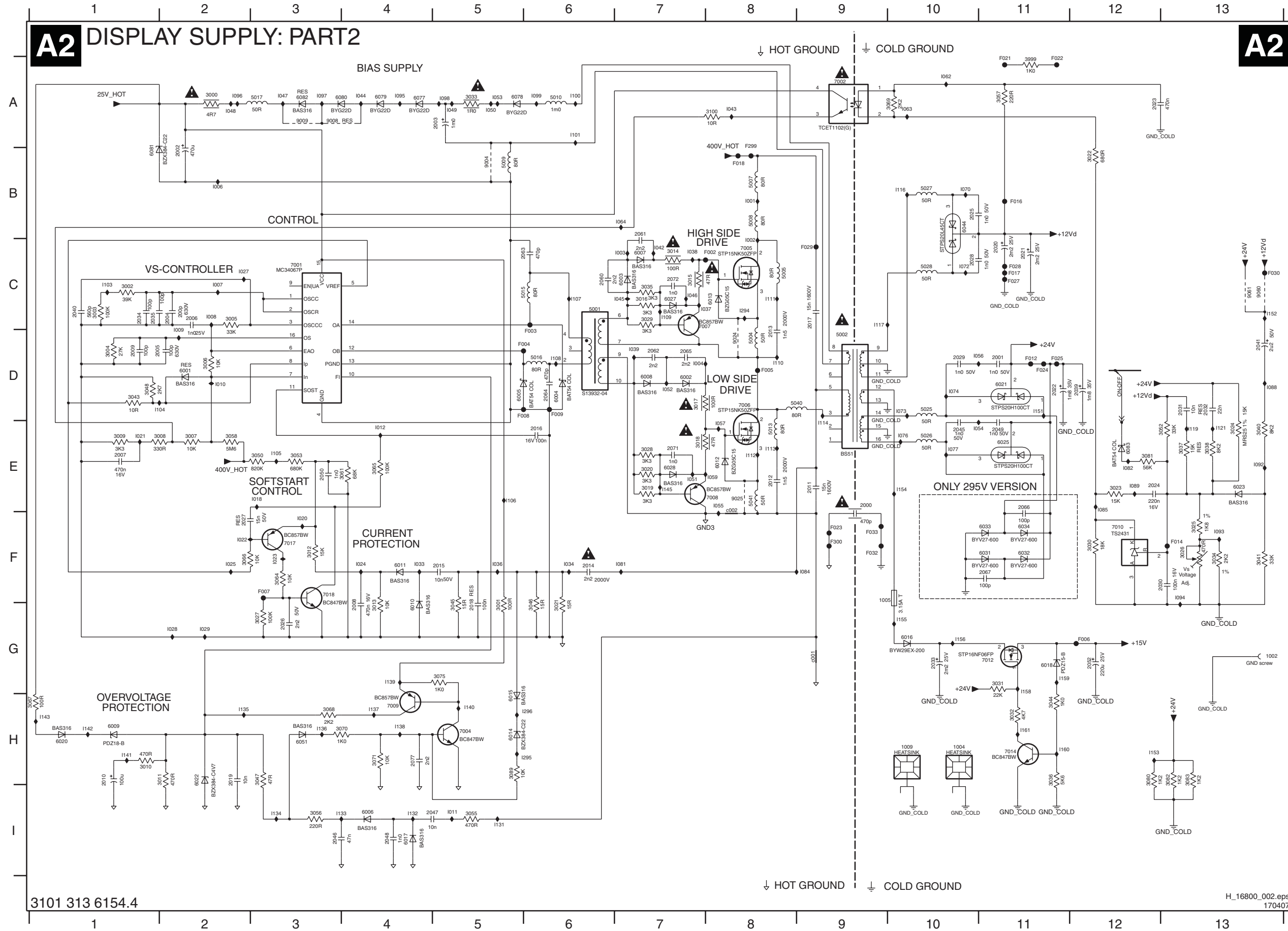
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Display Supply (32" & 37"): Part 2

A2

DISPLAY SUPPLY: PART 2

A2



1002 G13	3064 F3	F299 B8
1004 H10	3065 E4	F300 F9
1005 F9	3066 F2	I001 B8
1009 H10	3067 H1	I002 C8
2000 E9	3068 H3	I003 C7
2001 D11	3069 A10	I004 D7
2002 B2	3070 H4	I006 B2
2003 A5	3071 H4	I007 C2
2004 C2	3075 G5	I008 C2
2005 D1	3080 H12	I009 D2
2006 C2	3081 E12	I010 D2
2007 E1	3082 H13	I011 I5
2008 G4	3083 H13	I012 E4
2009 D1	3089 H5	I018 E3
2010 H1	3100 A8	I020 F3
2011 E9	3999 A11	I021 E1
2012 E8	5001 C6	I022 F2
2013 D8	5002 D9	I023 F3
2014 F6	5004 D8	I024 F4
2015 F5	5005 C8	I025 F2
2016 E6	5007 B8	I027 C2
2017 C9	5008 B8	I028 G2
2018 G5	5009 B5	I029 G2
2019 H2	5010 A6	I033 F4
2020 C11	5013 E8	I034 F6
2021 C11	5015 C6	I036 F5
2022 D11	5016 D6	I037 C8
2023 A12	5017 A3	I038 C7
2024 E12	5025 D10	I039 D7
2025 B10	5028 E10	I042 C7
2026 G3	5027 B10	I043 A8
2027 F2	5028 C10	I044 A4
2028 C10	5040 D8	I045 C7
2029 D10	5041 E8	I046 C7
2030 F13	6001 D2	I047 A3
2031 D13	6002 D7	I048 A2
2032 D13	6003 C7	I049 A5
2033 G10	6004 D6	I050 A5
2034 C1	6005 D5	I051 E7
2035 C1	6006 I4	I052 D7
2039 D12	6007 C7	I053 A5
2040 C1	6008 D7	I054 E10
2041 D13	6009 H1	I055 E8
2045 E10	6010 G4	I056 D10
2046 I3	6011 F4	I057 E8
2047 I4	6012 E8	I059 E8
2048 I4	6013 C8	I062 A10
2049 E11	6014 H5	I063 A10
2050 E3	6015 H5	I064 B7
2052 G12	6016 G10	I070 B10
2060 C6	6017 I4	I072 C10
2061 B7	6018 G11	I073 D10
2062 D7	6020 H1	I074 D10
2063 C6	6021 D11	I076 E10
2064 D6	6022 H2	I077 E10
2065 D7	6023 E13	I081 F7
2066 E11	6025 E11	I082 E12
2067 F11	6027 C7	I084 F9
2071 E7	6028 E7	I085 E12
2072 C7	6031 F11	I086 B13
2077 H4	6032 F11	I089 E12
3000 A2	6033 F11	I092 E13
3001 G5	6034 F11	I093 F13
3002 C1	6044 B10	I094 F13
3003 C1	6051 H3	I095 A4
3004 D1	6077 A4	I096 A2
3005 C2	6078 A5	I097 A3
3006 D2	6079 A4	I098 A5
3007 E2	6080 A3	I099 A6
3008 E2	6081 B1	I100 A6
3009 E7	6082 A3	I101 A6
3010 H1	6083 E12	I103 C1
3011 H2	7001 C3	I104 D2
3012 F3	7002 A9	I105 E3
3013 G4	7004 H5	I106 E5
3014 C7	7005 C8	I107 C6
3015 C7	7006 D8	I108 D6
3016 C7	7007 C7	I109 C7
3017 D7	7008 E8	I110 D8
3018 E7	7009 H4	I111 C8
3019 E7	7010 F12	I112 E8
3020 E7	7012 G11	I113 E8
3021 G6	7014 H11	I114 E9
3022 B12	7017 F3	I116 B10
3023 E12	7018 F3	I117 C9
3024 E13	9004 B5	I119 E13
3025 F13	9008 A3	I121 E13
3026 F13	9009 A3	I131 I5
3027 G3	9024 D8	I132 I4
3028 E7	9025 E8	I133 I3
3029 C7	9060 C13	I134 I3
3030 F12	9061 C13	I135 H2
3031 G11	F002 C8	I136 H3
3032 H11	F003 D6	I137 H4
3033 A5	F004 D6	I138 H4
3034 F13	F005 D8	I139 G4
3035 C7	F006 G12	I140 H5
3036 H11	F007 F3	I141 H1
3037 E13	F008 D5	I142 H1
3038 E13	F009 D6	I143 H1
3040 E13	F021 D11	I145 E7
3041 F13	F014 F13	I151 D11
3043 D1	F016 B11	I152 C13
3044 H11	F017 C11	I153 H12
3045 G5	F018 B8	I154 E10
3046 G6	F021 A11	I155 G10
3047 H3	F022 A11	I156 G10
3048 D1	F023 F9	I158 G11
3050 E3	F024 D11	I159 G11
3052 E13	F025 D11	I160 H11
3053 E3	F027 C11	I161 H11
3055 I5	F028 C11	I294 C8
3056 I3	F029 C9	I295 H6
3057 A11	F030 C13	I296 H6
3058 E2	F032 F9	C001 G9
3061 E4	F033 F9	C002 F8

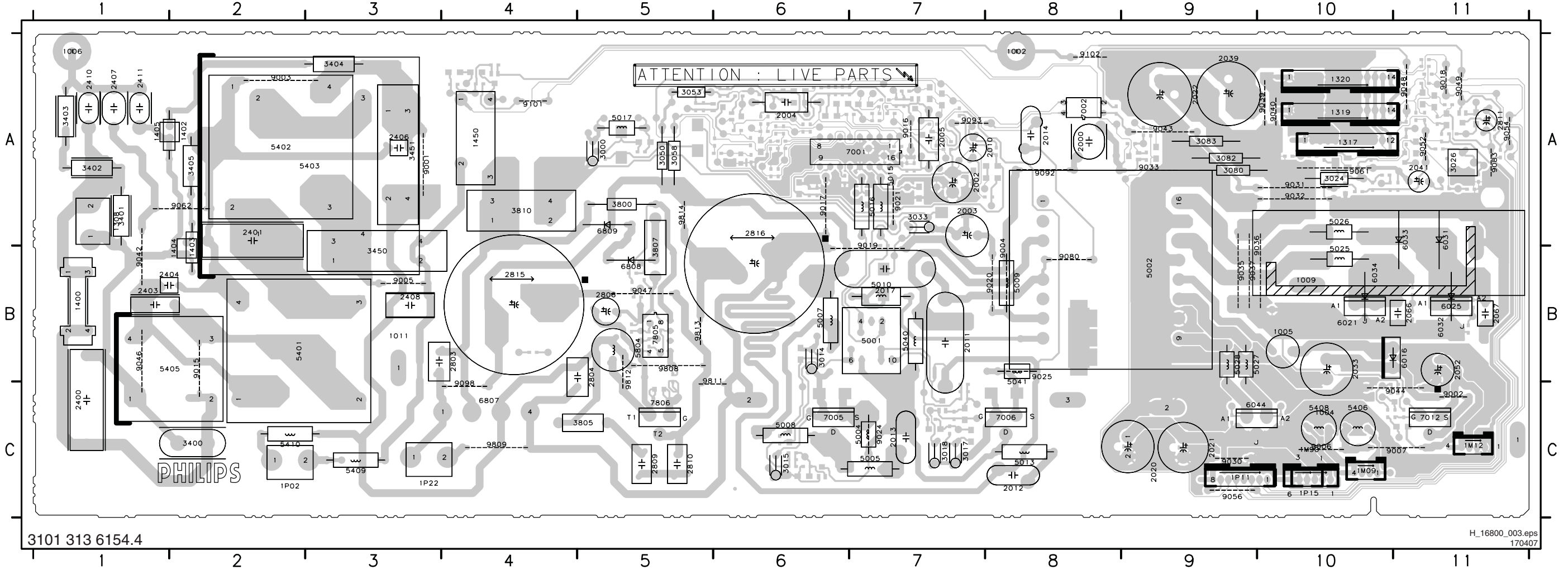
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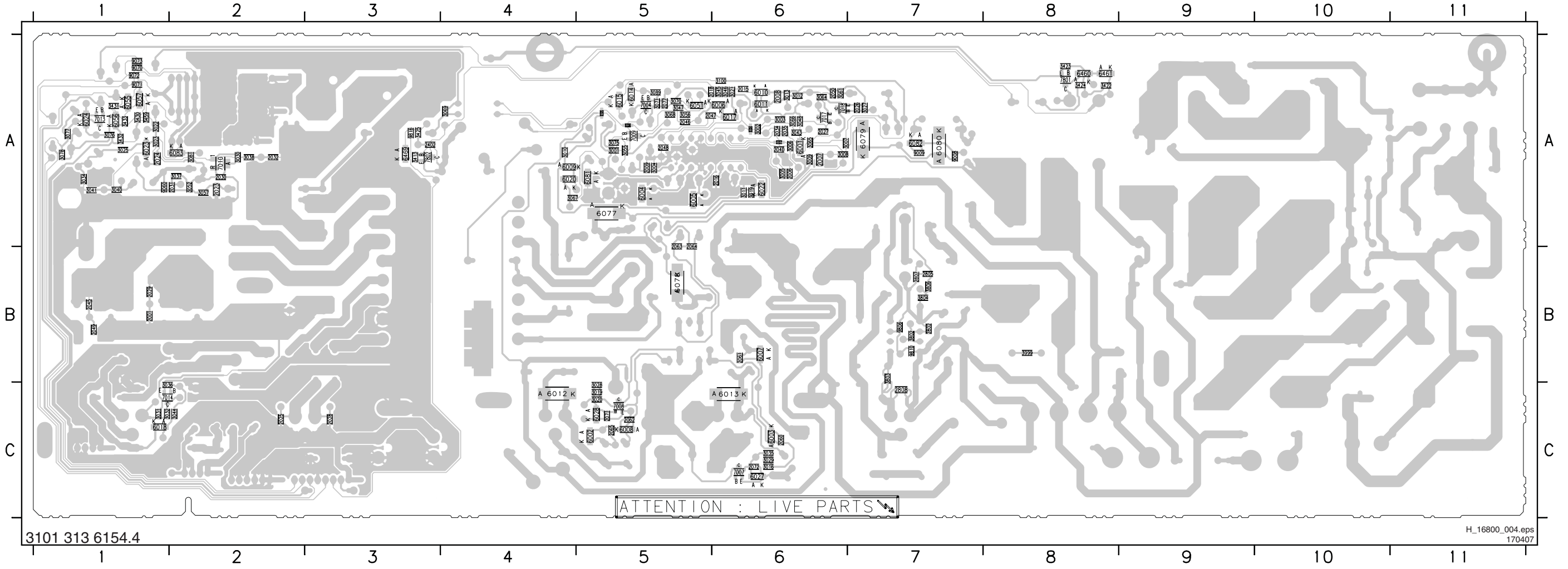
### Layout Display Supply Panel (32" & 37") (Top Side)

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1004 C11	1400 B1	1P02 C3	2010 A8	2033 B10	2404 B1	2809 C5	3018 C7	3083 A9	3800 A5	5008 C6	5027 B9	5408 C10	6033 A11	7006 C8	9006 C10	9024 C7	9039 A10	9052 A11	9098 B4	
1005 B10	1402 A1	1P11 C9	2011 B7	2039 A9	2406 A3	2810 C5	3024 A10	3400 C1	3805 C5	5009 B8	5028 B9	5409 C3	6034 B10	7012 C11	9007 C11	9025 B8	9040 A10	9054 A11	9101 A4	
1006 A1	1403 A2	1P15 C10	2012 C8	2041 A11	2407 A1	2811 A11	3026 A11	3401 A1	3807 B5	5010 B7	5040 B7	5410 C2	6044 C9	7805 B5	9015 B2	9030 C9	9042 A1	9056 C9	9102 A8	
1009 B10	1404 B2	1P22 C3	2013 C7	2052 B11	2408 B3	2815 A4	3033 A7	3402 A1	3810 A4	5013 C8	5041 B7	5804 B5	6807 C3	7806 C5	9016 A7	9031 A10	9043 A9	9061 A10	9808 B5	
1011 C3	1405 A2	2000 A8	2014 A8	2066 B11	2410 A1	2816 A6	3050 A5	3403 A1	5001 B7	5015 A7	5401 B3	6016 B10	6808 B5	9001 A3	9017 A6	9032 A10	9044 C11	9062 A1	9809 C4	
1308 A1	1450 A4	2002 A7	2017 A7	2067 B11	2411 A1	2815 A4	3000 A5	3053 A5	5002 B9	5016 A6	5402 A2	6021 B10	6809 A5	9002 C11	9018 A11	9033 A9	9046 B1	9080 B8	9811 B5	
1317 A10	1M09 C10	2003 A7	2020 C9	2400 C1	2803 B4	3014 B6	3058 A5	3405 A2	5004 C7	5017 A5	5403 A3	6025 B11	7001 A7	9003 A2	9019 A7	9035 B9	9047 B5	9083 A11	9812 C5	
1319 A10	1M12 C11	2004 A6	2021 C9	2401 B2	2804 B5	3015 C6	3080 A10	3450 B3	5005 C7	5025 B10	5405 B2	6031 A11	7002 A8	9004 A8	9020 B8	9036 B10	9048 A11	9092 A8	9813 B5	



Layout Display Supply Panel (32" & 37") (Bottom Side)

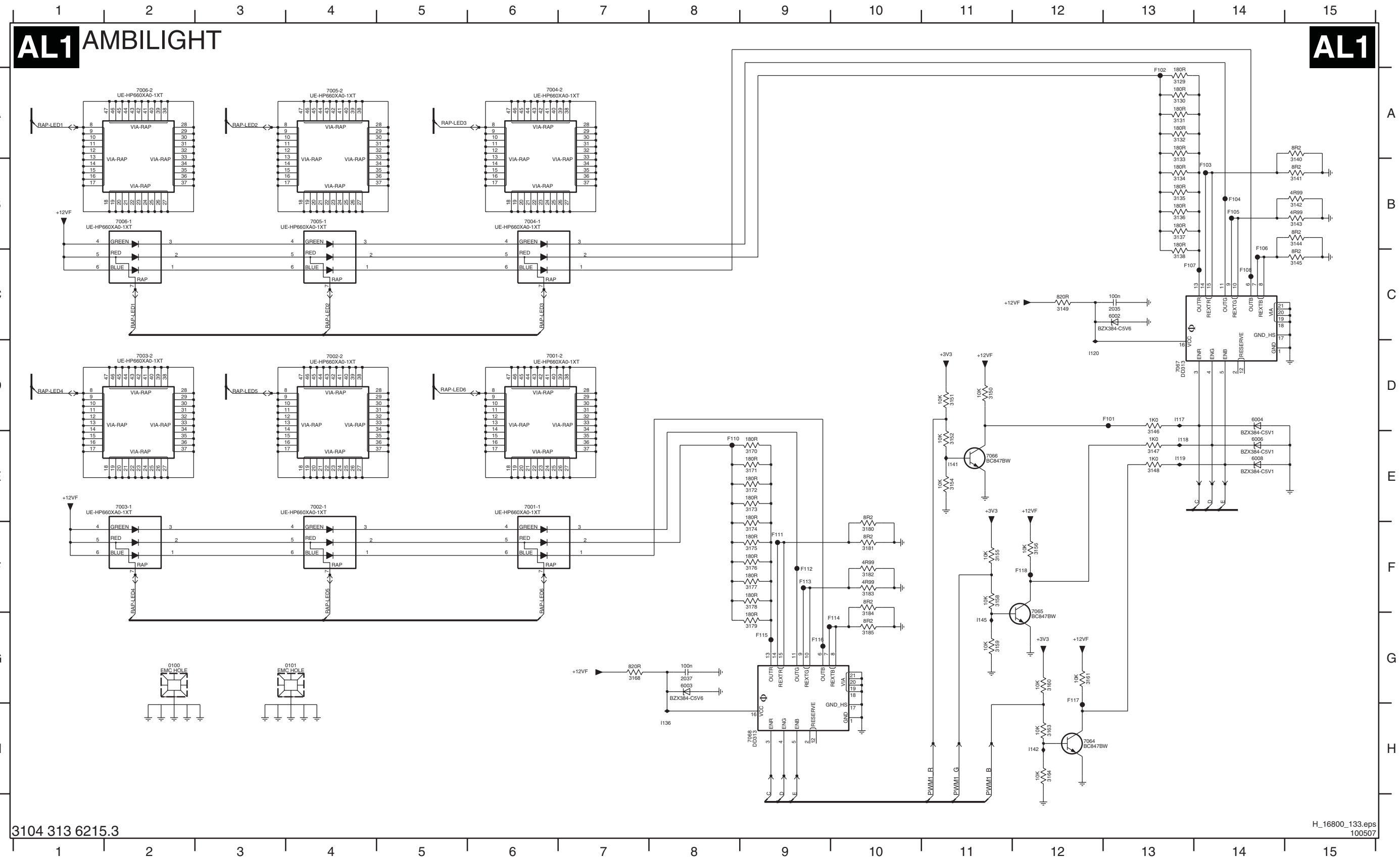
2001 B1	2019 A6	2030 A2	2047 A5	2064 A5	2807 B7	3007 A6	3019 C5	3029 C6	3038 A2	3048 A6	3066 A6	3077 A1	3424 A8	3803 B7	6005 A5	6013 C6	6024 A1	6078 B5	6469 A3	7017 A6	9071 A1	9810 B7
2006 A6	2023 A2	2031 A2	2048 A5	2065 C5	2808 C7	3008 A6	3020 C5	3030 A2	3040 A1	3052 A2	3067 A4	3078 A1	3425 A3	3804 B7	6006 A6	6014 A5	6027 C6	6079 A7	7004 A5	7018 A6	9072 A1	9816 A3
2007 A6	2024 A1	2032 A2	2049 B1	2071 C5	3001 A5	3009 A6	3021 A6	3031 C1	3041 A1	3055 A5	3068 A5	3081 A2	3430 A1	3806 B7	6007 B6	6015 A5	6028 C5	6080 A7	7007 C6	7801 A8	9073 A1	
2008 A6	2025 C2	2034 A6	2050 A6	2072 C6	3002 A6	3010 A4	3022 A1	3032 C1	3043 A6	3056 A5	3069 A4	3089 A5	3431 A1	3999 B8	6008 C5	6017 A6	6051 A5	6081 A5	7008 C5	7802 A3	9104 A6	
2009 A5	2026 A7	2035 A6	2060 C6	2077 A5	3003 A6	3011 A6	3023 A1	3034 A1	3044 C2	3057 A2	3070 A5	3100 A6	3432 A1	6001 A6	6009 A4	6018 C1	6055 A1	6082 A7	7009 A5	9008 A7	9105 A5	
2015 A6	2027 A6	2040 A6	2061 B6	2402 A3	3004 A5	3012 A6	3025 A1	3035 C6	3045 A6	3061 A6	3071 A5	3413 A3	3433 A1	6002 C5	6010 A6	6020 A4	6056 A1	6083 A2	7010 A2	9009 A7	9106 A5	
2016 A6	2028 C3	2045 B1	2062 C5	2802 B7	3005 A6	3013 A6	3027 A7	3036 C1	3046 A6	3064 A6	3075 A5	3422 A8	3459 A1	6003 C6	6011 A6	6022 A6	6057 A1	6460 A8	7013 A1	9060 A1	9107 A6	
2018 A5	2029 B1	2046 A5	2063 A5	2805 B7	3006 A6	3016 C6	3028 C5	3037 A2	3047 A5	3065 A6	3076 A1	3423 A8	3802 B7	6004 A5	6012 C4	6023 A1	6077 A5	6461 A8	7014 C1	9070 A1	9806 B7	



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**AmbiLight (2 sided 6 LED)**

0100 G2	3129 A13	3133 A13	3137 B13	3142 B15	3146 D13	3150 D11	3155 F11	3160 G12	3168 G7	3173 E9	3177 F9	3181 F10	3185 G10	6006 E14	7002-1 E4	7004-1 B6	7006-1 B2	7066 E11	F102 A13	F106 B14	F111 F9	F115 G9	I117 D13	I136 H8
0101 G4	3130 A13	3134 B13	3138 C13	3143 B15	3147 E13	3151 D11	3156 F12	3161 G12	3170 E9	3174 F9	3178 F9	3182 F10	3186 G10	6008 E14	7002-2 D4	7004-2 A7	7006-2 A2	7067 D13	F103 B14	F107 C13	F112 F9	F116 G9	I118 E13	I141 E11
2035 C13	3131 A13	3135 B13	3140 A15	3144 B15	3148 E13	3152 E11	3158 F11	3163 H12	3171 E9	3175 F9	3179 G9	3183 F10	3187 G10	7001-1 E6	7003-1 E2	7005-1 B4	7064 H12	7068 H9	F104 B14	F108 C14	F113 F9	F117 G12	I119 E13	I142 H12
2037 G8	3132 A13	3136 B13	3141 B15	3145 C15	3149 C12	3154 E11	3159 G11	3164 H12	3172 E9	3176 F9	3180 F10	3184 F10	6004 D14	7001-2 D7	7003-2 D2	7005-2 A4	7065 G12	F101 D13	F105 B14	F110 E8	F114 G10	F118 F12	I120 D12	I145 G11

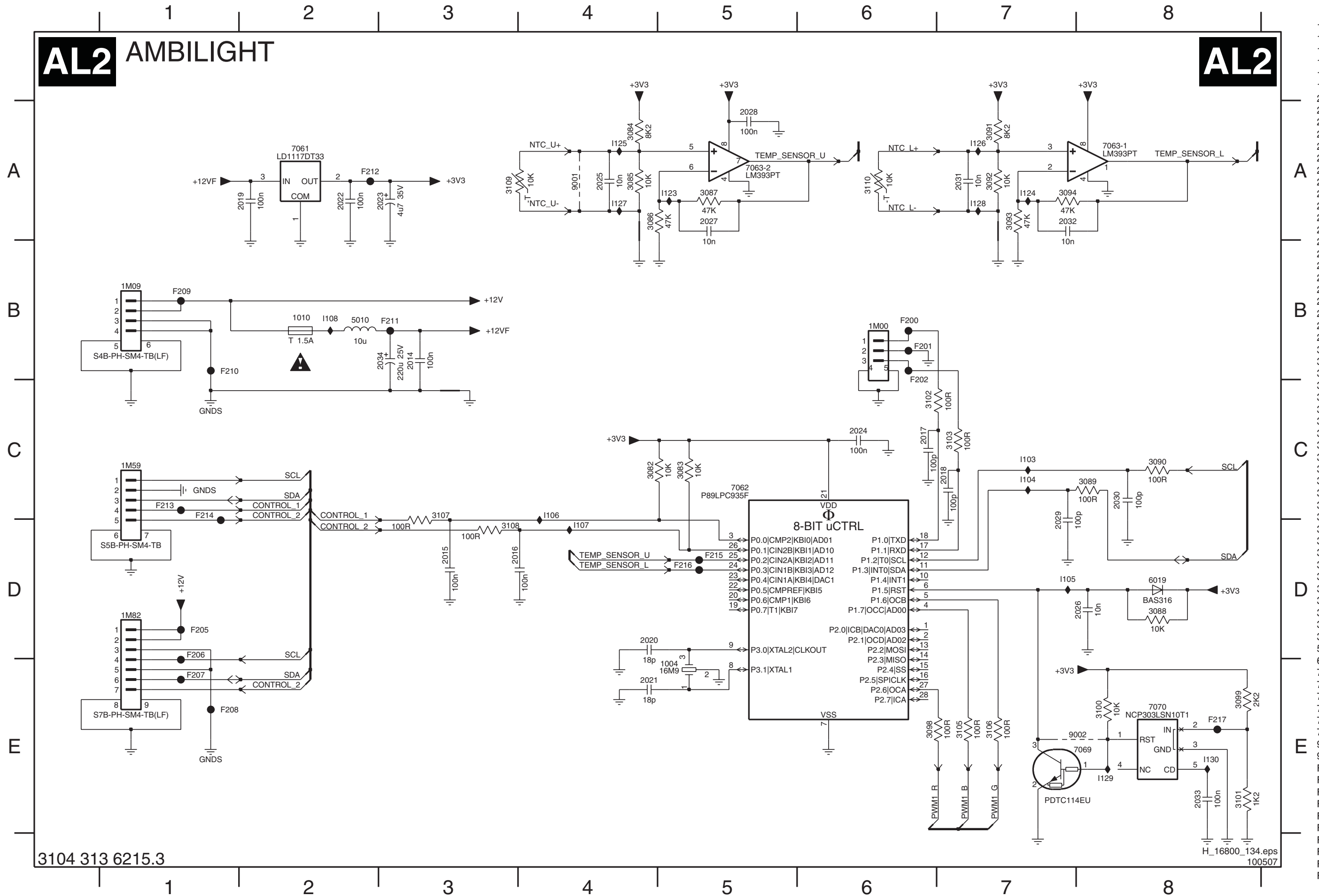


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AmbiLight (2 sided 6 LED)

AL2 AMBILIGHT

AL2



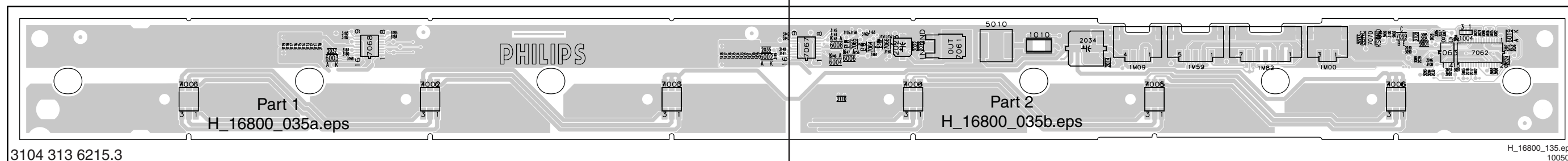
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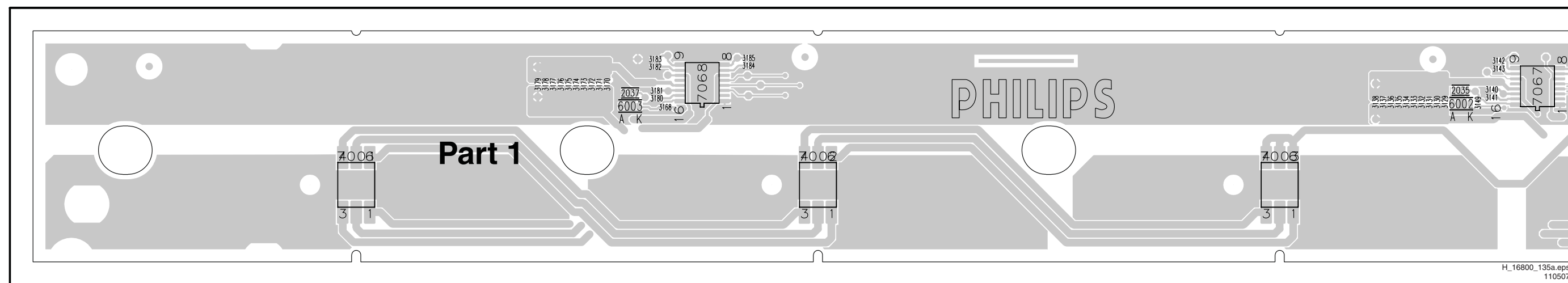


Layout AmbiLight (2 sided 6 LED) (Overview Top Side)

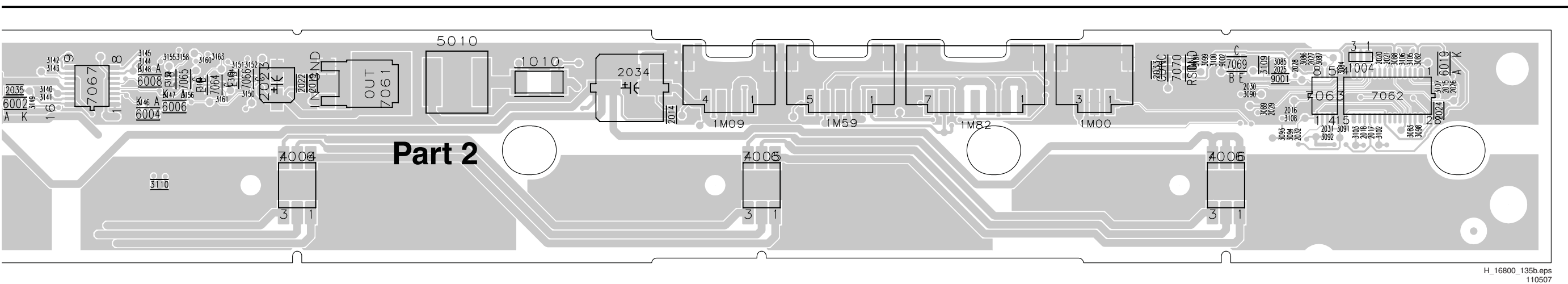
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1010 ---	2015 ---	2021 ---	2027 ---	2033 ---	3084 ---	3090 ---	3099 ---	3106 ---	3130 ---	3136 ---	3143 ---	3149 ---	3156 ---	3164 ---	3174 ---	3180 ---	5010 ---	6019 ---	7006 ---	7066 ---	9002 ---
1M00 ---	2016 ---	2022 ---	2028 ---	2034 ---	3085 ---	3091 ---	3100 ---	3107 ---	3131 ---	3137 ---	3144 ---	3150 ---	3158 ---	3168 ---	3175 ---	3181 ---	6002 ---	7001 ---	7061 ---	7067 ---	
1M09 ---	2017 ---	2023 ---	2029 ---	2035 ---	3086 ---	3092 ---	3101 ---	3108 ---	3132 ---	3138 ---	3145 ---	3151 ---	3159 ---	3170 ---	3176 ---	3182 ---	6003 ---	7002 ---	7062 ---	7068 ---	
1M59 ---	2018 ---	2024 ---	2030 ---	2037 ---	3087 ---	3093 ---	3102 ---	3109 ---	3133 ---	3140 ---	3146 ---	3152 ---	3160 ---	3171 ---	3177 ---	3183 ---	6004 ---	7003 ---	7063 ---	7069 ---	
1M82 ---	2019 ---	2025 ---	2031 ---	3082 ---	3088 ---	3094 ---	3103 ---	3110 ---	3134 ---	3141 ---	3147 ---	3154 ---	3161 ---	3172 ---	3178 ---	3184 ---	6006 ---	7004 ---	7064 ---	7070 ---	



Layout AmbiLight (2 sided 6 LED) (Part 1 Top Side)



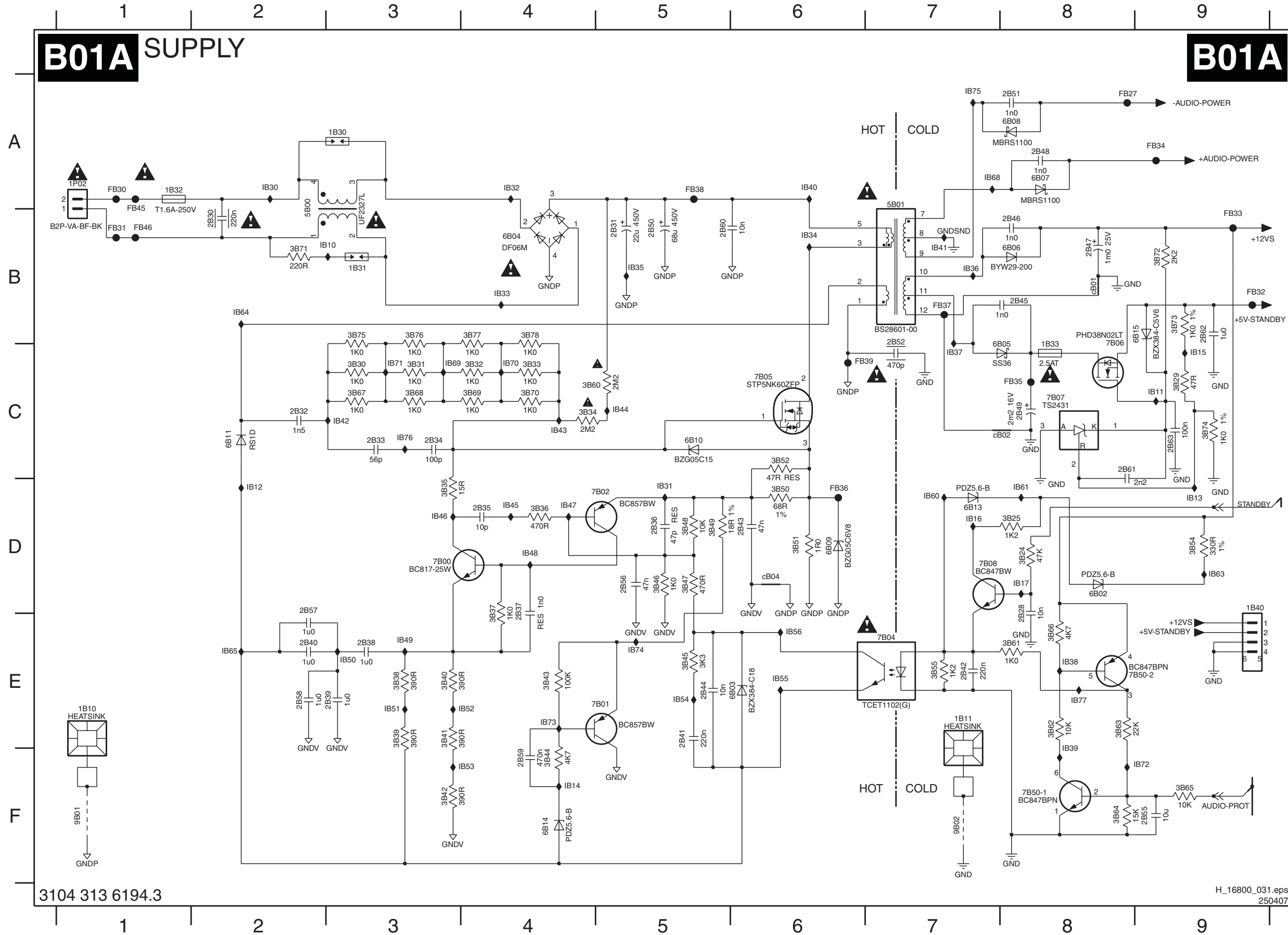
Layout AmbiLight (2 sided 6 LED) (Part 2 Top Side)



SSB: Supply

B01A SUPPLY

B01A



1B10	E1	3B70	C4	IB52	E4
1B11	E7	3B71	B2	IB53	F4
1B30	A3	3B72	B9	IB54	E5
1B31	B3	3B73	B9	IB55	E6
1B32	A1	3B74	C9	IB56	E6
1B33	C8	3B75	B3	IB60	D7
1B40	D9	3B76	B3	IB61	D8
1P02	A1	3B77	B4	IB63	D9
2B30	B2	3B78	B4	IB64	B2
2B31	B5	5B00	A2	IB65	E2
2B32	C2	5B01	A7	IB68	A7
2B33	C3	6B02	D8	IB69	C3
2B34	C3	6B03	E6	IB70	C4
2B35	D4	6B04	B4	IB71	C3
2B36	D5	6B05	C8	IB72	F9
2B37	D4	6B06	B8	IB73	E4
2B38	E3	6B07	A8	IB74	E5
2B39	E3	6B08	A8	IB75	A7
2B40	E2	6B09	D6	IB76	C3
2B41	E5	6B10	C5	IB77	E8
2B42	E7	6B11	C2	cB01	B8
2B43	D6	6B13	D7	cB02	D8
2B44	E5	6B14	F4	cB04	D6
2B45	B8	6B15	B9		
2B46	B8	7B00	D3		
2B47	B8	7B01	E4		
2B48	A8	7B02	D4		
2B49	D8	7B04	E7		
2B50	B5	7B05	C6		
2B51	A7	7B06	B8		
2B52	C7	7B07	C8		
2B55	F9	7B50-1	F8		
2B56	D5	7B50-2	E8		
2B57	D2	9B01	F1		
2B58	E2	9B02	F7		
2B59	F4	FB27	A8		
2B60	B5	FB30	A1		
2B61	C8	FB31	B1		
2B62	B9	FB32	B9		
2B63	C9	FB33	B9		
3B29	C9	FB34	A9		
3B30	C3	FB35	D8		
3B31	C3	FB36	D6		
3B32	C4	FB37	B7		
3B33	C4	FB38	A5		
3B34	C4	FB39	C6		
3B35	D3	FB45	B1		
3B36	D4	FB46	B1		
3B37	D4	IB10	B3		
3B38	E3	IB11	C9		
3B39	E3	IB12	D2		
3B40	E3	IB13	D9		
3B41	E3	IB14	F4		
3B42	F3	IB15	C9		
3B43	E4	IB30	A2		
3B44	F4	IB31	D5		
3B45	E5	IB32	A4		
3B46	D5	IB33	B4		
3B47	D5	IB34	B6		
3B48	D5	IB35	B5		
3B49	D5	IB36	B7		
3B50	D6	IB37	C7		
3B51	D6	IB38	E8		
3B52	C6	IB39	E8		
3B54	D9	IB40	A6		
3B55	E7	IB41	B7		
3B60	C4	IB42	C3		
3B61	E8	IB43	C4		
3B62	E8	IB44	C5		
3B63	E8	IB45	D4		
3B64	F8	IB46	D3		
3B65	F9	IB47	D4		
3B66	E8	IB48	D4		
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3B68	C3	IB50	E3		
3B69	C4	IB51	E3		

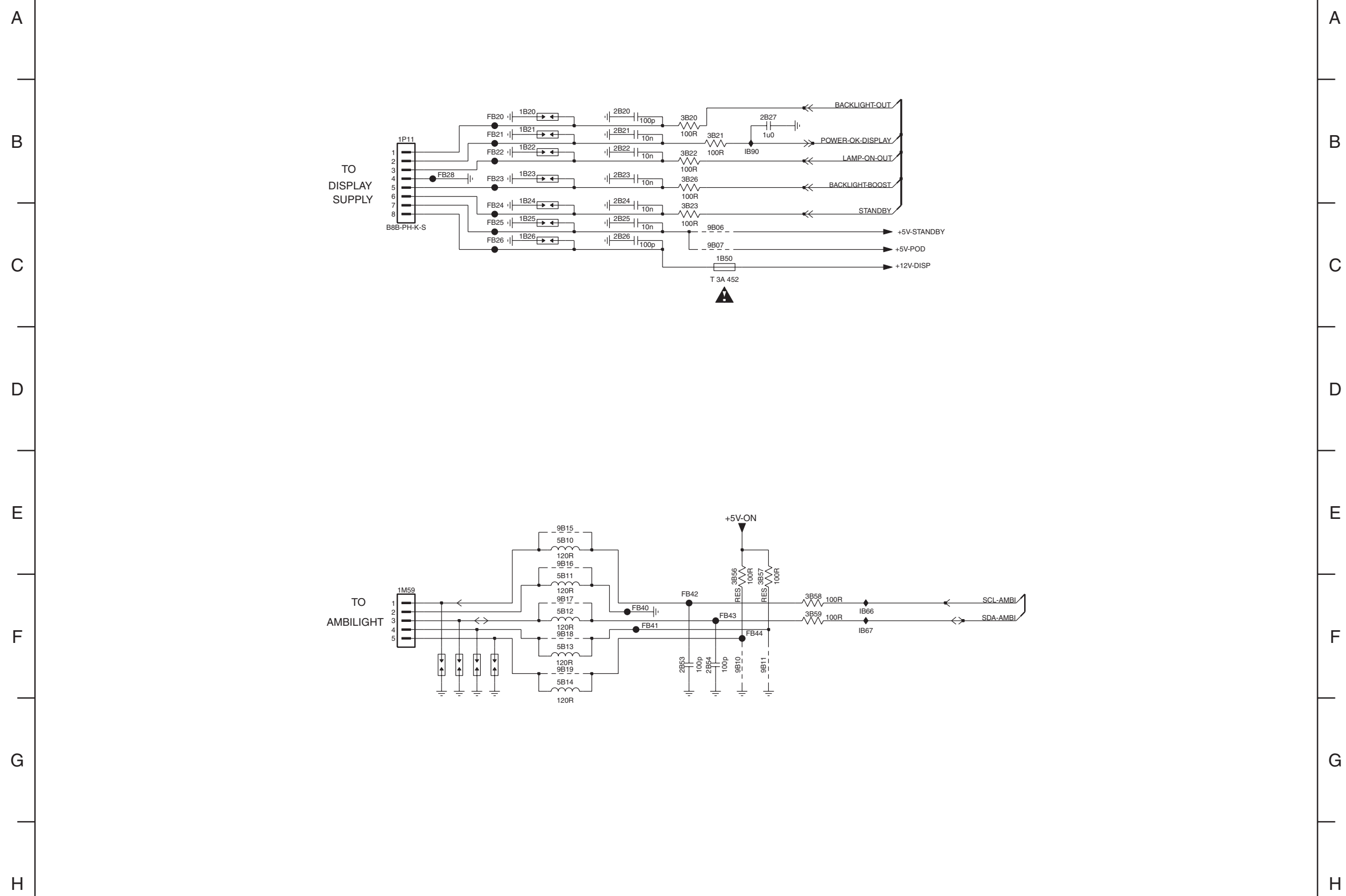
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SSB: Supply

**B01B** SUPPLY

**B01B**



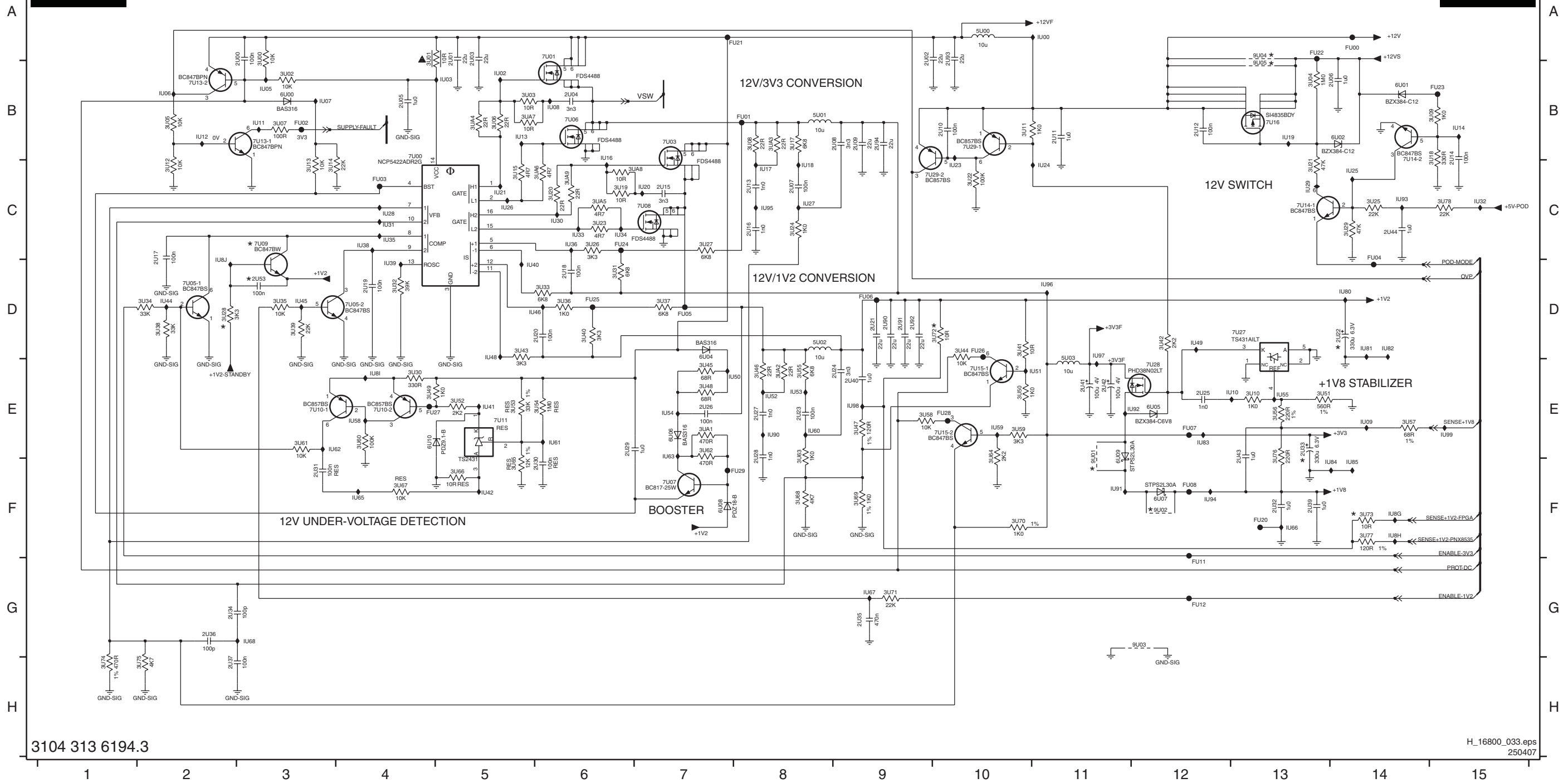
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- 1B25 C4
- 1B26 C4
- 1B50 C6
- 1M59 F3
- 1P11 B3
- 2B20 B5
- 2B21 B5
- 2B22 B5
- 2B23 B5
- 2B24 C5
- 2B25 C5
- 2B26 C5
- 2B27 B6
- 2B53 F6
- 2B54 F6
- 3B20 B6
- 3B21 B6
- 3B22 B6
- 3B23 C6
- 3B26 B6
- 3B56 F6
- 3B57 F6
- 3B58 F7
- 3B59 F7
- 5B10 E5
- 5B11 F5
- 5B12 F5
- 5B13 F5
- 5B14 F5
- 9B06 C6
- 9B07 C6
- 9B10 F6
- 9B11 F6
- 9B15 E5
- 9B16 E5
- 9B17 F5
- 9B18 F5
- 9B19 F5
- FB20 B4
- FB21 B4
- FB22 B4
- FB23 B4
- FB24 C4
- FB25 C4
- FB26 C4
- FB28 B4
- FB40 F5
- FB41 F5
- FB42 F6
- FB43 F6
- FB44 F6
- IB66 F7
- IB67 F7
- IB90 B6

SSB: DC / DC

2U00 A3	2U10 B10	2U20 D6	2U30 F6	2U41 E11	3U00 A3	3U10 E13	3U21 C13	3U31 D6	3U41 D10	3U51 E13	3U61 E3	3U71 G9	3U83 B8	5U03 E11	6U10 E4	7U10-1 E3	7U27 D13	FU01 B8	FU20 F13	IU00 A11	IU12 B2	IU24 C11	IU34 C6	IU46 D6	IU59 E10	IU81 D14	IU91 F11
2U01 A5	2U11 B11	2U21 D9	2U31 F3	2U42 E11	3U01 A4	3U11 B10	3U22 C10	3U32 D4	3U42 D12	3U52 E5	3U62 E7	3U72 D10	3U84 B5	6U00 B3	7U00 B4	7U10-2 E4	7U28 E12	FU02 B3	FU21 A8	IU02 B5	IU13 B5	IU25 C14	IU35 C4	IU48 D5	IU60 E8	IU82 D14	IU92 E12
2U02 A9	2U12 B12	2U22 D14	2U32 F13	2U43 E13	3U02 B3	3U12 C2	3U23 C6	3U33 D6	3U43 D5	3U53 E5	3U63 E8	3U73 F14	3U85 C6	6U01 B14	7U01 A6	7U11 E5	7U29-1 B10	FU03 C4	FU22 A13	IU03 B5	IU14 B15	IU26 C5	IU36 C6	IU49 D12	IU61 E6	IU83 E12	IU93 C14
2U03 A5	2U13 C8	2U23 D8	2U33 E13	2U44 C14	3U03 B5	3U13 C3	3U24 C8	3U34 D2	3U44 D10	3U54 E6	3U64 E10	3U74 H1	3U86 C9	6U02 B14	7U03 B7	7U13-1 B3	7U29-2 C10	FU04 C14	FU23 B15	IU05 B3	IU16 B6	IU27 C8	IU38 C4	IU50 E3	IU62 E3	IU84 F14	IU94 F12
2U04 B6	2U14 B15	2U24 E9	2U34 G2	2U45 D3	3U04 B13	3U14 C3	3U25 C14	3U35 D3	3U45 E7	3U55 E8	3U65 F5	3U75 H2	3U87 B5	6U04 D7	7U05-1 D2	7U13-2 B2	9U01 E11	FU05 D7	FU24 C6	IU06 B2	IU17 C8	IU28 C4	IU39 D4	IU51 E11	IU63 E7	IU85 F14	IU95 C8
2U05 B4	2U15 C7	2U25 E12	2U35 G9	2U46 D9	3U05 B2	3U15 C5	3U26 C6	3U36 D6	3U46 E8	3U56 E13	3U66 F5	3U76 E13	3U88 C7	6U05 E12	7U05-2 D4	7U14-1 C13	9U02 F12	FU06 D9	FU25 D6	IU07 B3	IU18 C8	IU29 C13	IU40 D5	IU52 E8	IU65 F4	IU86 D11	IU96 D11
2U06 B14	2U16 C8	2U26 E7	2U36 G2	2U47 D9	3U06 B5	3U17 B8	3U27 C7	3U37 D7	3U47 E9	3U57 E14	3U67 F4	3U77 F14	3U89 C6	6U06 E7	7U06 B6	7U14-2 B14	9U03 G12	FU07 E12	FU26 D10	IU08 B6	IU19 B13	IU30 C6	IU41 E5	IU53 E8	IU66 F13	IU88 F14	IU97 D11
2U07 C8	2U17 C2	2U27 E8	2U37 H2	2U48 D9	3U07 B3	3U18 B15	3U28 D2	3U38 D2	3U48 E7	3U58 E9	3U68 F8	3U78 C15	3U90 A10	6U07 F12	7U07 F7	7U15-1 E10	9U04 A13	FU08 F12	FU27 E4	IU09 E14	IU20 C7	IU31 C4	IU42 F5	IU54 E7	IU67 G9	IU89 E9	IU98 E9
2U08 B9	2U18 D6	2U28 E8	2U39 F13	2U49 A10	3U08 B8	3U19 C6	3U29 C14	3U39 D3	3U49 E4	3U59 E10	3U69 F9	3U79 A7	3U91 B8	6U08 F7	7U08 C7	7U15-2 E10	9U05 B13	FU11 G12	FU28 E10	IU10 E13	IU21 C5	IU32 C15	IU44 D2	IU55 E13	IU68 G3	IU81 D2	IU99 E15
2U09 B9	2U19 D4	2U29 E6	2U40 E9	2U49 B9	3U09 B15	3U20 C6	3U30 E4	3U40 D6	3U50 E10	3U60 E4	3U70 F10	3U82 E8	3U92 D8	6U09 E11	7U09 C3	7U16 B13	FU00 A14	FU12 G12	FU29 F8	IU11 B3	IU23 C10	IU33 C6	IU45 D3	IU58 E4	IU80 D14	IU90 E8	IU99 E15

B02A DC / DC

B02A



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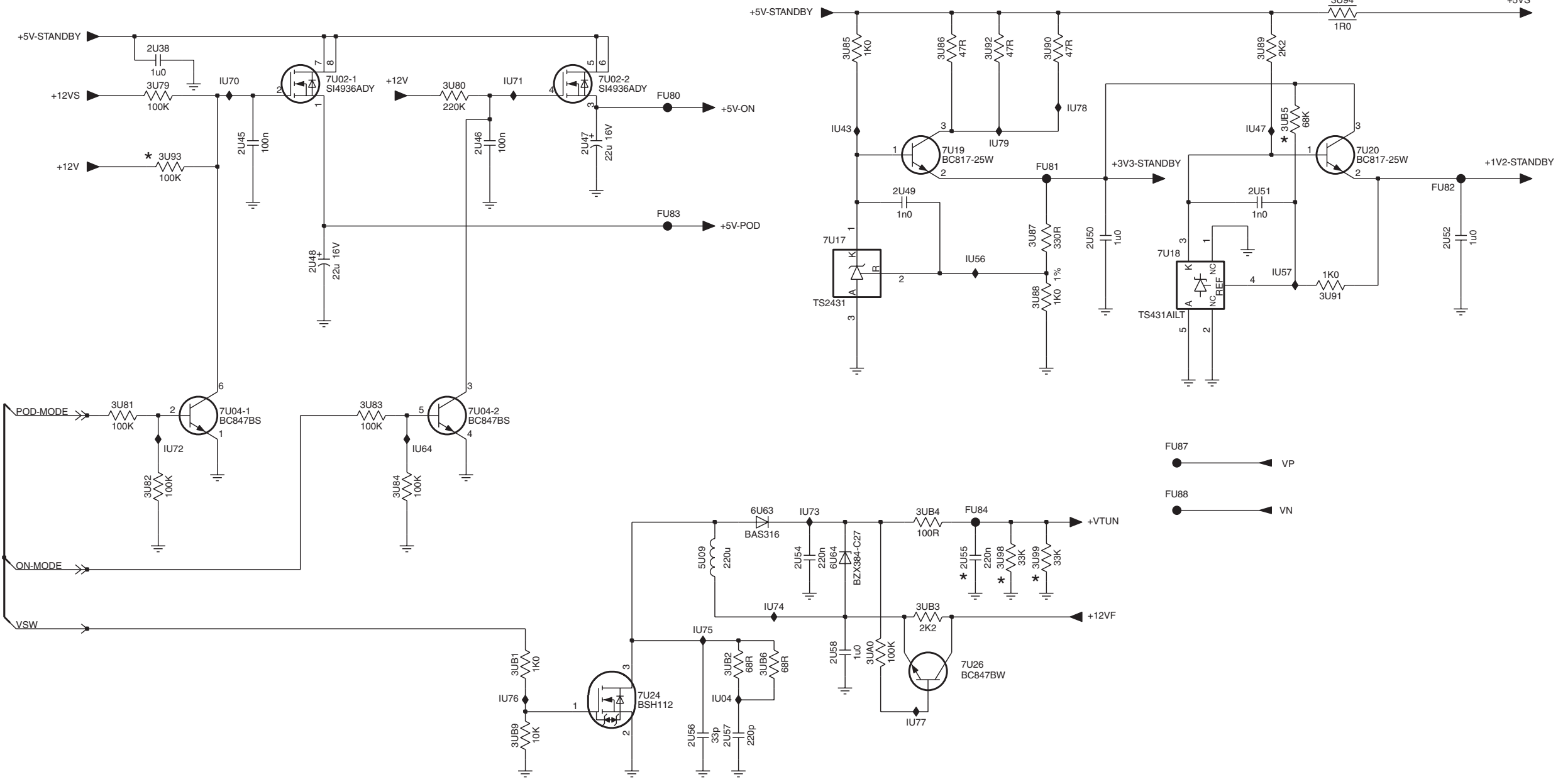
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SSB: DC / DC

**B02B** DC / DC

**B02B**

A  
B  
C  
D  
E  
F



- 2U38 A1
- 2U45 B2
- 2U46 B3
- 2U47 B4
- 2U48 C2
- 2U49 B6
- 2U50 B7
- 2U51 B8
- 2U52 B9
- 2U54 D5
- 2U55 D6
- 2U56 E5
- 2U57 E5
- 2U58 E5
- 3U79 B1
- 3U80 B3
- 3U81 C1
- 3U82 D1
- 3U83 C3
- 3U84 D3
- 3U85 A5
- 3U86 A6
- 3U87 B7
- 3U88 C7
- 3U89 A8
- 3U90 A7
- 3U91 C8
- 3U92 A6
- 3U93 B1
- 3U94 A8
- 3U98 D6
- 3U99 D7
- 3UA0 E6
- 3UB1 E3
- 3UB2 E5
- 3UB3 E6
- 3UB4 D6
- 3UB5 B8
- 3UB6 E5
- 3UB9 E3
- 5U09 D5
- 6U63 D5
- 6U64 D5
- 7U02-1 B2
- 7U02-2 B4
- 7U04-1 D2
- 7U04-2 D3
- 7U17 B5
- 7U18 C7
- 7U19 B6
- 7U20 B9
- 7U24 E4
- 7U26 E6
- FU80 B4
- FU81 B7
- FU82 B9
- FU83 B4
- FU84 D6
- FU87 D7
- FU88 D7
- IU04 E5
- IU43 B5
- IU47 B8
- IU56 C6
- IU57 C8
- IU64 D3
- IU70 B2
- IU71 B3
- IU72 D1
- IU73 D5
- IU74 E5
- IU75 E5
- IU76 E3
- IU77 E6
- IU78 B7
- IU79 B6

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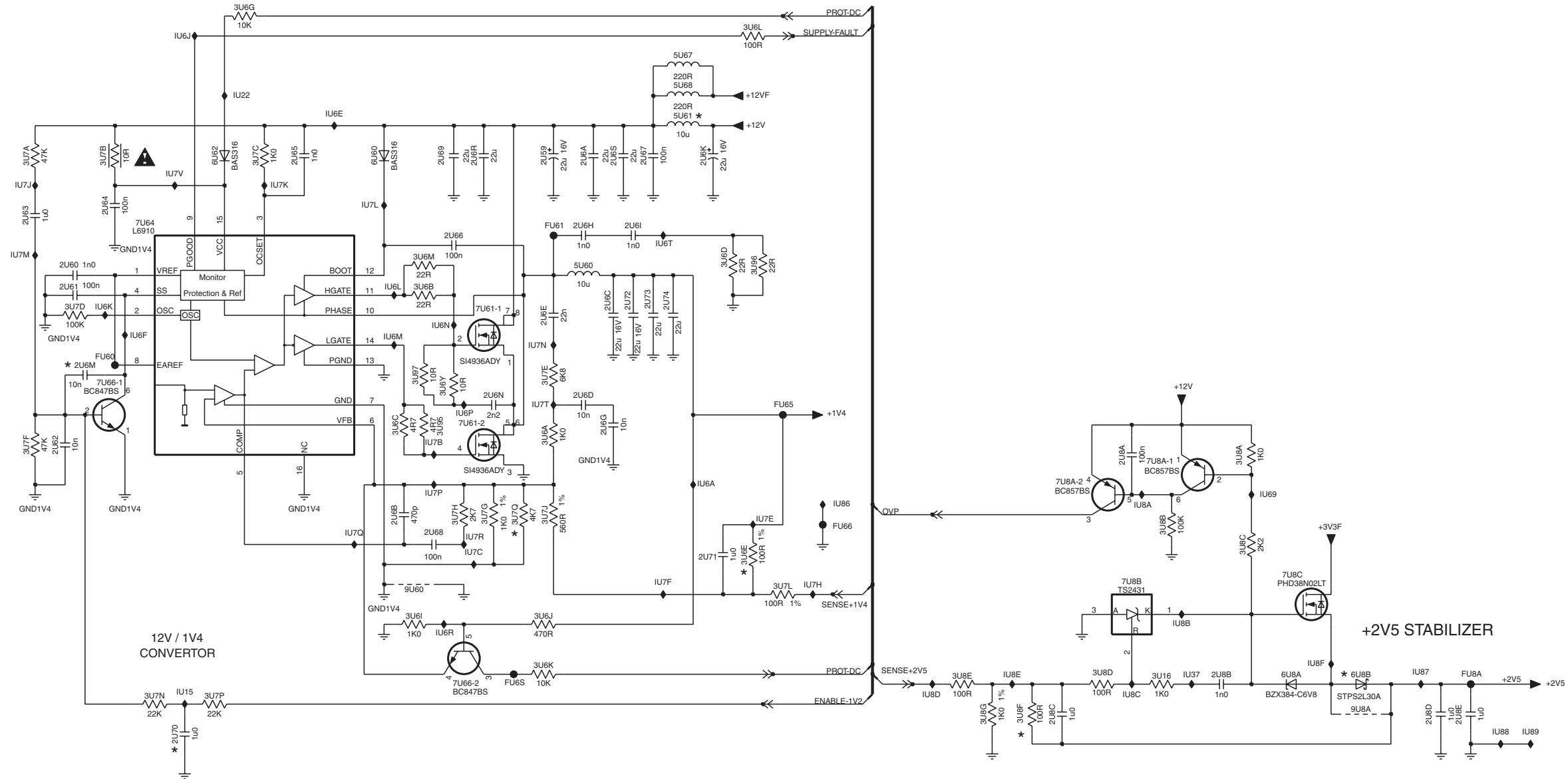
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SSB: DC / DC

B02C DC/DC

B02C

PNX 5050 SUPPLY



- 2U59 B5
- 2U60 C2
- 2U61 C2
- 2U62 D2
- 2U63 C1
- 2U64 C2
- 2U65 B3
- 2U66 C4
- 2U67 B6
- 2U68 E4
- 2U69 B4
- 2U6A B5
- 2U6B E4
- 2U6C C5
- 2U6D D5
- 2U6E D5
- 2U6G D5
- 2U6H C5
- 2U6I C6
- 2U6K B6
- 2U6M D2
- 2U6N D5
- 2U6R B5
- 2U6S B6
- 2U70 G2
- 2U71 E6
- 2U72 C6
- 2U73 C6
- 2U74 C6
- 2U8A E9
- 2U8B F10
- 2U8C F9
- 2U8D F11
- 2U8E F12
- 3U16 F9
- 3U6A D5
- 3U6B C4
- 3U6C D4
- 3U6D C6
- 3U6E E6
- 3U6G A3
- 3U6I F4
- 3U6J F5
- 3U6K F5
- 3U6L A7
- 3U6M C4
- 3U6Y D4
- 3U7A B1
- 3U7B B2
- 3U7C B3
- 3U7D C2
- 3U7E D5
- 3U7F D1
- 3U7G E5
- 3U7H E4
- 3U7J E5
- 3U7L E7
- 3U7N F2
- 3U7P F3
- 3U7Q E5
- 3U8A E10
- 3U8B E9
- 3U8C E10
- 3U8D F9
- 3U8E F8
- 3U8F F8
- 3U8G F8
- 3U95 D4
- 3U96 C7
- 3U97 C5
- 5U60 C5
- 5U61 B6
- 5U67 B6
- 5U68 B6
- 6U60 B4
- 6U62 B3
- 6U8A F10
- 6U8B F11
- 7U61-1 C5
- 7U61-2 D5
- 7U64 C2
- 7U66-1 D2
- 7U66-2 F4
- 7U8A-1 E10
- 7U8A-2 E9
- 7U8B E9
- 7U8C E10
- 9U60 E4
- 9U8A F11
- FU60 D2
- FU61 C5
- FU65 D7
- FU66 E7
- FU6S F5
- FU8A F12
- IU15 F2
- IU22 B3
- IU37 F10
- IU69 E10
- IU6A E6
- IU6E B4
- IU6F D2
- IU6J B2
- IU6K C2
- IU6L C4
- IU6M D4
- IU6N D4
- IU6P D4
- IU6R F4
- IU6T C6
- IU6V D4
- IU7B D4
- IU7C E5
- IU7E E7
- IU7F E6
- IU7H E7
- IU7J C1
- IU7K C3
- IU7L C4
- IU7M C1
- IU7N D5
- IU7P E4
- IU7Q E4
- IU7R E5
- IU7T D5
- IU7V B2
- IU86 E7
- IU87 F11
- IU88 F12
- IU89 F12
- IU8A E9
- IU8B F10
- IU8C F9
- IU8D F8
- IU8E F8
- IU8F F11

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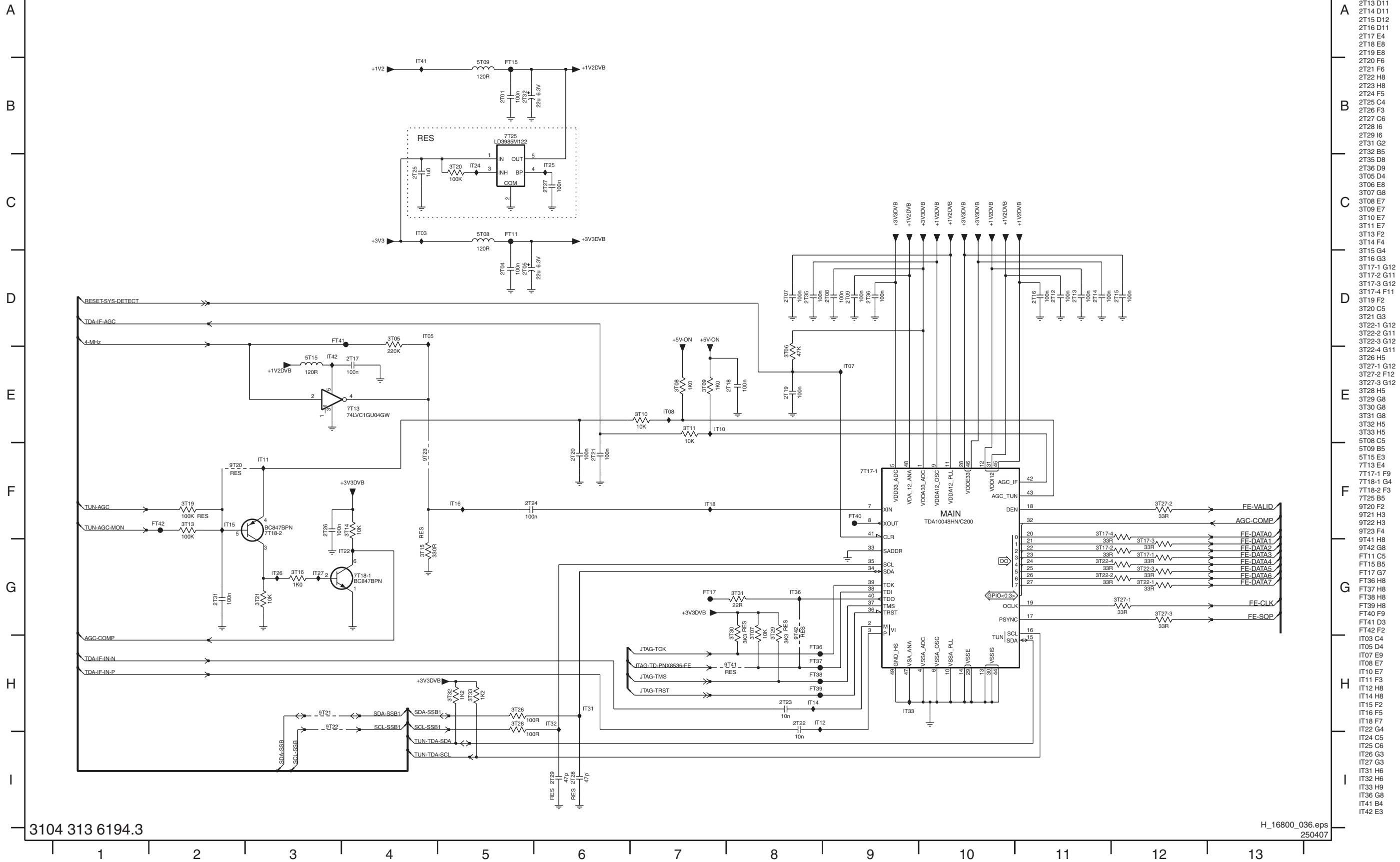
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SSB: Channel Decoder

B03A CHANNEL DECODER

B03A



- 2T01 B5
- 2T04 D5
- 2T05 D5
- 2T07 D8
- 2T08 D9
- 2T09 D9
- 2T12 D11
- 2T13 D11
- 2T14 D11
- 2T15 D12
- 2T16 D11
- 2T17 E4
- 2T18 E8
- 2T19 E8
- 2T20 F6
- 2T21 F6
- 2T22 H8
- 2T23 H8
- 2T24 F5
- 2T25 C4
- 2T26 F3
- 2T27 C6
- 2T28 I6
- 2T29 I6
- 2T31 G2
- 2T32 B5
- 2T36 D9
- 3T05 D4
- 3T06 E8
- 3T07 G8
- 3T08 E7
- 3T09 E7
- 3T10 E7
- 3T11 E7
- 3T13 F2
- 3T14 F4
- 3T15 G4
- 3T16 G3
- 3T17-1 G12
- 3T17-2 G11
- 3T17-3 G12
- 3T17-4 F11
- 3T19 F2
- 3T20 C5
- 3T21 G3
- 3T22-1 G12
- 3T22-2 G11
- 3T22-3 G12
- 3T22-4 G11
- 3T26 H5
- 3T27-1 G12
- 3T27-2 F12
- 3T27-3 G12
- 3T28 H5
- 3T29 G8
- 3T30 G8
- 3T31 G8
- 3T32 H5
- 3T33 H5
- 5T08 C5
- 5T09 B5
- 5T15 E3
- 7T13 E4
- 7T17-1 F9
- 7T18-1 G4
- 7T18-2 F3
- 7T25 B5
- 9T20 F2
- 9T21 H3
- 9T22 H3
- 9T23 F4
- 9T41 H8
- 9T42 G8
- FT11 C5
- FT15 B5
- FT17 G7
- FT36 H8
- FT37 H8
- FT38 H8
- FT39 H8
- FT40 F9
- FT41 D3
- FT42 F2
- IT03 C4
- IT05 D4
- IT07 E9
- IT08 E7
- IT10 E7
- IT11 F3
- IT12 H8
- IT14 H8
- IT15 F2
- IT16 F5
- IT18 F7
- IT22 G4
- IT24 C5
- IT25 C6
- IT26 G3
- IT27 C3
- IT31 H6
- IT32 H6
- IT33 H9
- IT36 G8
- IT41 B4
- IT42 E3

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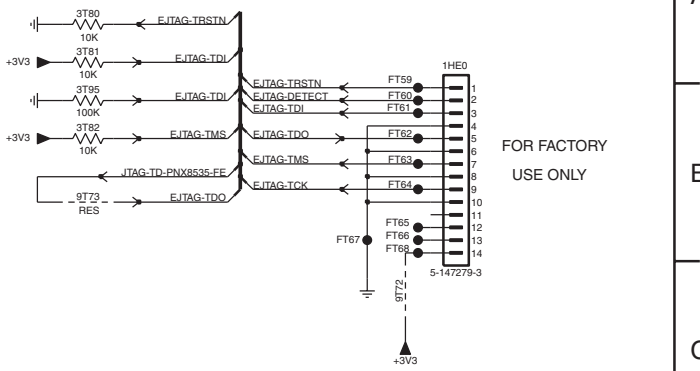
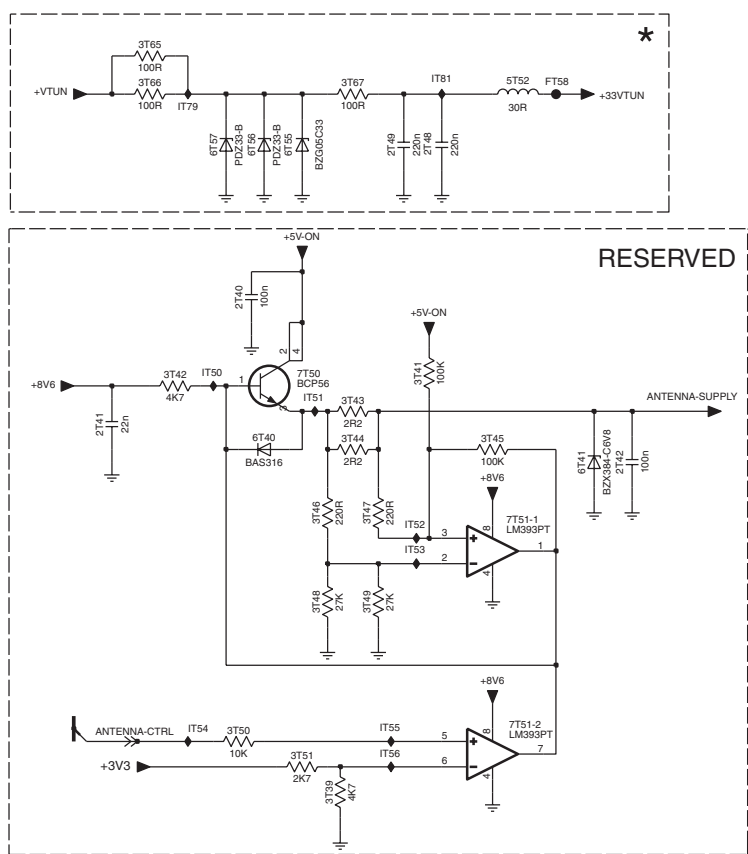
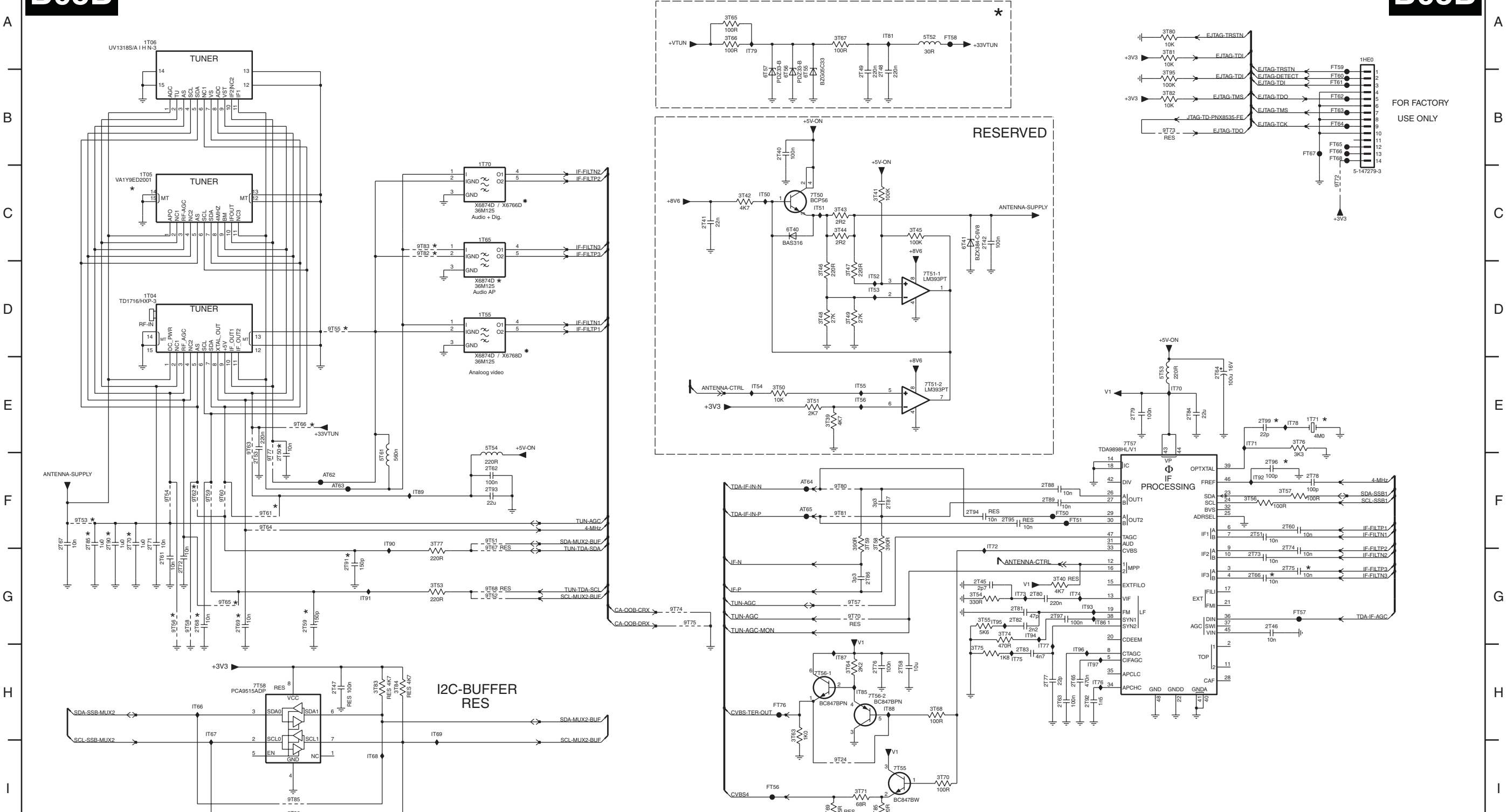
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SSB: Main Tuner

1HE0 A14	1T71 E14	2T48 B9	2T60 F14	2T67 F1	2T74 G14	2T81 G11	2T88 F11	2T95 F11	3T42 C8	3T49 D9	3T57 F14	3T67 A9	3T76 E14	3T85 I9	6T41 C10	7T55 I10	9T52 G5	9T59 F2	9T66 E3	9T75 G7	9T86 I3	FT56 I8	FT63 B14	IT50 C8	IT66 H2	IT73 G11	IT81 A9	IT91 G4	
1T04 D2	2T40 B8	2T49 B9	2T61 G2	2T68 G2	2T75 G14	2T82 G11	2T89 F11	2T96 F13	3T43 C9	3T50 E8	3T58 F9	3T68 H10	3T77 F5	3T85 B12	6T55 B9	7T56-1 H9	9T53 F1	9T60 F2	9T67 G5	9T77 F3	9T86 I3	AT62 F4	FT57 G14	FT64 B14	IT51 C9	IT67 H2	IT74 G11	IT85 H9	IT92 F13
1T05 C2	2T41 C8	2T50 F3	2T62 F5	2T69 G3	2T76 H9	2T83 H11	2T90 F1	2T97 G11	3T44 C9	3T51 E9	3T59 F9	3T69 I9	3T80 A12	5T52 A10	6T56 B8	7T56-2 H9	9T54 F2	9T61 F3	9T68 G5	9T78 F9	9T86 I3	AT63 F4	FT58 A10	FT65 B14	IT52 D9	IT68 I4	IT75 H11	IT86 G12	IT93 G11
1T06 A2	2T42 C10	2T51 F13	2T63 H11	2T70 F2	2T77 H11	2T84 E13	2T91 G4	2T99 E13	3T45 C10	3T53 G5	3T63 H8	3T70 I10	3T81 A12	5T53 E12	6T57 B8	7T57 E12	9T55 D4	9T62 F2	9T70 G9	9T81 F9	9T86 I3	AT64 F9	FT59 A14	FT66 B14	IT53 D9	IT69 H5	IT76 H12	IT87 H9	IT94 G11
1T55 D5	2T45 G10	2T53 F3	2T64 E13	2T71 F2	2T78 F14	2T85 F1	2T92 H12	3T39 E9	3T46 D9	3T54 G10	3T64 H9	3T71 I9	3T82 B12	5T54 E5	7T50 C9	9T56 G2	9T63 E3	9T72 C14	9T82 C5	9T86 I3	AT65 F9	FT60 B14	FT67 B14	IT54 E8	IT70 E12	IT77 H11	IT88 H9	IT95 G11	
1T65 C5	2T46 G13	2T58 H10	2T65 H11	2T72 G2	2T79 E12	2T86 G9	2T93 F5	3T40 G11	3T47 D9	3T55 G10	3T65 A8	3T74 G11	3T83 H4	5T51 F4	7T51-1 D10	9T57 G9	9T64 F3	9T73 B12	9T83 C5	9T86 I3	AT66 F11	FT61 B14	FT68 B14	IT55 E9	IT71 E13	IT78 E14	IT89 F5	IT96 H11	
1T70 C5	2T47 H4	2T59 G3	2T66 G13	2T73 G13	2T80 G11	2T87 F9	2T94 F10	3T41 C9	3T48 D9	3T56 F13	3T66 A8	3T75 H10	3T84 H4	6T40 C8	7T51-2 E10	9T51 F5	9T58 G2	9T65 G2	9T74 G7	9T85 I3	FT51 F11	FT62 B14	FT76 H8	IT56 E9	IT72 F10	IT79 A8	IT90 F4	IT97 H12	

B03B MAIN TUNER

B03B

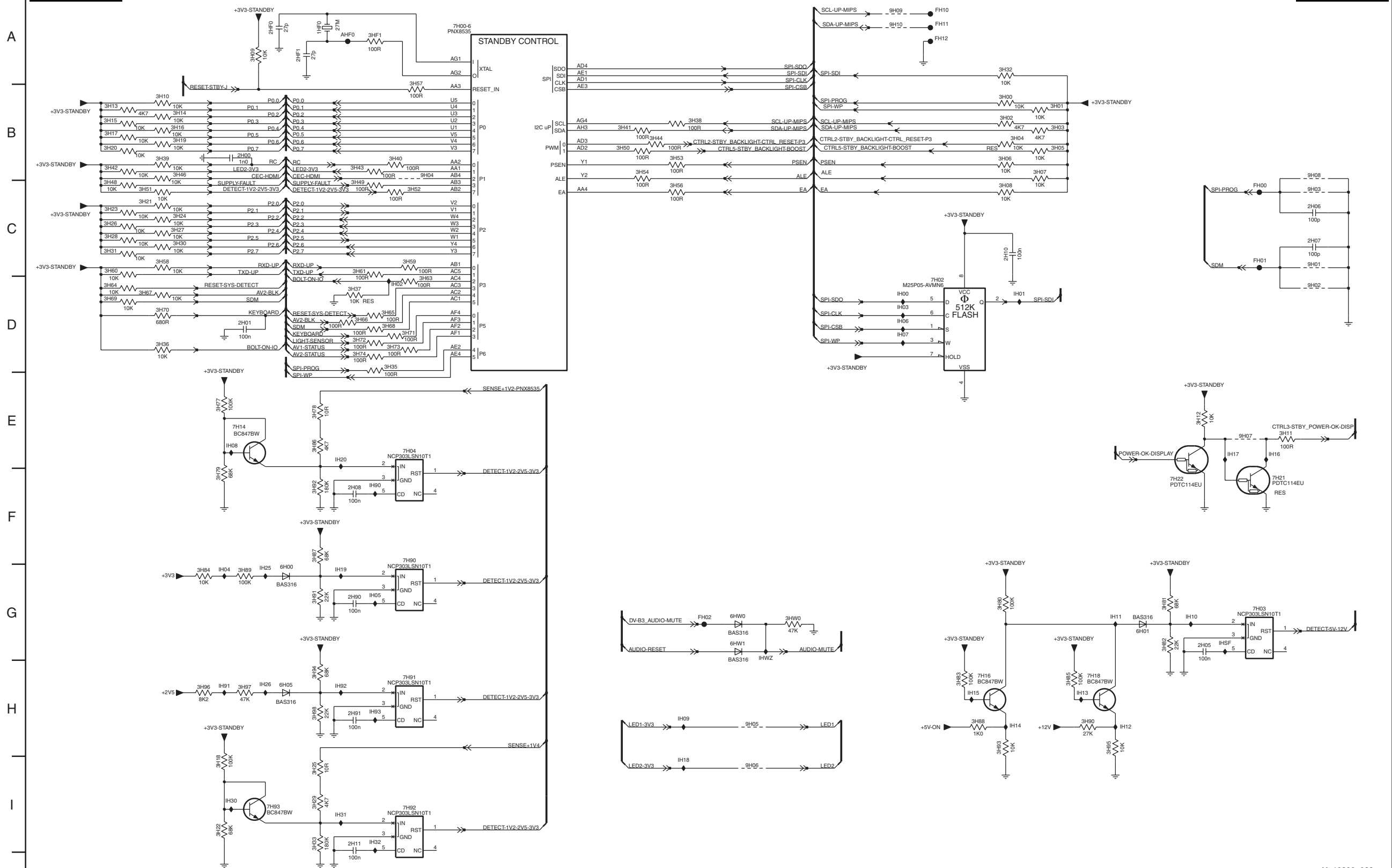


FOR FACTORY USE ONLY

PNX 8535: Standby Controller

B04A PNX 8535: STANDBY CONTROLLER

B04A



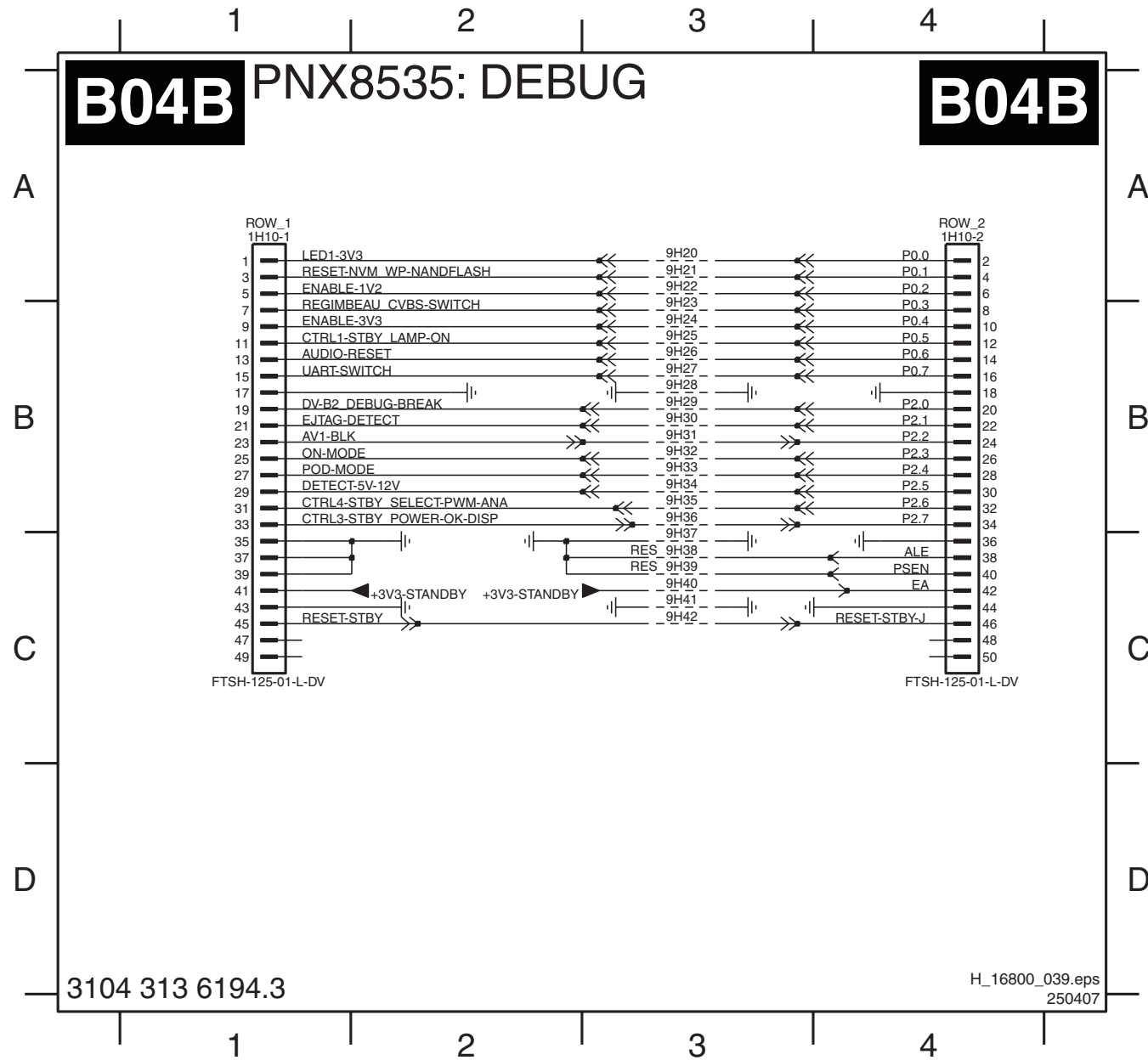
- 1H0 A3
- 2H00 B3
- 2H01 D3
- 2H05 G13
- 2H06 C14
- 2H07 C14
- 2H08 F4
- 2H10 C11
- 2H11 I4
- 2H90 G4
- 2H91 H4
- 2H90 A3
- 2H90 C14
- 2H90 F4
- 2H92 I4
- 2H93 I3
- 2H91 C14
- 2H92 D14
- 2H90 C14
- 2H90 B5
- 2H90 H8
- 2H90 I8
- 2H90 E13
- 2H90 B14
- 2H90 A9
- 2H90 A9
- 2H90 A4
- 2H90 C13
- 2H90 G7
- 2H90 A10
- 2H90 A10
- 2H90 A10
- 2H90 D10
- 2H90 D11
- 2H90 D4
- 2H90 A4
- 2H90 G4
- 2H90 D10
- 2H90 D10
- 2H90 E3
- 2H90 G13
- 2H90 G12
- 2H90 H12
- 2H90 H11
- 2H90 H11
- 2H90 G4
- 2H90 D10
- 2H90 E3
- 2H90 G13
- 2H90 G12
- 2H90 H12
- 2H90 H11
- 2H90 G4
- 2H90 E4
- 2H90 G3
- 2H90 H3
- 2H90 I3
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- 2H90 F4
- 2H90 H2
- 2H90 H4
- 2H90 H4
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- 2H90 G13
- 2H90 E4

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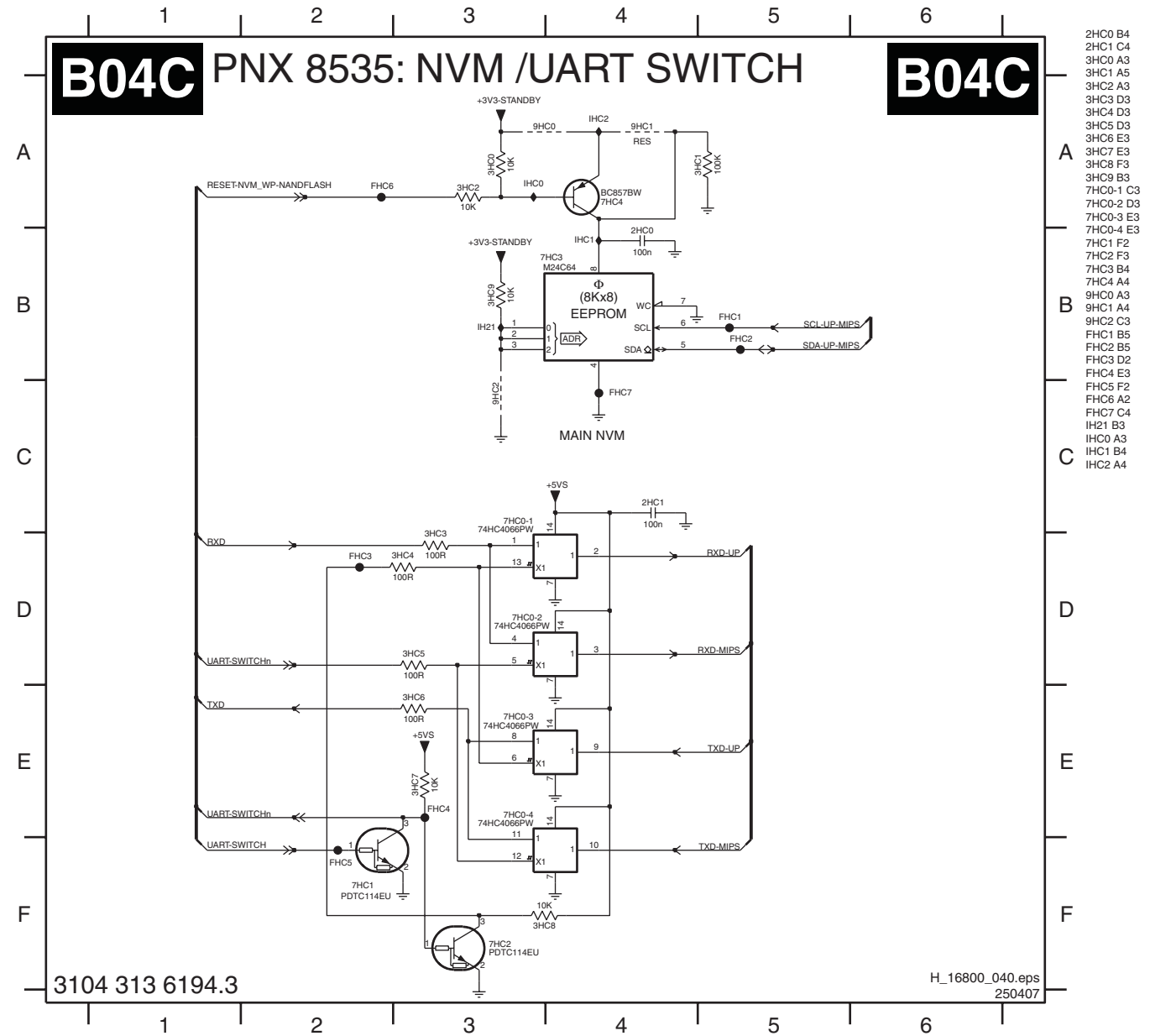
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**PNX 8535: Debug**

9H20 A3	9H23 B3	9H26 B3	9H29 B3	9H32 B3	9H35 B3	9H38 C3	9H41 C3	9H44 C3
9H21 A3	9H24 B3	9H27 B3	9H30 B3	9H33 B3	9H36 B3	9H39 C3	9H42 C3	
9H22 A3	9H25 B3	9H28 B3	9H31 B3	9H34 B3	9H37 C3	9H40 C3	9H43 C3	



**PNX 8535: NVM / UART Switch**

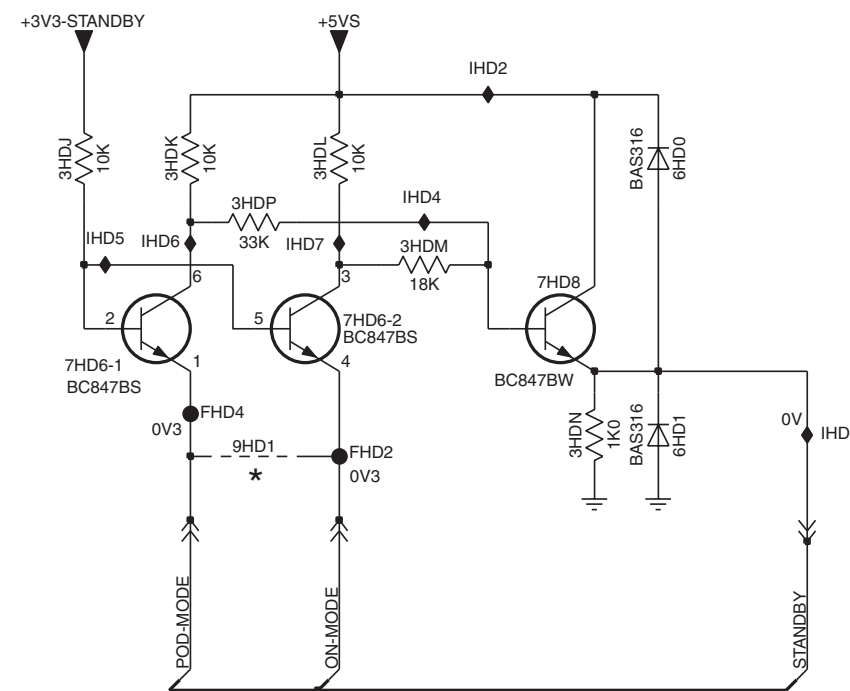
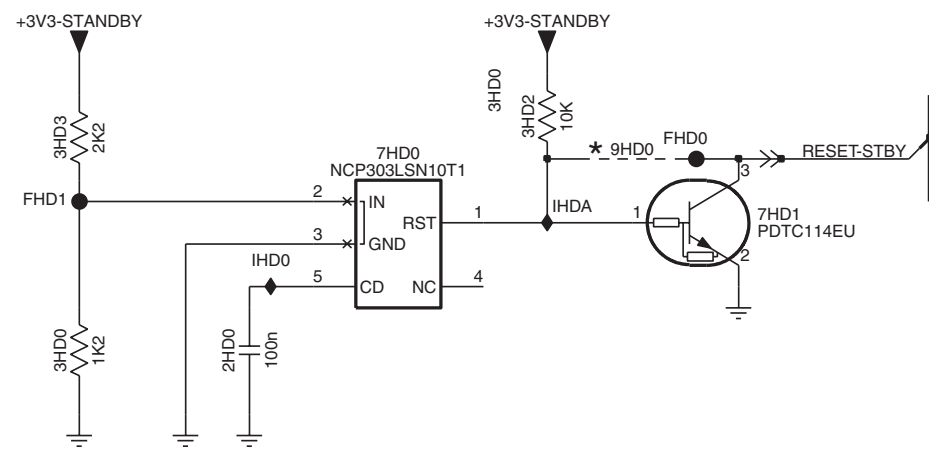


- 2HC0 B4
- 2HC1 C4
- 3HC0 A3
- 3HC1 A5
- 3HC2 A3
- 3HC3 D3
- 3HC4 D3
- 3HC5 D3
- 3HC6 E3
- 3HC7 E3
- 3HC8 F3
- 3HC9 B3
- 7HC0-1 C3
- 7HC0-2 D3
- 7HC0-3 E3
- 7HC0-4 E3
- 7HC1 F2
- 7HC2 F3
- 7HC3 B4
- 7HC4 A4
- 9HC0 A3
- 9HC1 A4
- 9HC2 C3
- FHC1 B5
- FHC2 B5
- FHC3 D2
- FHC4 E3
- FHC5 F2
- FHC6 A2
- FHC7 C4
- IHC0 A3
- IHC1 B4
- IHC2 A4

PNX 8535: Miscellaneous

**B04D** PNX 8535: MISCELLANEOUS

**B04D**



- 2HD0 D2
- 3HD0 D2
- 3HD2 C3
- 3HD3 C2
- 3HDJ C5
- 3HDK C6
- 3HDL C6
- 3HDM C6
- 3HDN C7
- 3HDP C6
- 6HD0 C7
- 6HD1 C7
- 7HD0 C3
- 7HD1 C4
- 7HD6-1 C5
- 7HD6-2 C6
- 7HD8 C7
- 9HD0 C3
- 9HD1 D6
- FHD0 C4
- FHD1 C1
- FHD2 D6
- FHD4 C6
- IHD0 C2
- IHD2 B7
- IHD4 C6
- IHD5 C5
- IHD6 C5
- IHD7 C6
- IHDA C3
- IHDE C8

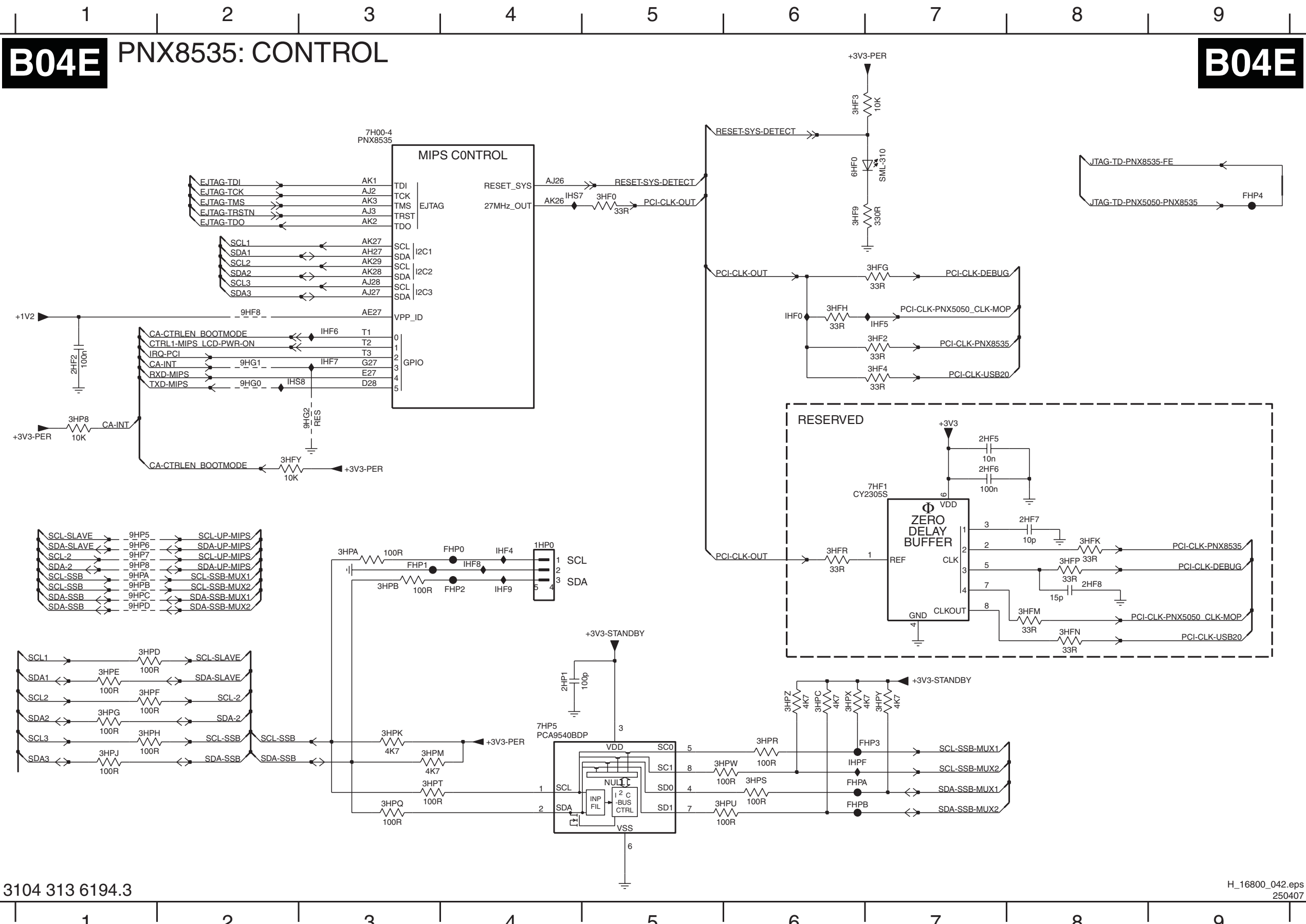
PNX 8535: Control

**B04E** PNX8535: CONTROL

**B04E**

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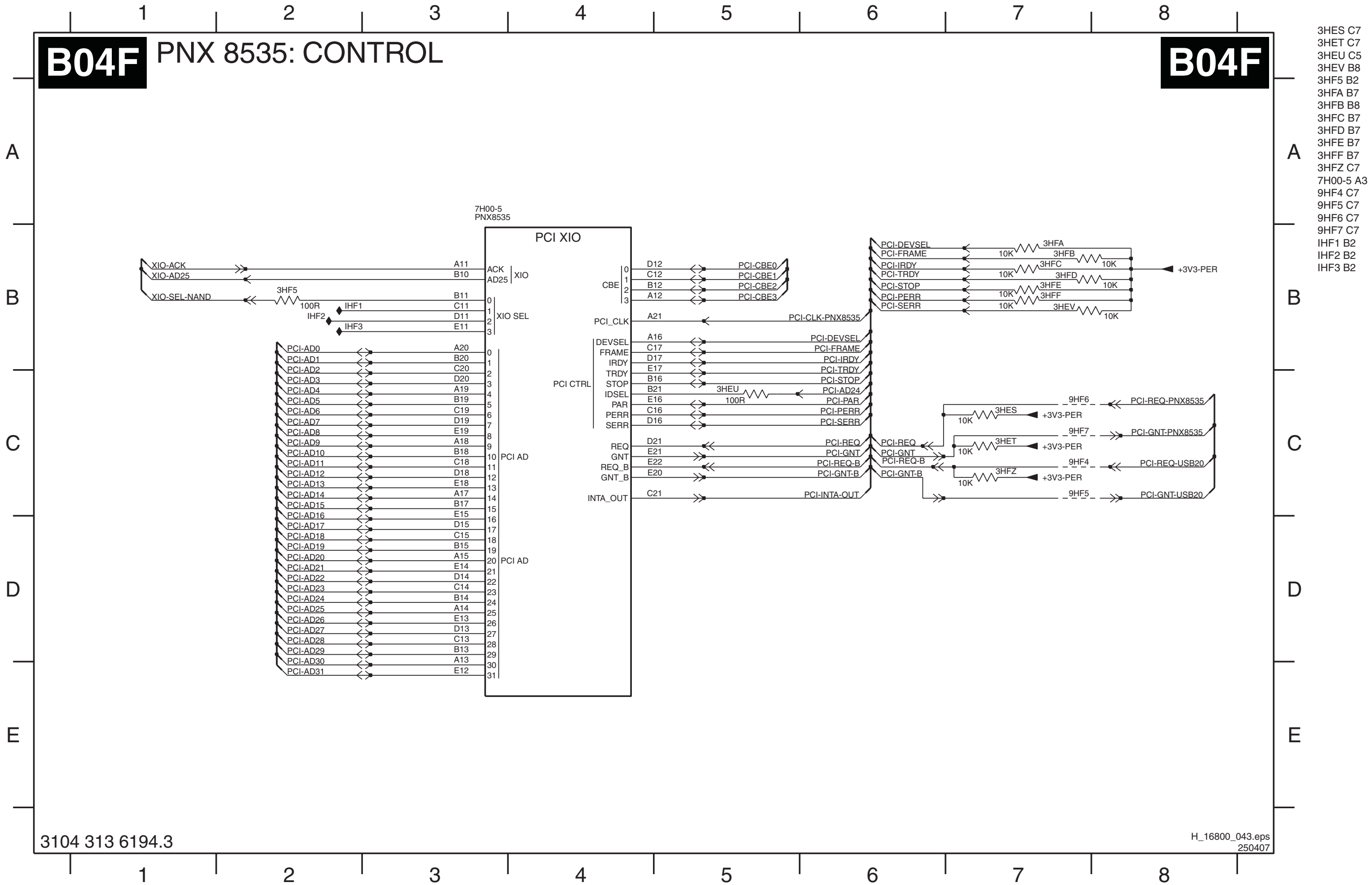
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- 2HP1 E4
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- 3HF2 C7
- 3HF3 A6
- 3HF4 C7
- 3HF9 B6
- 3HF8 B7
- 3HFH B6
- 3HFK D8
- 3HFM E8
- 3HFN E8
- 3HFP D8
- 3HFR D6
- 3HFX C2
- 3HP8 C1
- 3HPA D3
- 3HPB D3
- 3HPC E6
- 3HPD E1
- 3HPE E1
- 3HPF E1
- 3HPG E1
- 3HPH E1
- 3HPJ F1
- 3HPK E3
- 3HPM F3
- 3HPQ F3
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- 3HPS F6
- 3HPT F3
- 3HPU F6
- 3HPW F6
- 3HPX E6
- 3HPY E7
- 3HPZ E6
- 6HF0 A6
- 7H00-4 A3
- 7HF1 D7
- 7HP5 E4
- 9HF8 B2
- 9HG0 C2
- 9HG1 C2
- 9HG2 C3
- 9HP5 D1
- 9HP6 D1
- 9HP7 D1
- 9HP8 D1
- 9HPA D1
- 9HPB D1
- 9HPC D1
- 9HPD D1
- FHP0 D4
- FHP1 D3
- FHP2 D4
- FHP3 E7
- FHP4 B9
- FHPA F6
- FHPB F6
- IHF0 B6
- IHF4 D4
- IHF5 B7
- IHF6 C3
- IHF7 C3
- IHF8 D4
- IHF9 D4
- IHPF F6
- IHS7 B4
- IHS8 C2



PNX 8535: Control

# B04F PNX 8535: CONTROL

# B04F

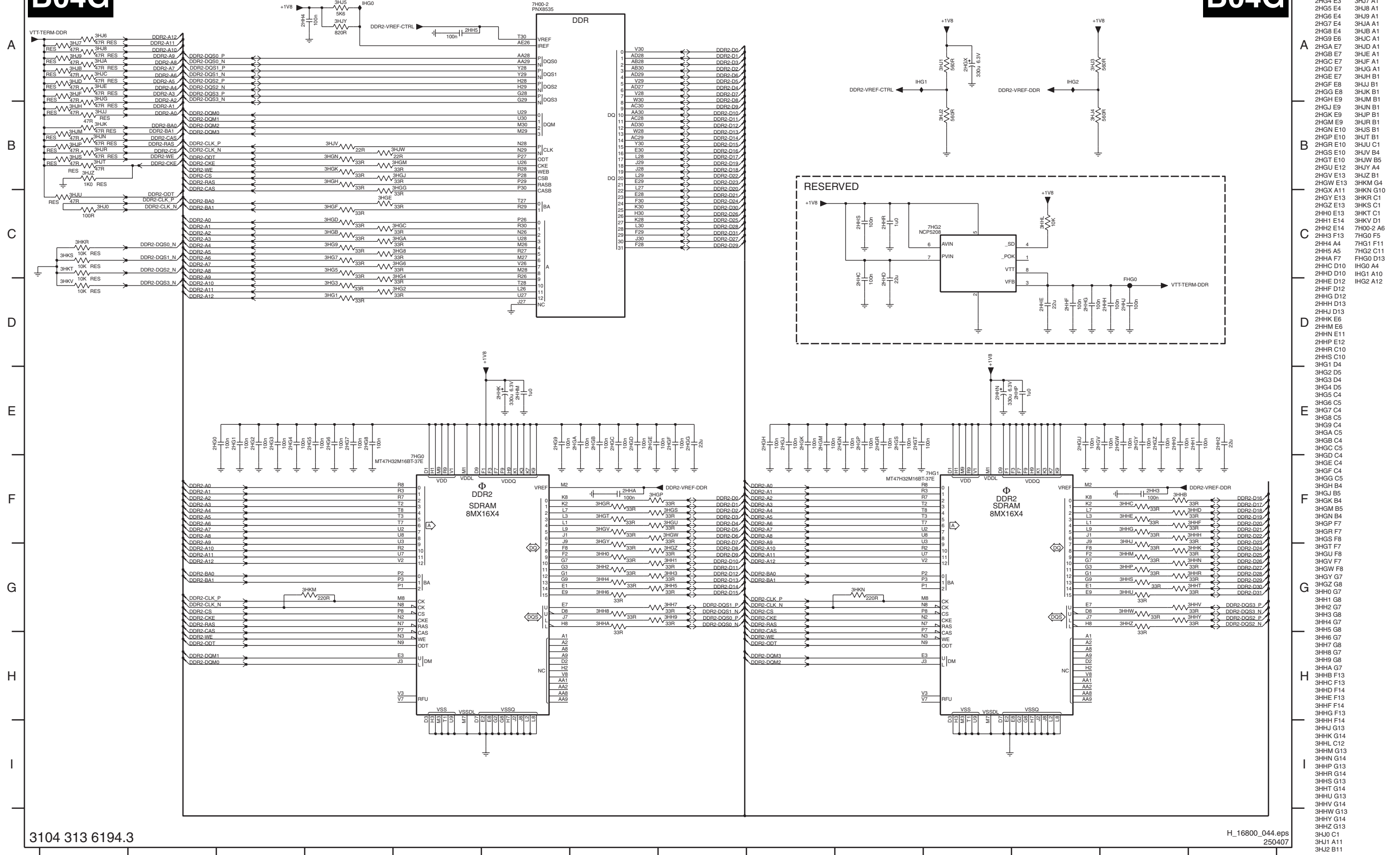


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- 3HEU C5
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- 3HFC B7
- 3HFD B7
- 3HFE B7
- 3HFF B7
- 3HFZ C7
- 7H00-5 A3
- 9HF4 C7
- 9HF5 C7
- 9HF6 C7
- 9HF7 C7
- IHF1 B2
- IHF2 B2
- IHF3 B2

PNX 8535: SDRAM

B04G PNX 8535: SDRAM

B04G



- 2HG0 E3
- 2HG1 E3
- 2HG2 E3
- 2HG3 E3
- 2HG4 E3
- 2HG5 E4
- 2HG6 E4
- 2HG7 E4
- 2HG8 E4
- 2HG9 E6
- 2HGA E7
- 2HGB E7
- 2HGC E7
- 2HGD E7
- 2HGE E7
- 2HGF E8
- 2HGG E8
- 2HGH E9
- 2HGJ E9
- 2HGK E9
- 2HGM E9
- 2HGN E10
- 2HGP E10
- 2HGR E10
- 2HGS E10
- 2HGT E10
- 2HGU E12
- 2HGV E13
- 2HGW E13
- 2HGX A11
- 2HGY E13
- 2HGZ E13
- 2HH0 E13
- 2HH1 E14
- 2HH2 E14
- 2HH3 F13
- 2HH4 A4
- 2HH5 A5
- 2HH6 F7
- 2HH7 E11
- 2HH8 E12
- 2HH9 C10
- 2HHA C4
- 2HHB D5
- 2HH3 D4
- 2HH4 D5
- 2HH5 C4
- 2HH6 C5
- 2HH7 C4
- 2HH8 C5
- 2HH9 C5
- 2HHA C5
- 2HHB C4
- 2HH3 B5
- 2HH4 B4
- 2HH5 B5
- 2HH6 B4
- 2HH7 F7
- 2HH8 F7
- 2HH9 F8
- 2HHA F7
- 2HHB F8
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- 2HH4 G7
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PNX 8535: Audio

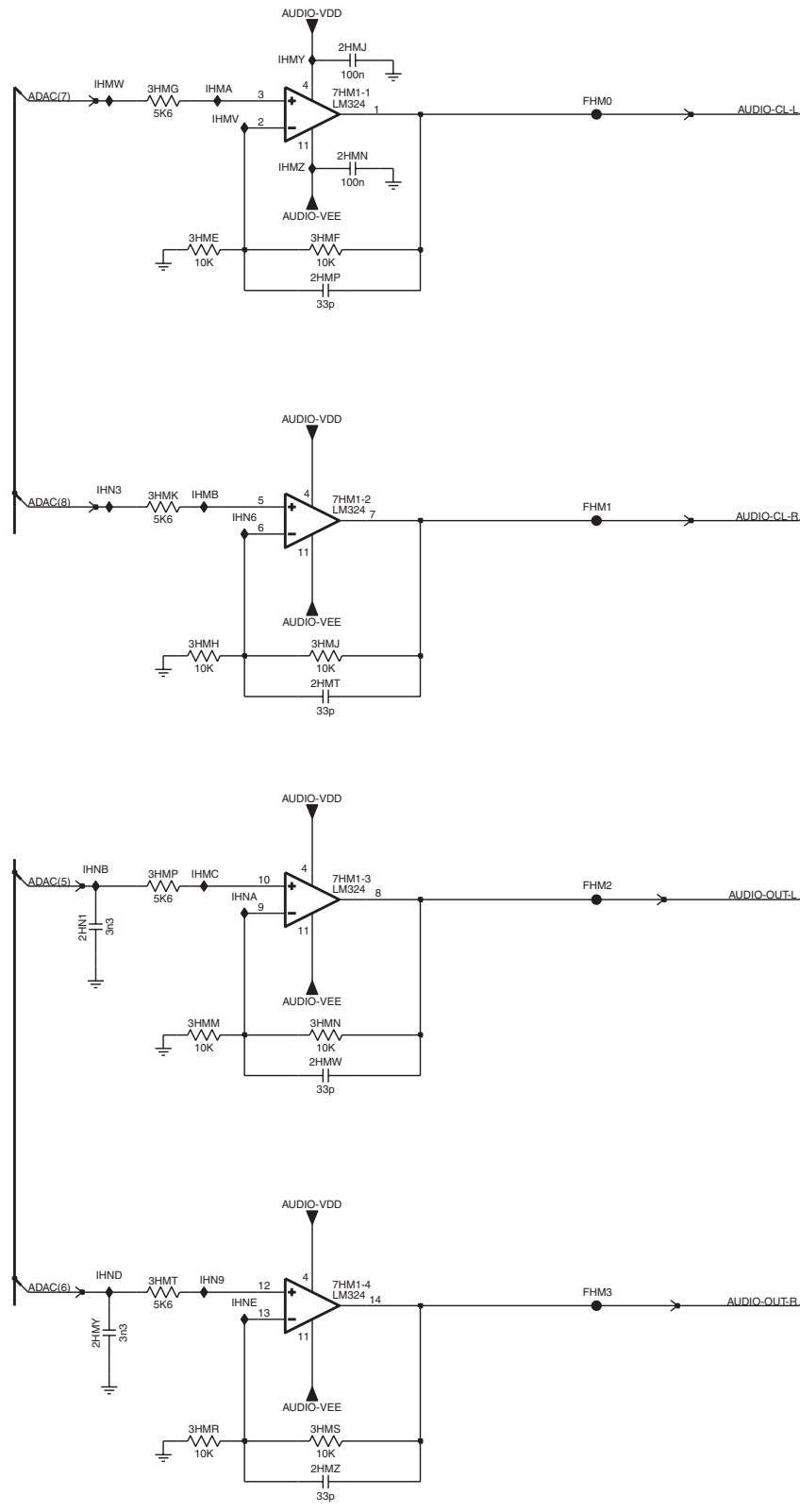
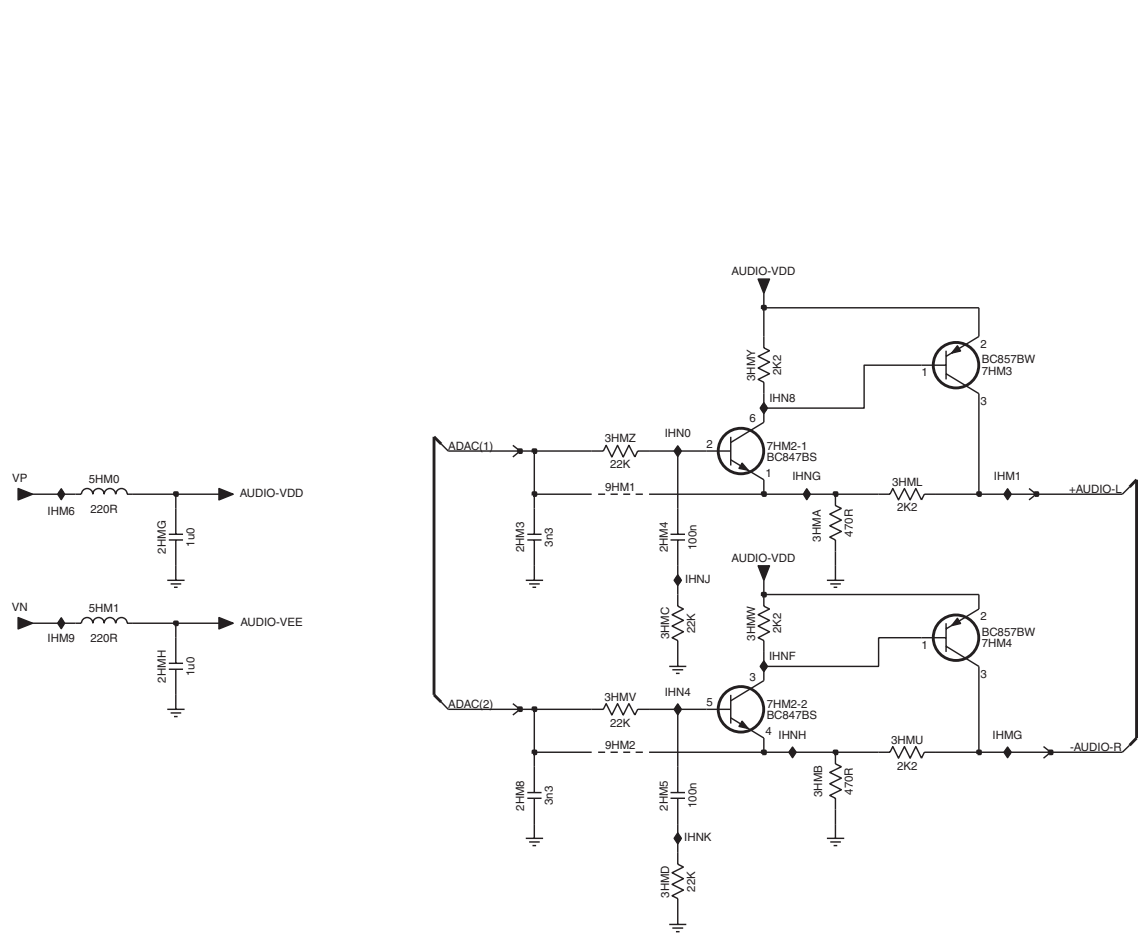
B041

PNX 8535: AUDIO

B041

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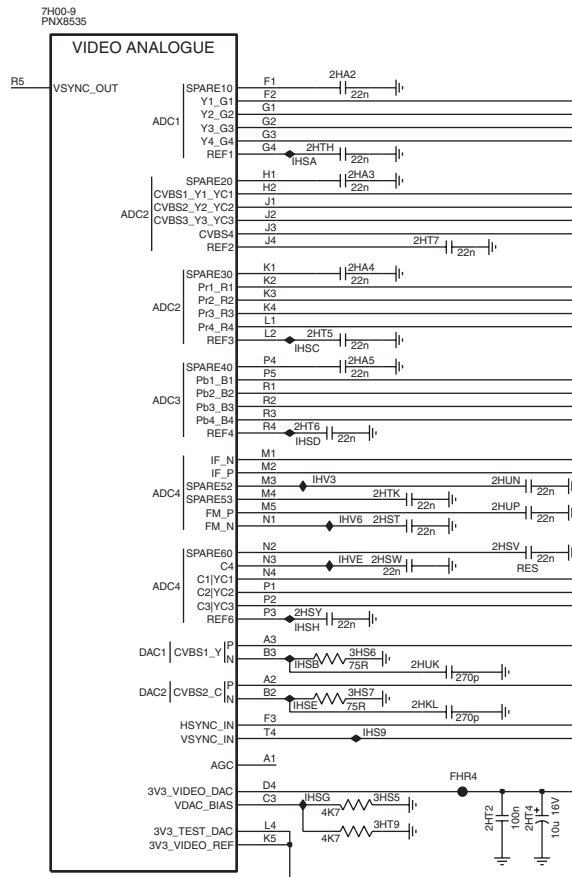
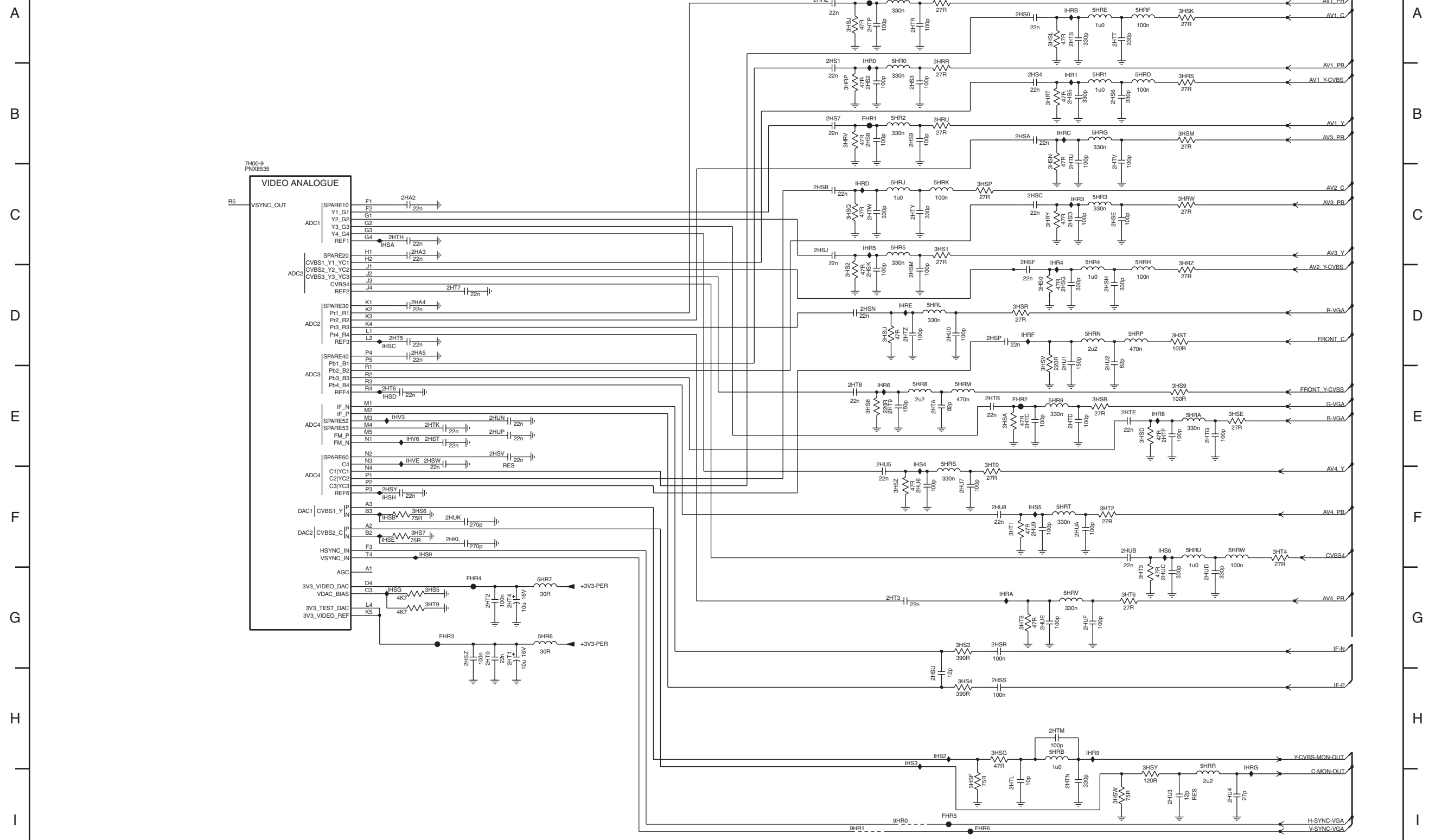
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- 2HMP C9
- 2HMT E9
- 2HMW G9
- 2HMY H8
- 2HMZ I9
- 2HN1 F8
- 3HMA D5
- 3HMB E5
- 3HMC D4
- 3HMD E4
- 3HME B9
- 3HMF B9
- 3HMG B9
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- 3HMT H9
- 3HMU E6
- 3HMV E4
- 3HMW D5
- 3HMY C5
- 3HMZ C4
- 5HM0 C2
- 5HM1 D2
- 7HM1-1 B9
- 7HM1-2 D9
- 7HM1-3 F9
- 7HM1-4 H9
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- 7HM3 C6
- 7HM4 D6
- 9HM1 D4
- 9HM2 E4
- FHM0 B11
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- FHM2 F11
- FHM3 H11
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- IHM6 D1
- IHM9 D1
- IHMA B9
- IHMB D9
- IHMC F9
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- IHMV B9
- IHMW B8
- IHMY A9
- IHMZ B9
- IHN0 C5
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- IHN4 E5
- IHN6 D9
- IHN8 C5
- IHN9 H9
- IHNA F9
- IHNB F8
- IHND H8
- IHNE H9
- IHNF D5
- IHNG C5
- IHNH E5
- IHNJ D5
- IHNK E5

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PNX 8535: Analogue AV

B04K PNX 8535: ANALOGUE AV

B04K



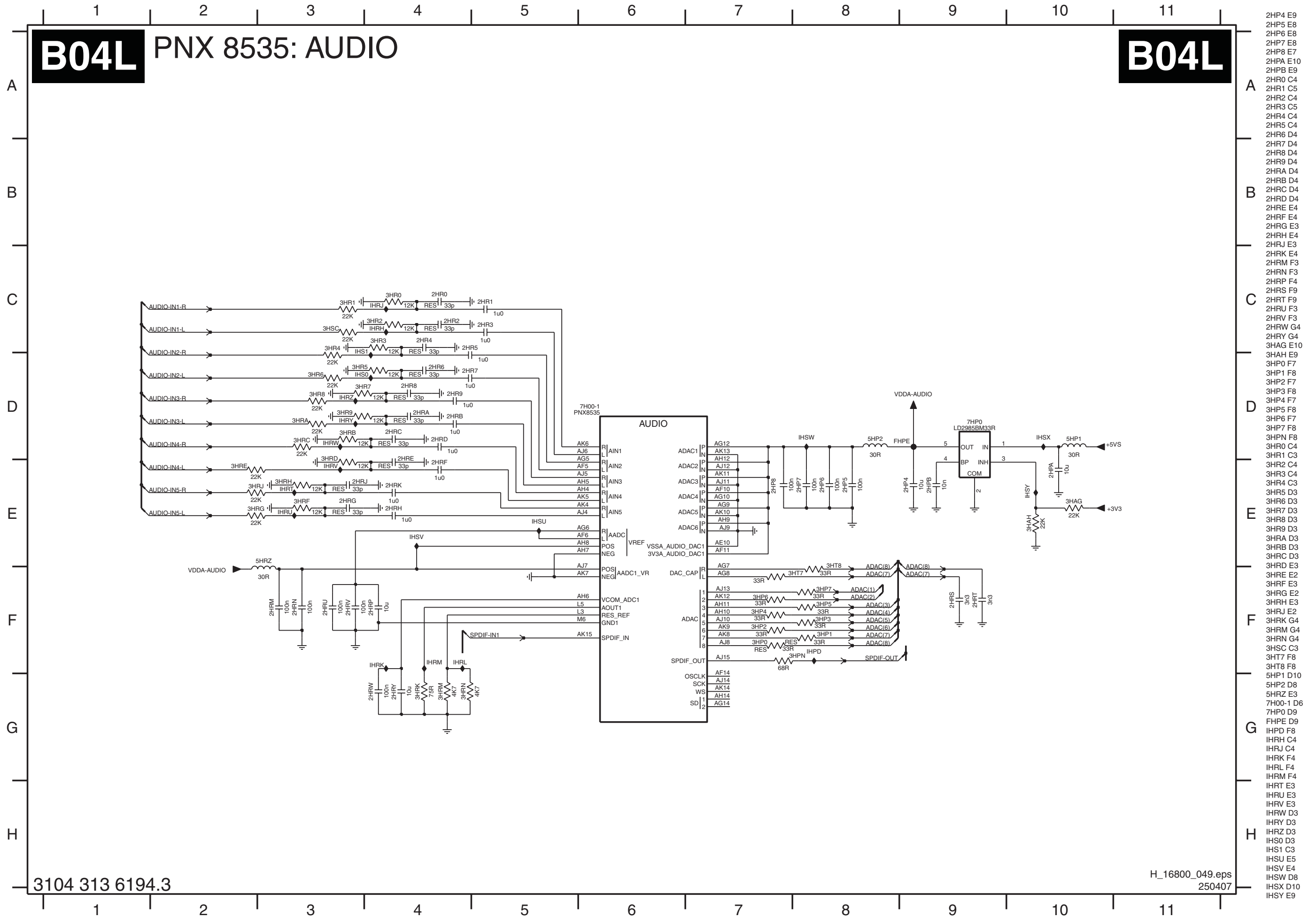
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- 3HTR G C9
- 3HTR H C9
- 3HTR I C9
- 3HTR J C9
- 3HTR K C9
- 3HTR L C9
- 3HTR M C9
- 3HTR N C9
- 3HTR O C9
- 3HTR P C9
- 3HTR Q C9
- 3HTR R C9
- 3HTR S C9
- 3HTR T C9
- 3HTR U C9
- 3HTR V C9
- 3HTR W C9
- 3HTR X C9
- 3HTR Y C9
- 3HTR Z C9
- 3HTRA C9
- 3HTRB C9
- 3HTRC C9
- 3HTRD C9
- 3HTR E C9
- 3HTR F C9
- 3HTR G C9
- 3HTR H C9
- 3HTR I C9
- 3HTR J C9
- 3HTR K C9
- 3HTR L C9
- 3HTR M C9
- 3HTR N C9
- 3HTR O C9
- 3HTR P C9
- 3HTR Q C9
- 3HTR R C9
- 3HTR S C9
- 3HTR T C9
- 3HTR U C9
- 3HTR V C9
- 3HTR W C9
- 3HTR X C9
- 3HTR Y C9
- 3HTR Z C9
- 3HTRA C9
- 3HTRB C9
- 3HTRC C9
- 3HTRD C9
- 3HTR E C9
- 3HTR F C9
- 3HTR G C9
- 3HTR H C9
- 3HTR I C9
- 3HTR J C9
- 3HTR K C9
- 3HTR L C9
- 3HTR M C9
- 3HTR N C9
- 3HTR O C9
- 3HTR P C9
- 3HTR Q C9
- 3HTR R C9
- 3HTR S C9
- 3HTR T C9
- 3HTR U C9
- 3HTR V C9
- 3HTR W C9
- 3HTR X C9
- 3HTR Y C9
- 3HTR Z C9
- 3HTRA C9
- 3HTRB C9
- 3HTRC C9
- 3HTRD C9
- 3HTR E C9
- 3HTR F C9
- 3HTR G C9
- 3HTR H C9
- 3HTR I C9
- 3HTR J C9
- 3HTR K C9
- 3HTR L C9
- 3HTR M C9
- 3HTR N C9
- 3HTR O C9
- 3HTR P C9
- 3HTR Q C9
- 3HTR R C9
- 3HTR S C9
- 3HTR T C9
- 3HTR U C9
- 3HTR V C9
- 3HTR W C9
- 3HTR X C9
- 3HTR Y C9
- 3HTR Z C9

PNX 8535: Audio

B04L

PNX 8535: AUDIO

B04L



- 2HP4 E9
- 2HP5 E8
- 2HP6 E8
- 2HP7 E8
- 2HP8 E7
- 2HPA E10
- 2HPB E9
- 2HR0 C4
- 2HR1 C5
- 2HR2 C4
- 2HR3 C5
- 2HR4 C4
- 2HR5 C4
- 2HR6 D4
- 2HR7 D4
- 2HR8 D4
- 2HR9 D4
- 2HRA D4
- 2HRB D4
- 2HRC D4
- 2HRD D4
- 2HRE E4
- 2HRF E4
- 2HRG E3
- 2HRH E4
- 2HRJ E3
- 2HRK E4
- 2HRM F3
- 2HRN F3
- 2HRP F4
- 2HRS F9
- 2HRT F9
- 2HRU F3
- 2HRV F3
- 2HRW G4
- 2HRY G4
- 3HAG E10
- 3HAH E9
- 3HP0 F7
- 3HP1 F8
- 3HP2 F7
- 3HP3 F8
- 3HP4 F7
- 3HP5 F8
- 3HP6 F7
- 3HP7 F8
- 3HPN F8
- 3HR0 C4
- 3HR1 C3
- 3HR2 C4
- 3HR3 C4
- 3HR4 C3
- 3HR5 D3
- 3HR6 D3
- 3HR7 D3
- 3HR8 D3
- 3HR9 D3
- 3HRA D3
- 3HRB D3
- 3HRC D3
- 3HRD E3
- 3HRE E2
- 3HRF E3
- 3HRG E2
- 3HRH E3
- 3HRJ E2
- 3HRK G4
- 3HRM G4
- 3HRN G4
- 3HSC C3
- 3HT7 F8
- 3HT8 F8
- 5HP1 D10
- 5HP2 D8
- 5HRZ E3
- 7H00-1 D6
- 7HP0 D9
- FHPE D9
- IHPD F8
- IHRH C4
- IHRJ C4
- IHRK F4
- IHRL F4
- IHRM F4
- IHRT E3
- IHRU E3
- IHRV E3
- IHRW D3
- IHRY D3
- IHRZ D3
- IHS0 D3
- IHS1 C3
- IHSU E5
- IHSV E4
- IHSW D8
- IHSX D10
- IHSY E9



PNX 8535: Audio

**B04M** PNX 8535: AUDIO

**B04M**

A

B

C

D

E

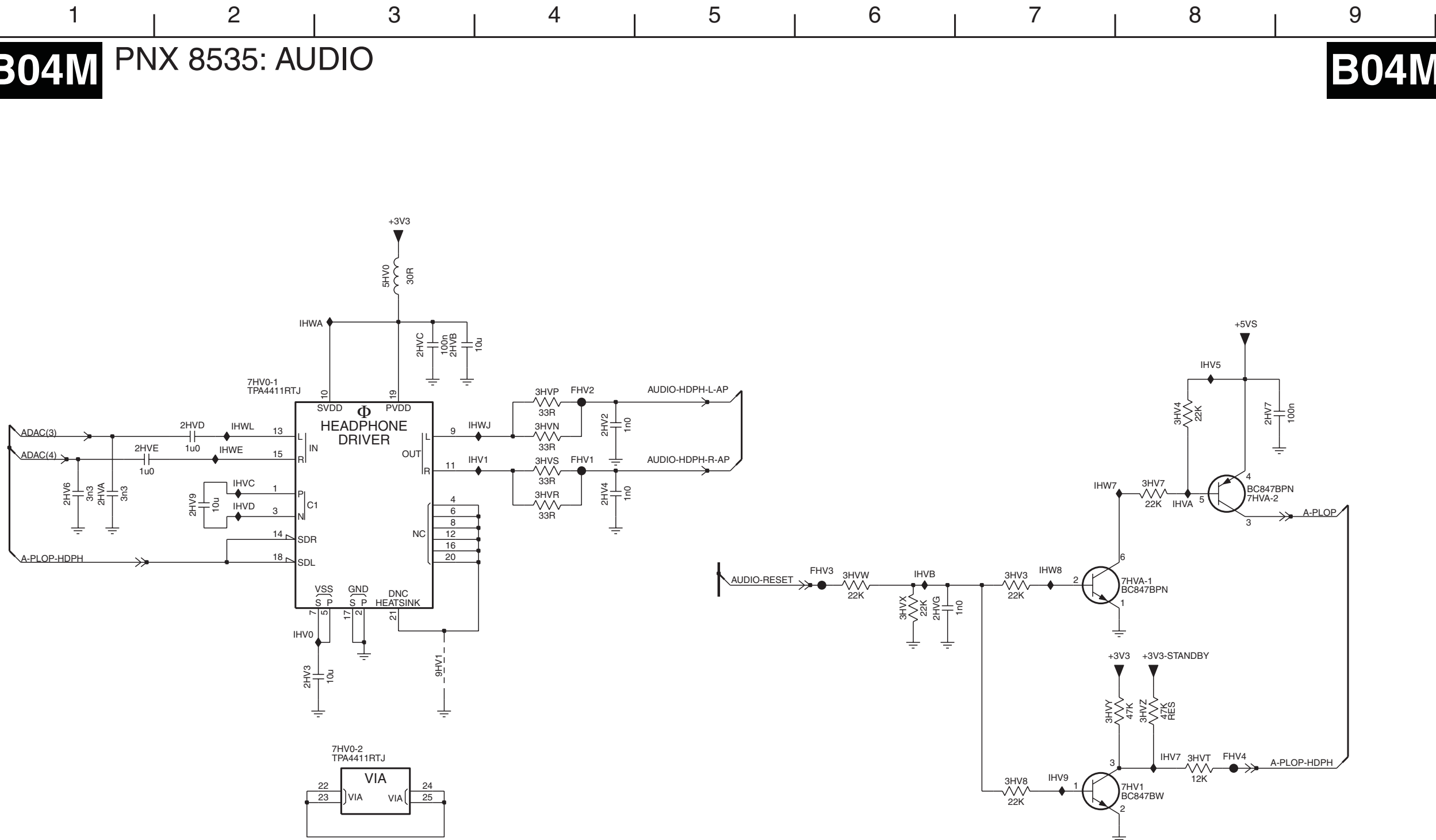
A

B

C

D

E



- 2HV2 C4
- 2HV3 D2
- 2HV4 C4
- 2HV6 C1
- 2HV7 B8
- 2HV9 C2
- 2HVA C1
- 2HVB B3
- 2HVC B3
- 2HVD C2
- 2HVE C1
- 2HVG D6
- 3HV3 D7
- 3HV4 B8
- 3HV7 C8
- 3HV8 E7
- 3HVN C4
- 3HVP B4
- 3HVR C4
- 3HVS C4
- 3HVT E8
- 3HVV D6
- 3HVD D6
- 3HVY D7
- 3HVZ D8
- 5HV0 B3
- 7HV0-1 B2
- 7HV0-2 E3
- 7HV1 E8
- 7HVA-1 D8
- 7HVA-2 C8
- 9HV1 D3
- FHV1 C4
- FHV2 B4
- FHV3 C6
- FHV4 E8
- IHV0 D2
- IHV1 C4
- IHV5 B8
- IHV7 E8
- IHV9 E7
- IHVA C8
- IHVB C6
- IHVC C2
- IHVD C2
- IHW7 C7
- IHW8 C7
- IHWA B2
- IHWE C2
- IHWJ C4
- IHWL C2

PNX 8535: Video Streams

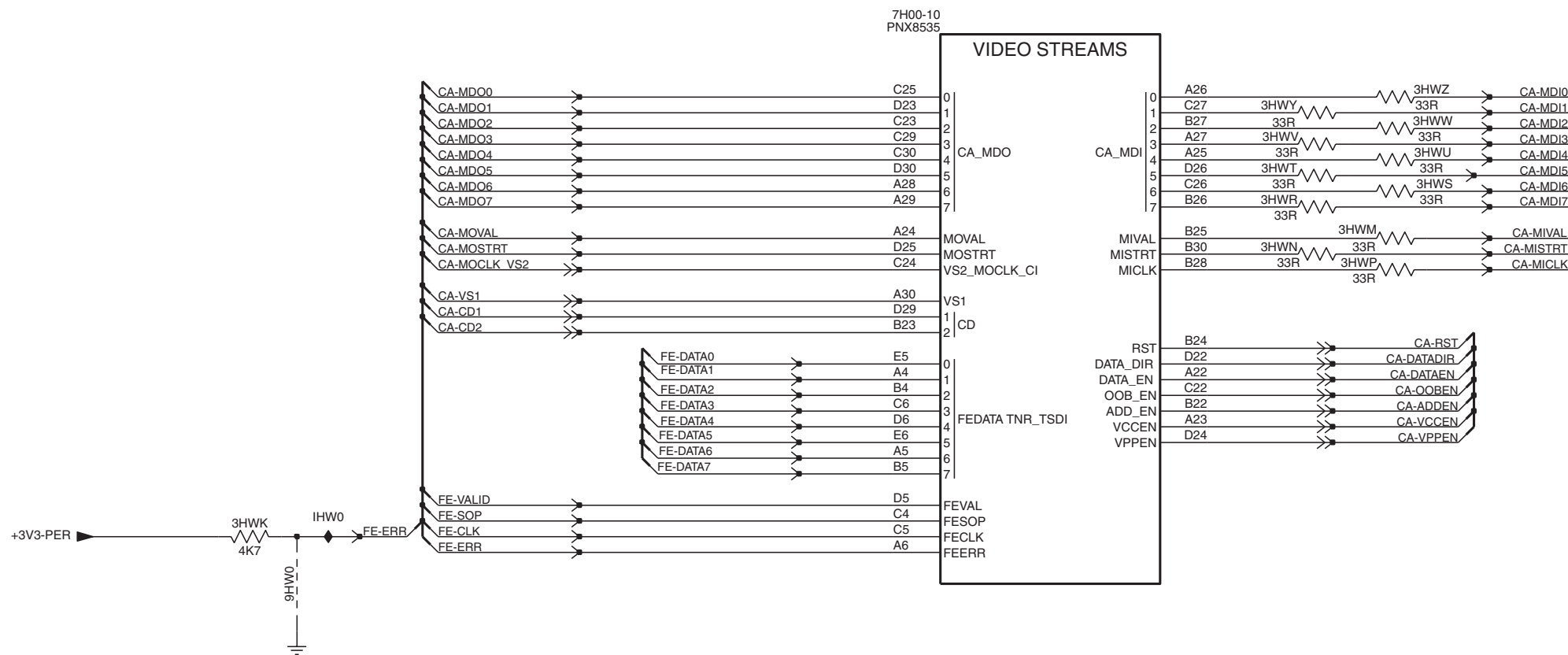
**B04N** PNX 8535: VIDEO STREAMS

**B04N**

A  
B  
C  
D  
E  
F

A  
B  
C  
D  
E  
F

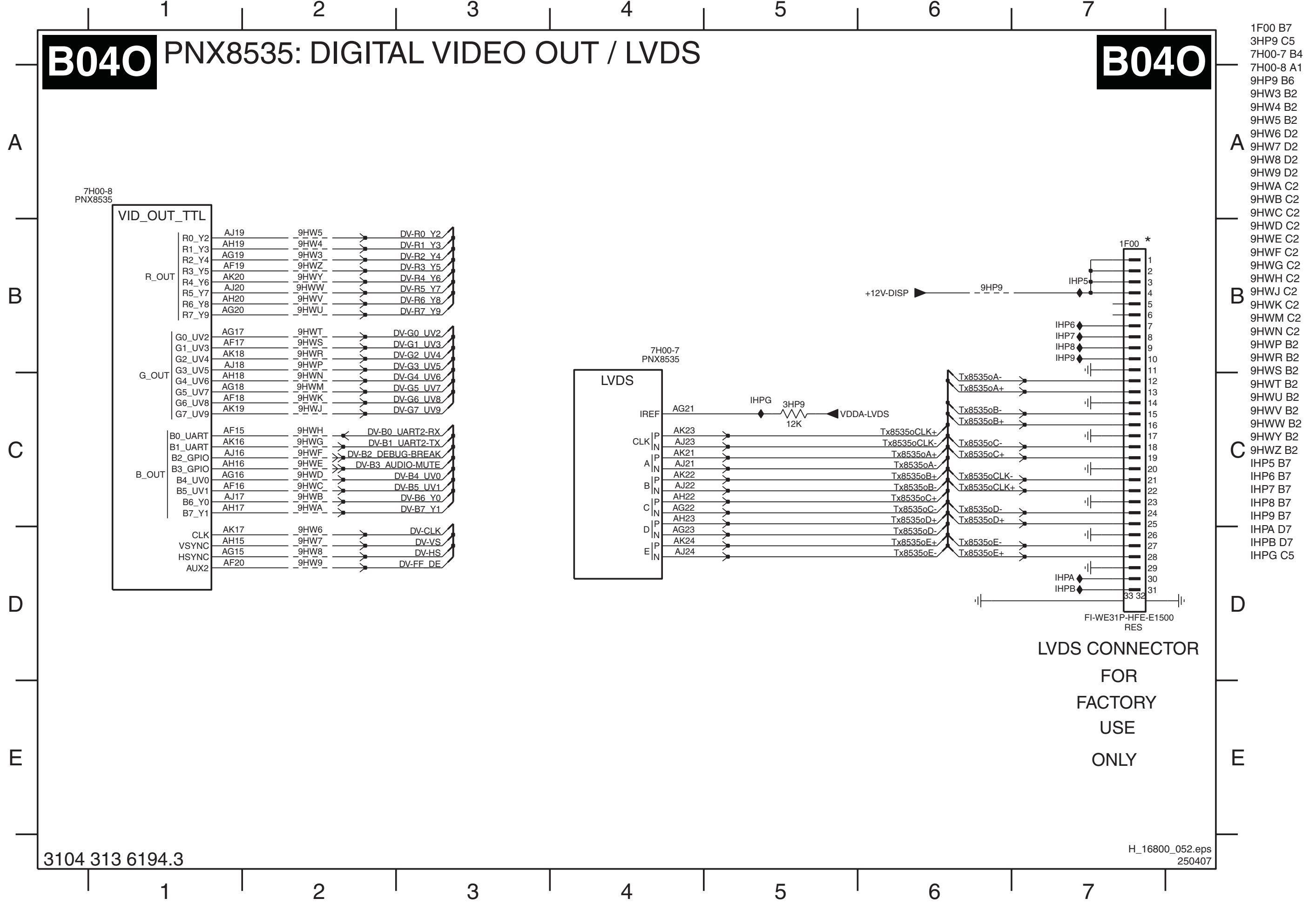
- 3HWK D2
- 3HWM C7
- 3HWN C7
- 3HWP C7
- 3HWR C7
- 3HWS C7
- 3HWT C7
- 3HWU C7
- 3HWV B7
- 3HWW B7
- 3HWY B7
- 3HWZ B7
- 7H00-10 B5
- 9HW0 D2
- IHW0 D2



1 2 3 4 5 6 7 8 9

PNX 8535: Digital Video

**B040** PNX8535: DIGITAL VIDEO OUT / LVDS **B040**

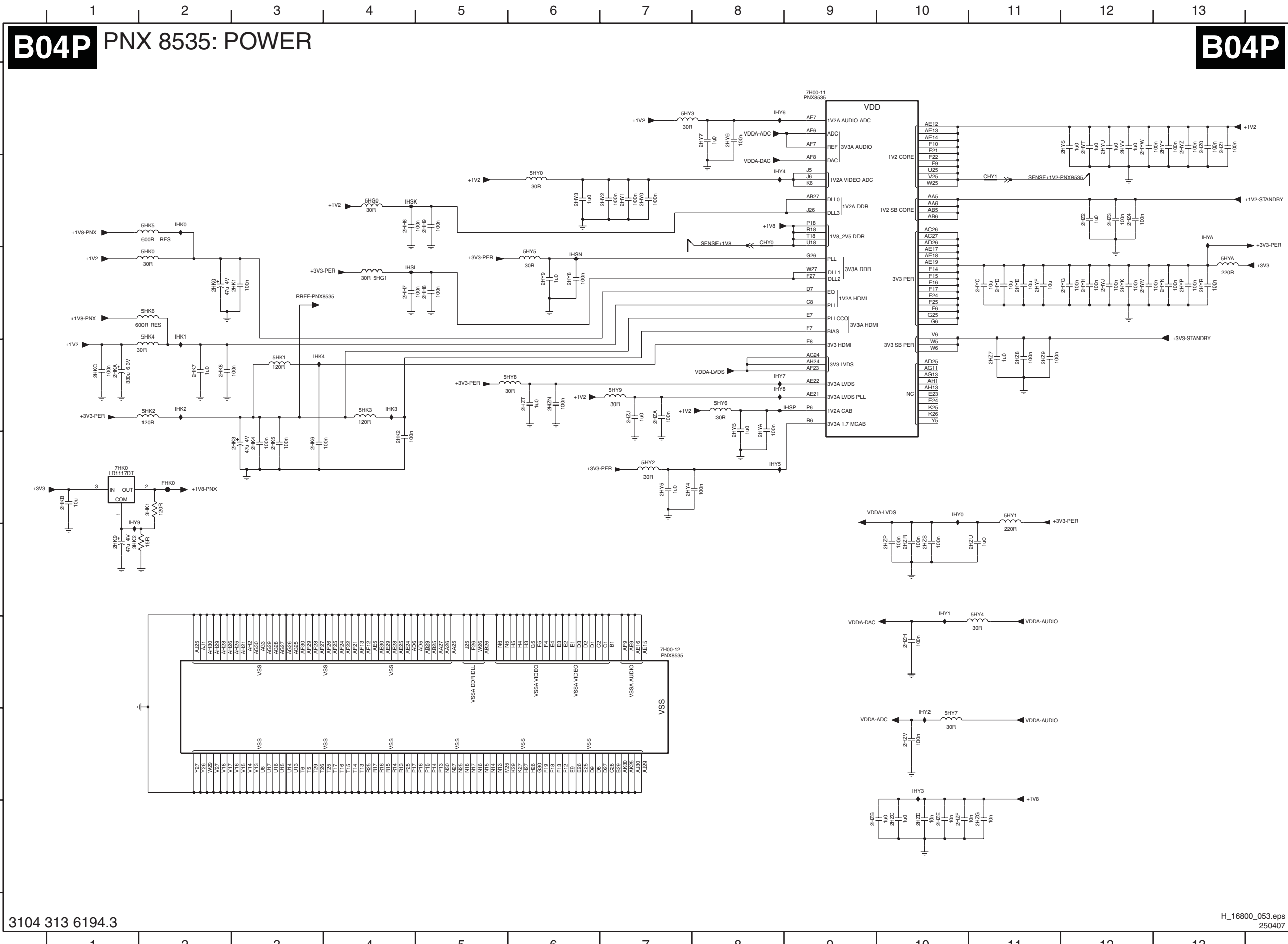


PNX 8535: Power

# B04P PNX 8535: POWER

# B04P

2HH6 B4  
2HH7 C4  
2HH8 C5  
2HH9 B5  
2HK0 C2  
2HK1 C3  
2HK2 E4  
2HK3 E3  
2HK4 E3  
2HK5 E3  
2HK6 E3  
2HK7 D2  
2HK8 D2  
2HK9 F1  
2HKA D1  
2HKB E1  
2HKB D1  
2HY0 B7  
2HY1 B7  
2HY2 B7  
2HY3 B6  
2HY4 E7  
2HY5 E7  
2HY6 A8  
2HY7 A8  
2HY8 C6  
2HY9 C6  
2HYA D8  
2HYB D8  
2HYC C11  
2HYD C11  
2HYE C11  
2HYF C12  
2HYG C12  
2HYH C12  
2HYJ C12  
2HYK C12  
2HYM C12  
2HYN C13  
2HYO C13  
2HYR C13  
2HYS A12  
2HYT A12  
2HYU A12  
2HYV A12  
2HYW A12  
2HYX A13  
2HYZ A13  
2HZ0 A13  
2HZ1 A13  
2HZ2 B12  
2HZ3 B12  
2HZ4 B12  
2HZ7 D11  
2HZ8 D11  
2HZ9 D11  
2HZA D7  
2HZB I9  
2HZC I10  
2HZD I10  
2HZE I10  
2HZF I10  
2HZG I11  
2HZH G10  
2HZJ D7  
2HZN D6  
2HZP F10  
2HZR F10  
2HZS F10  
2HZT D6  
2HZU F11  
2HZV H10  
3HK1 E2  
3HK2 F1  
5HG0 B4  
5HG1 C4  
5HK0 C2  
5HK1 D3  
5HK2 D2  
5HK3 D4  
5HK4 C2  
5HK5 B2  
5HK6 C2  
5HY0 B6  
5HY1 E11  
5HY2 E7  
5HY3 A7  
5HY4 G11  
5HY5 C6  
5HY6 D8  
5HY7 H10  
5HY8 D6  
5HY9 D7  
5HYA C13  
7H00-11 A9  
7H00-12 G7  
7HK0 E1  
CHY0 B8  
CHY1 B11  
FHK0 E2  
IHK0 B2  
IHK1 C2  
IHK2 D2  
IHK3 D4  
IHK4 D3  
IHSK B4  
IHSL C4  
IHSN C6  
IHSP D9  
IHY0 E10  
IHY1 F10  
IHY2 H10  
IHY3 H10  
IHY4 B8  
IHY5 E8  
IHY6 A8  
IHY7 D8



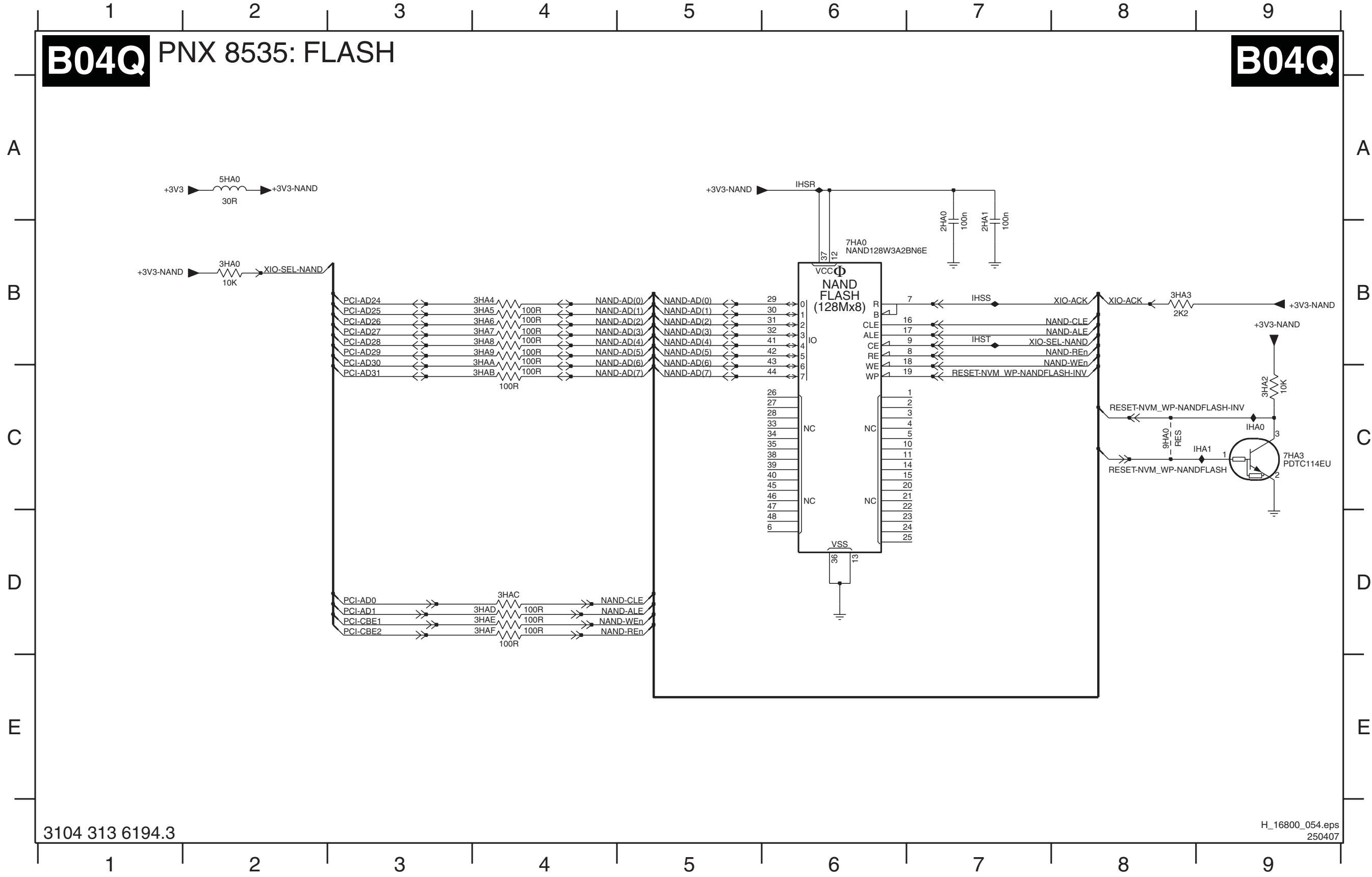
- 2HH6 B4
- 2HH7 C4
- 2HH8 C5
- 2HH9 B5
- 2HK0 C2
- 2HK1 C3
- 2HK2 E4
- 2HK3 E3
- 2HK4 E3
- 2HK5 E3
- 2HK6 E3
- 2HK7 D2
- 2HK8 D2
- 2HK9 F1
- 2HKA D1
- 2HKB E1
- 2HKB D1
- 2HY0 B7
- 2HY1 B7
- 2HY2 B7
- 2HY3 B6
- 2HY4 E7
- 2HY5 E7
- 2HY6 A8
- 2HY7 A8
- 2HY8 C6
- 2HY9 C6
- 2HYA D8
- 2HYB D8
- 2HYC C11
- 2HYD C11
- 2HYE C11
- 2HYF C12
- 2HYG C12
- 2HYH C12
- 2HYJ C12
- 2HYK C12
- 2HYM C12
- 2HYN C13
- 2HYO C13
- 2HYR C13
- 2HYS A12
- 2HYT A12
- 2HYU A12
- 2HYV A12
- 2HYW A12
- 2HYX A13
- 2HYZ A13
- 2HZ0 A13
- 2HZ1 A13
- 2HZ2 B12
- 2HZ3 B12
- 2HZ4 B12
- 2HZ7 D11
- 2HZ8 D11
- 2HZ9 D11
- 2HZA D7
- 2HZB I9
- 2HZC I10
- 2HZD I10
- 2HZE I10
- 2HZF I10
- 2HZG I11
- 2HZH G10
- 2HZJ D7
- 2HZN D6
- 2HZP F10
- 2HZR F10
- 2HZS F10
- 2HZT D6
- 2HZU F11
- 2HZV H10
- 3HK1 E2
- 3HK2 F1
- 5HG0 B4
- 5HG1 C4
- 5HK0 C2
- 5HK1 D3
- 5HK2 D2
- 5HK3 D4
- 5HK4 C2
- 5HK5 B2
- 5HK6 C2
- 5HY0 B6
- 5HY1 E11
- 5HY2 E7
- 5HY3 A7
- 5HY4 G11
- 5HY5 C6
- 5HY6 D8
- 5HY7 H10
- 5HY8 D6
- 5HY9 D7
- 5HYA C13
- 7H00-11 A9
- 7H00-12 G7
- 7HK0 E1
- CHY0 B8
- CHY1 B11
- FHK0 E2
- IHK0 B2
- IHK1 C2
- IHK2 D2
- IHK3 D4
- IHK4 D3
- IHSK B4
- IHSL C4
- IHSN C6
- IHSP D9
- IHY0 E10
- IHY1 F10
- IHY2 H10
- IHY3 H10
- IHY4 B8
- IHY5 E8
- IHY6 A8
- IHY7 D8

PNX 8535: Flash

B04Q

PNX 8535: FLASH

B04Q

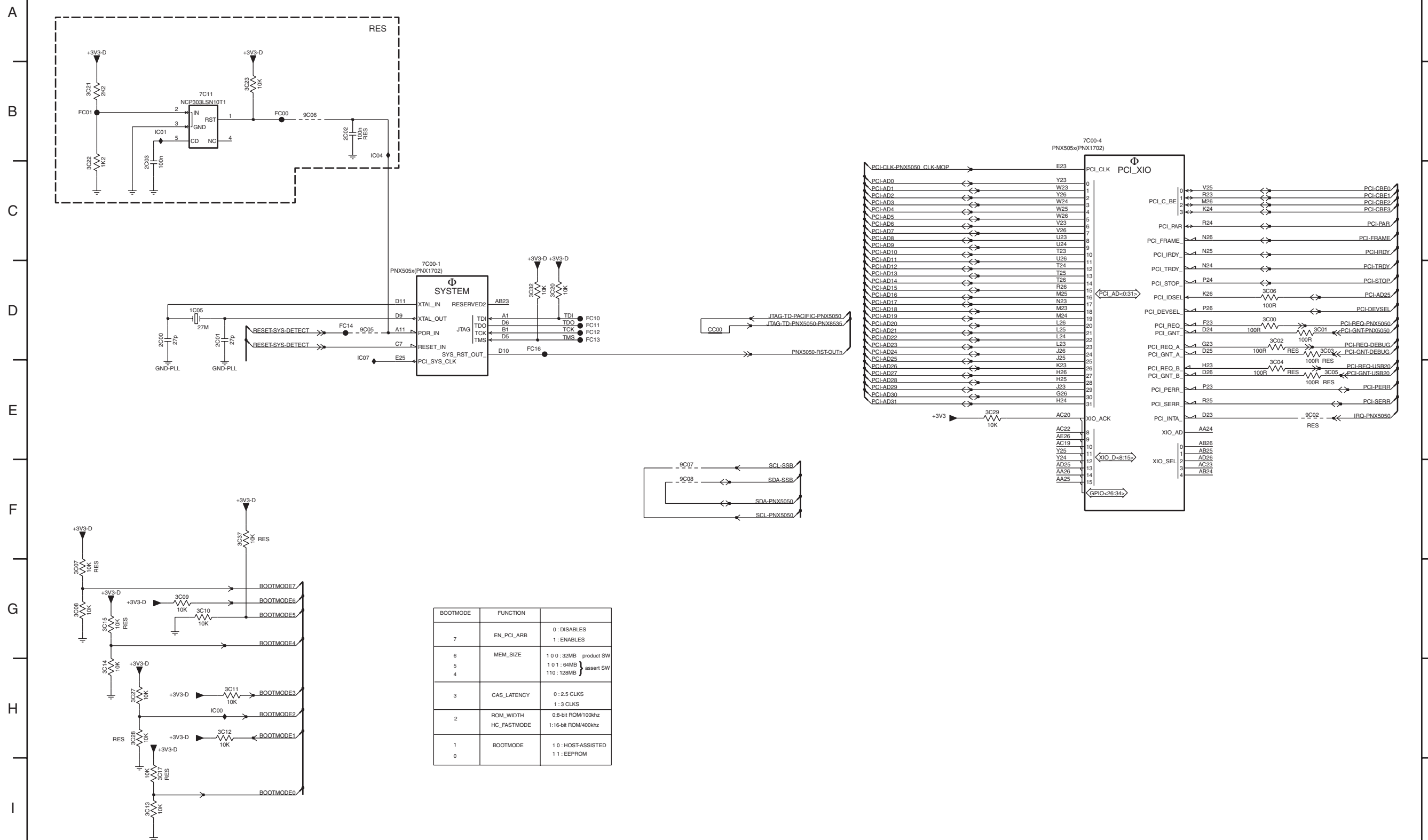


- 2HA0 B7
- 2HA1 B7
- 3HA0 B2
- 3HA2 C9
- 3HA3 B8
- 3HA4 B4
- 3HA5 B4
- 3HA6 B4
- 3HA7 B4
- 3HA8 B4
- 3HA9 B4
- 3HAA C4
- 3HAB C4
- 3HAC D4
- 3HAD D4
- 3HAE D4
- 3HAF D4
- 5HA0 A2
- 7HA0 B6
- 7HA3 C9
- 9HA0 C8
- IHA0 C9
- IHA1 C9
- IHSR A6
- IHSS B7
- IHST B7

PNX5050: Control

**B05A** PNX5050: CONTROL

**B05A**



- 1C05 D2
- 2C00 D2
- 2C01 D2
- 2C02 B4
- 2C03 C2
- 3C00 D13
- 3C01 D13
- 3C02 D13
- 3C03 D14
- 3C04 E13
- 3C05 E14
- 3C06 D13
- 3C07 G1
- 3C08 G1
- 3C09 G2
- 3C10 G2
- 3C11 H3
- 3C12 H2
- 3C13 I2
- 3C14 H1
- 3C15 G1
- 3C17 I2
- 3C20 D6
- 3C21 B1
- 3C22 C1
- 3C23 B3
- 3C27 H2
- 3C28 H2
- 3C29 E10
- 3C32 D6
- 3C37 F3
- 7C00-4 B11
- 7C11 B2
- 9C02 E13
- 9C05 D4
- 9C06 B3
- 9C07 F7
- 9C08 D7
- FC00 B3
- FC01 B1
- FC10 D6
- FC11 D6
- FC12 D6
- FC13 D6
- FC14 D4
- FC16 D6
- IC00 B2
- IC01 H1
- IC04 B4
- IC07 D4

BOOTMODE	FUNCTION	
7	EN_PCI_ARB	0 : DISABLES 1 : ENABLES
6	MEM_SIZE	1 0 0 : 32MB product SW 1 0 1 : 64MB } assert SW 1 1 0 : 128MB }
3	CAS_LATENCY	0 : 2.5 CLKs 1 : 3 CLKs
2	ROM_WIDTH HC_FASTMODE	0:8-bit ROM/100khz 1:16-bit ROM/400khz
1	BOOTMODE	1 0 : HOST-ASSISTED 1 1 : EEPROM
0		

3104 313 6194.3

H\_16800\_055.eps  
010507

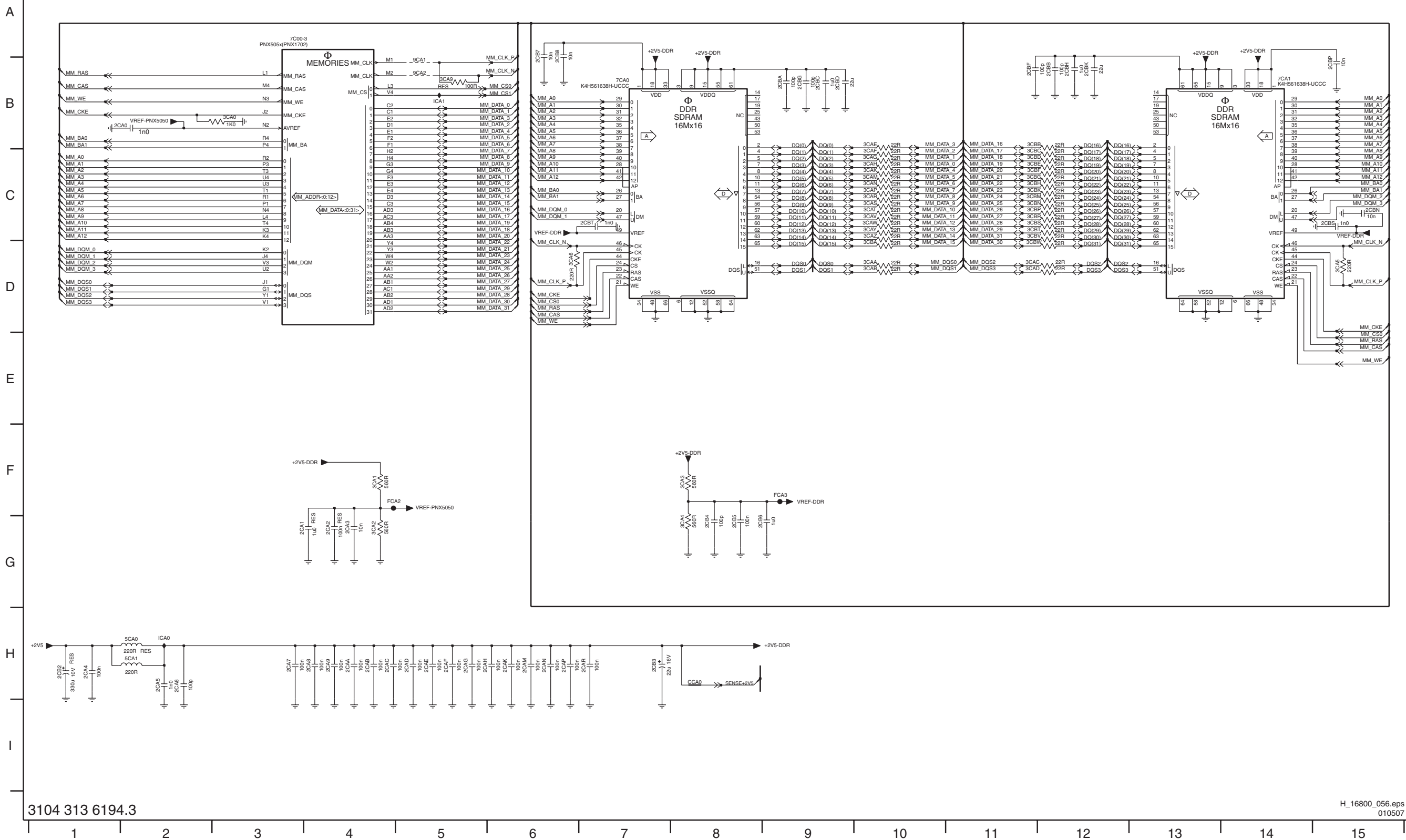


PNX5050: SDRAM

B05B

PNX5050: SDRAM

B05B



- 2CA0 B2
- 2CA1 G3
- 2CA2 G4
- 2CA3 G4
- 2CA4 H1
- 2CA5 H2
- 2CA6 H2
- 2CA7 H3
- 2CA8 H4
- 2CA9 H4
- 2CAA H4
- 2CAB H4
- 2CAC H4
- 2CAD H5
- 2CAE H5
- 2CAF H5
- 2CAG H5
- 2CAH H5
- 2CAK H6
- 2CAN H6
- 2CAP H6
- 2CAR H7
- 2CB2 H1
- 2CB3 H7
- 2CB4 G8
- 2CB5 G8
- 2CB6 G8
- 2CB7 A6
- 2CB8 A6
- 2CBA B9
- 2CBC B12
- 2CB B9
- 2CB B11
- 2CB B9
- 2CB B12
- 2CB B12
- 2CBM C15
- 2CBP B15
- 2CBS C15
- 2CBT C7
- 3CA0 B3
- 3CA1 F4
- 3CA2 G4
- 3CA3 F8
- 3CA4 G8
- 3CA5 D15
- 3CA6 D6
- 3CA9 B5
- 3CAA D10
- 3CAB D10
- 3CAC D11
- 3CAD D11
- 3CAE B10
- 3CAF C10
- 3CAG C10
- 3CAH C10
- 3CAK C10
- 3CAM C10
- 3CAN C10
- 3CAP C10
- 3CAR C10
- 3CAS C10
- 3CAT C10
- 3CAV C10
- 3CAW C10
- 3CAY C10
- 3CAZ C10
- 3CBA D10
- 3CBB B11
- 3CBC C11
- 3CBD C11
- 3CBE C11
- 3CBF C11
- 3CBG C11
- 3CBH C11
- 3CBK C11
- 3CBM C11
- 3CBN C11
- 3CBP C11
- 3CBR C11
- 3CBS C11
- 3CBT C11
- 3CBV C11
- 3CBW D11
- 7CA0 H2
- 7CA0 B7
- 7CA1 B14
- 9CA1 B5
- 9CA2 B5
- CCA0 H8
- FCA3 F4
- FCA3 F9
- ICA0 H2
- ICA1 B5

3104 313 6194.3

H\_16800\_056.eps  
010507

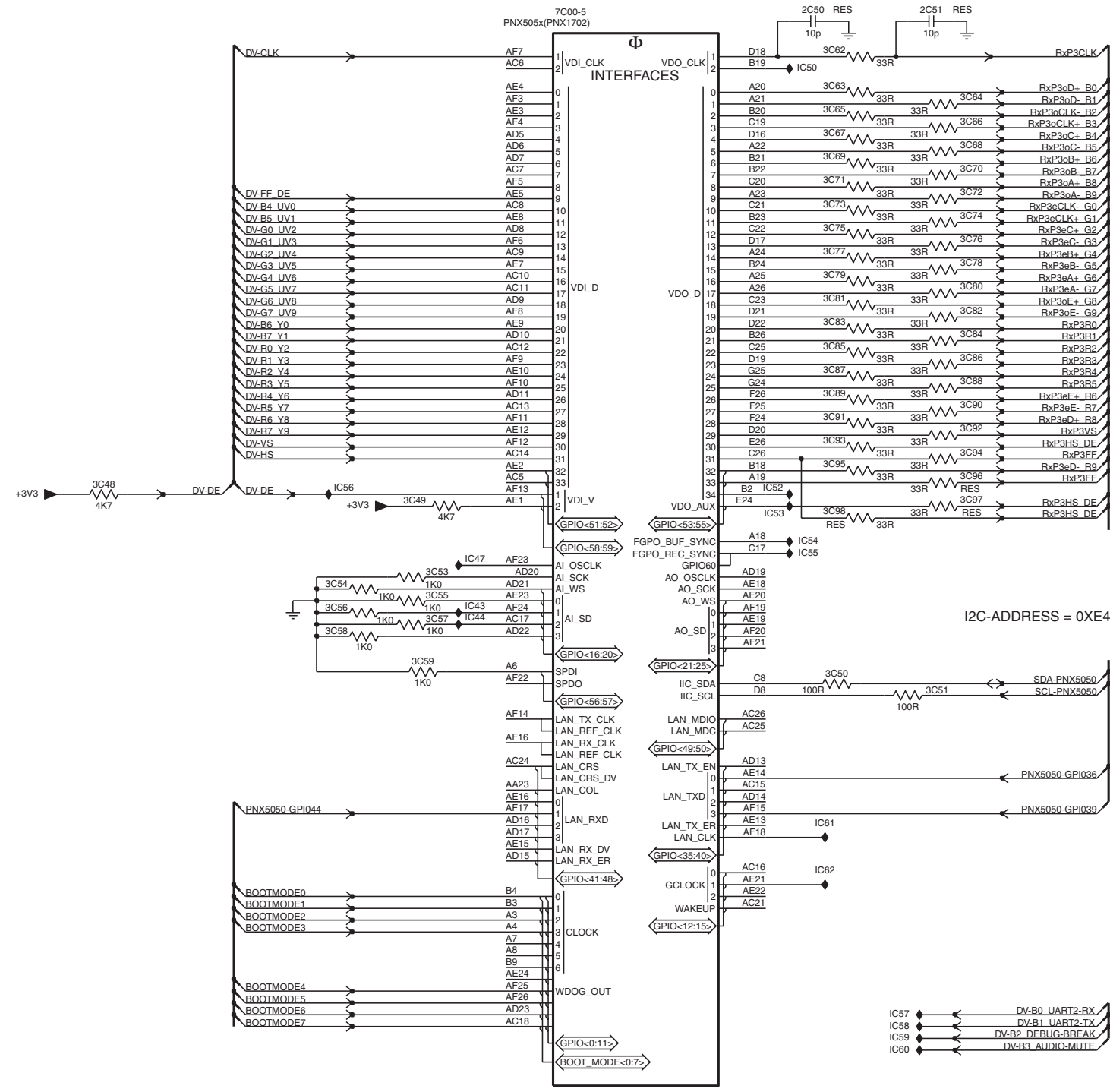
PNX5050: Video

B05C PNX5050: VIDEO

B05C

A  
B  
C  
D  
E  
F  
G  
H

A  
B  
C  
D  
E  
F  
G  
H

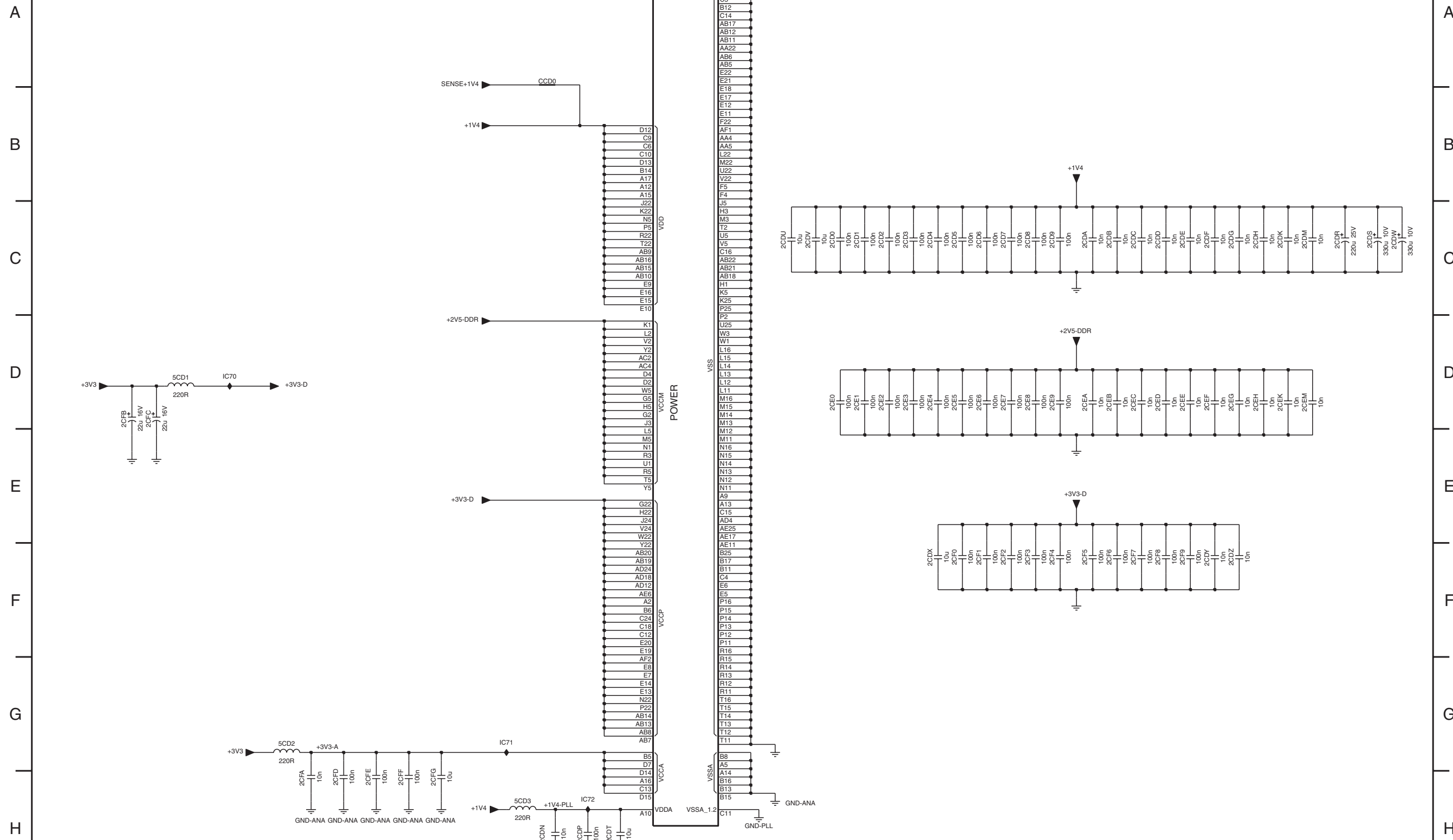


- 2C50 A8
- 2C51 A8
- 3C48 D3
- 3C49 D5
- 3C50 E8
- 3C51 E8
- 3C53 D5
- 3C54 E5
- 3C55 E5
- 3C56 E5
- 3C57 E5
- 3C58 E5
- 3C59 E5
- 3C62 A8
- 3C63 B8
- 3C64 B9
- 3C65 B8
- 3C66 B9
- 3C67 B8
- 3C68 B9
- 3C69 B8
- 3C70 B9
- 3C71 B8
- 3C72 B9
- 3C73 B8
- 3C74 B9
- 3C75 B8
- 3C76 B9
- 3C77 C8
- 3C78 C9
- 3C79 C8
- 3C80 C9
- 3C81 C8
- 3C82 C9
- 3C83 C8
- 3C84 C9
- 3C85 C8
- 3C86 C9
- 3C87 C8
- 3C88 C9
- 3C89 C8
- 3C90 C9
- 3C91 D8
- 3C92 D9
- 3C93 D8
- 3C94 D9
- 3C95 D8
- 3C96 D9
- 3C97 D9
- 3C98 D8
- 7C00-5 A6
- IC43 E6
- IC44 E6
- IC47 D6
- IC50 A8
- IC52 D7
- IC53 D7
- IC54 D8
- IC55 D8
- IC56 D5
- IC57 G8
- IC58 G8
- IC59 G8
- IC60 G8
- IC61 F8
- IC62 F8

PNX5050: Supply

**B05D** PNX5050: SUPPLY

**B05D**



- 2CD0 C7
- 2CD1 C8
- 2CD2 C8
- 2CD3 C8
- 2CD4 C8
- 2CD5 C8
- 2CD6 C9
- 2CD7 C9
- 2CD8 C9
- 2CD9 C9
- 2CDA C10
- 2CDB C10
- 2CDC C10
- 2CDD C10
- 2CDE C10
- 2CDF C11
- 2CDG C11
- 2CDH C11
- 2CDK C11
- 2CDM C12
- 2CDN H5
- 2CDP H5
- 2CDR C12
- 2CDS C12
- 2CDT H5
- 2CDU C7
- 2CDV C7
- 2CDW C12
- 2CDX F8
- 2CDY F11
- 2CDZ F11
- 2CE0 D7
- 2CE1 D8
- 2CE2 D8
- 2CE3 D8
- 2CE4 D8
- 2CE5 D8
- 2CE6 D9
- 2CE7 D9
- 2CE8 D9
- 2CE9 D9
- 2CEA D10
- 2CEB D10
- 2CEC D10
- 2CED D10
- 2CEE D10
- 2CEF D11
- 2CEG D11
- 2CEH D11
- 2CEK D11
- 2CEM D12
- 2CF0 F8
- 2CF1 F9
- 2CF2 F9
- 2CF3 F9
- 2CF4 F9
- 2CF5 F10
- 2CF6 F10
- 2CF7 F10
- 2CF8 F10
- 2CF9 F10
- 2CFA H3
- 2CFB D1
- 2CFC D1
- 2CFD H3
- 2CFE H3
- 2CFH H4
- 2CFG H4
- 5CD1 D2
- 5CD2 G3
- 5CD3 H5
- 7C00-2 A6
- CCD0 A5
- IC70 D2
- IC71 G5
- IC72 H5

Pacific 3: LVDS

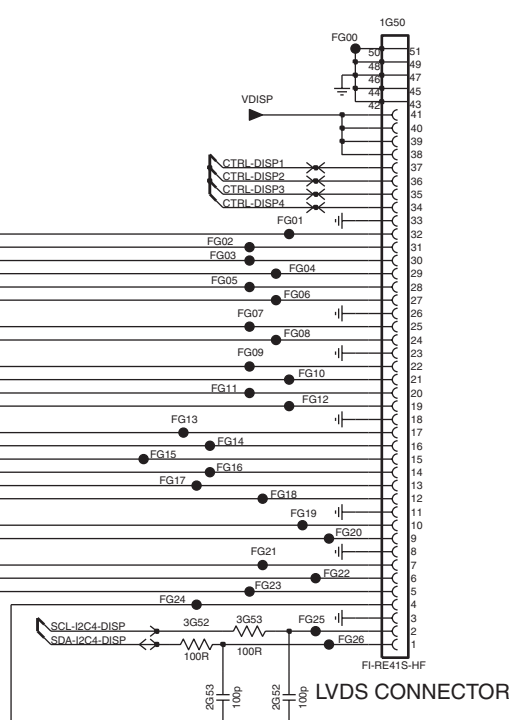
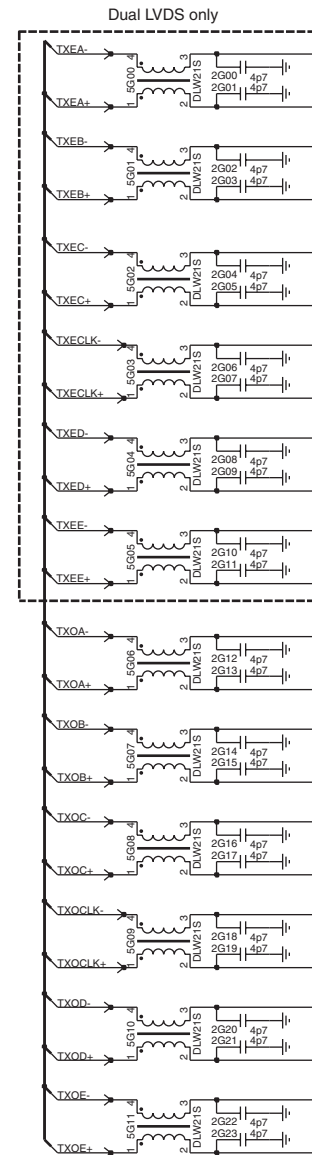
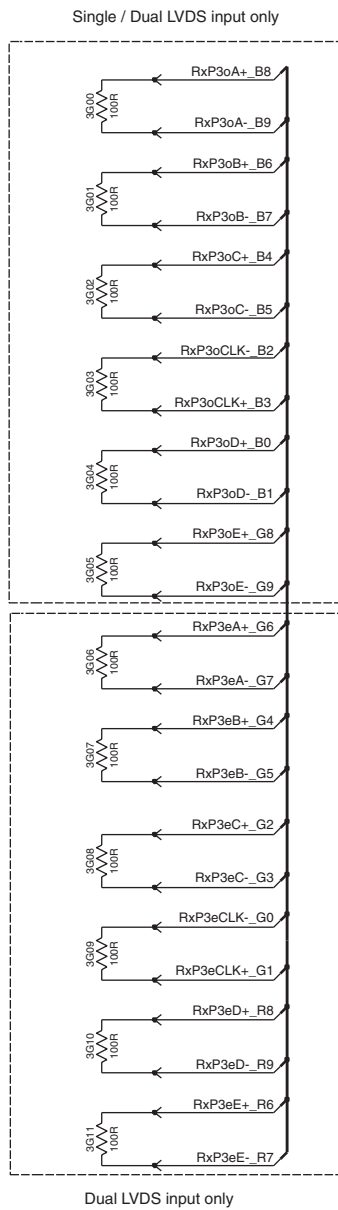
B06A

PACIFIC 3: LVDS

B06A

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H

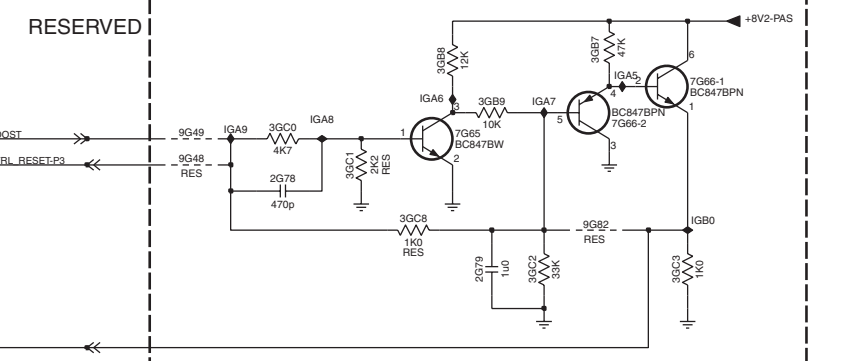
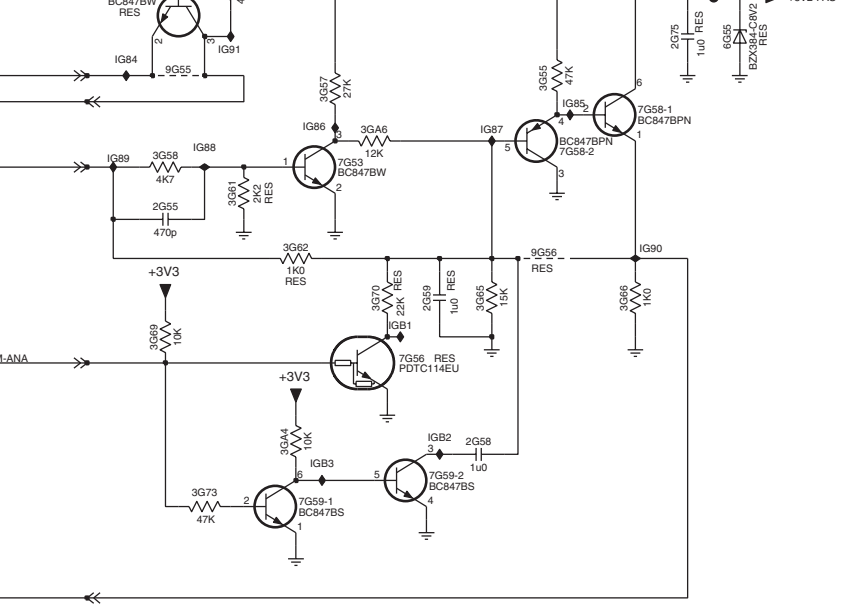
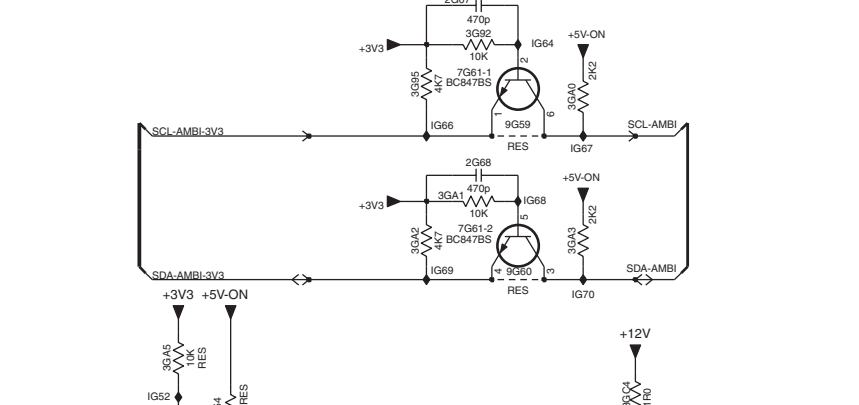
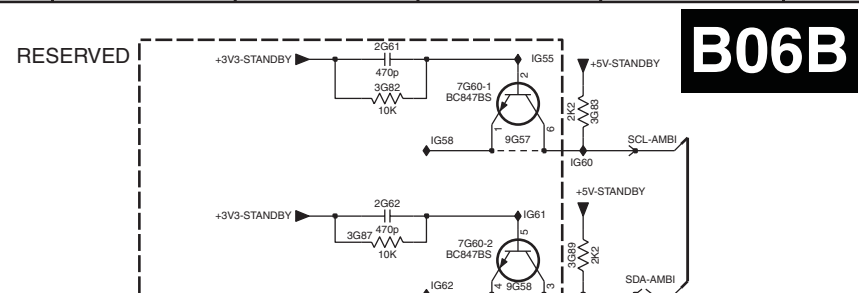
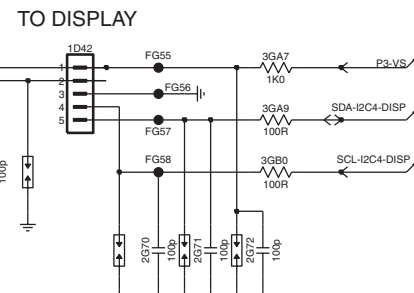
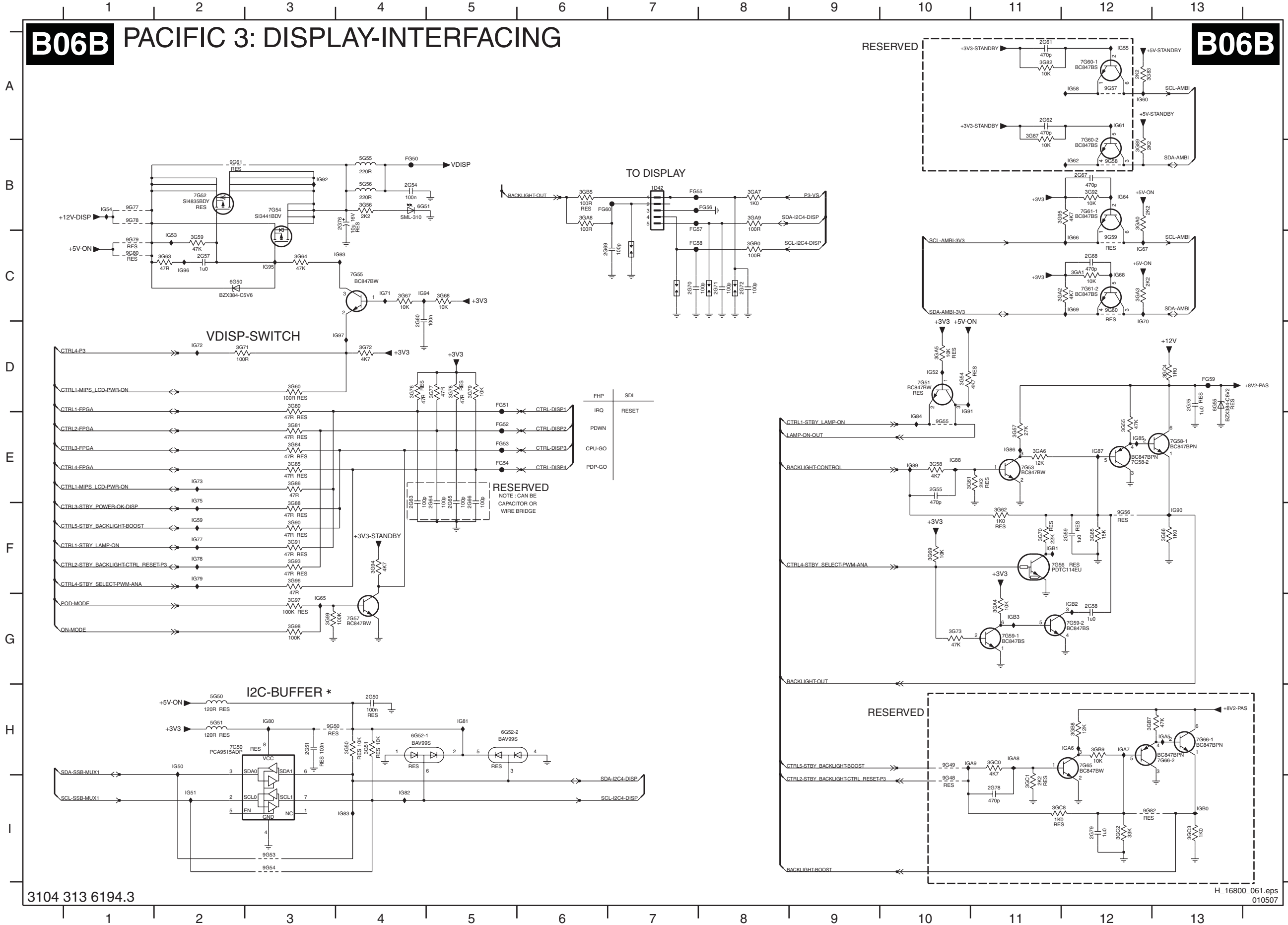
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 2G07 D9  
 2G08 D9  
 2G09 D9  
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 2G11 E9  
 2G12 E9  
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 2G21 G9  
 2G22 H9  
 2G23 H9  
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 3G02 C3  
 3G03 C3  
 3G04 D3  
 3G05 D3  
 3G06 E3  
 3G07 E3  
 3G08 F3  
 3G09 F3  
 3G10 G3  
 3G11 G3  
 3G52 D12  
 3G53 D13  
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 5G01 C9  
 5G02 C9  
 5G03 D9  
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 5G10 G9  
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 FG03 B13  
 FG04 B13  
 FG05 B13  
 FG06 B13  
 FG07 B13  
 FG08 C13  
 FG09 C13  
 FG10 C13  
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 FG12 C13  
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 FG14 C13  
 FG15 C12  
 FG16 C13  
 FG17 C12  
 FG18 C13  
 FG19 D13  
 FG20 D13  
 FG21 D13  
 FG22 D13  
 FG23 D13  
 FG24 D12  
 FG25 D13  
 FG26 D13



1 2 3 4 5 6 7 8 9 10 11 12 13 14

Pacific 3: Display & Interfacing

**B06B** PACIFIC 3: DISPLAY-INTERFACING



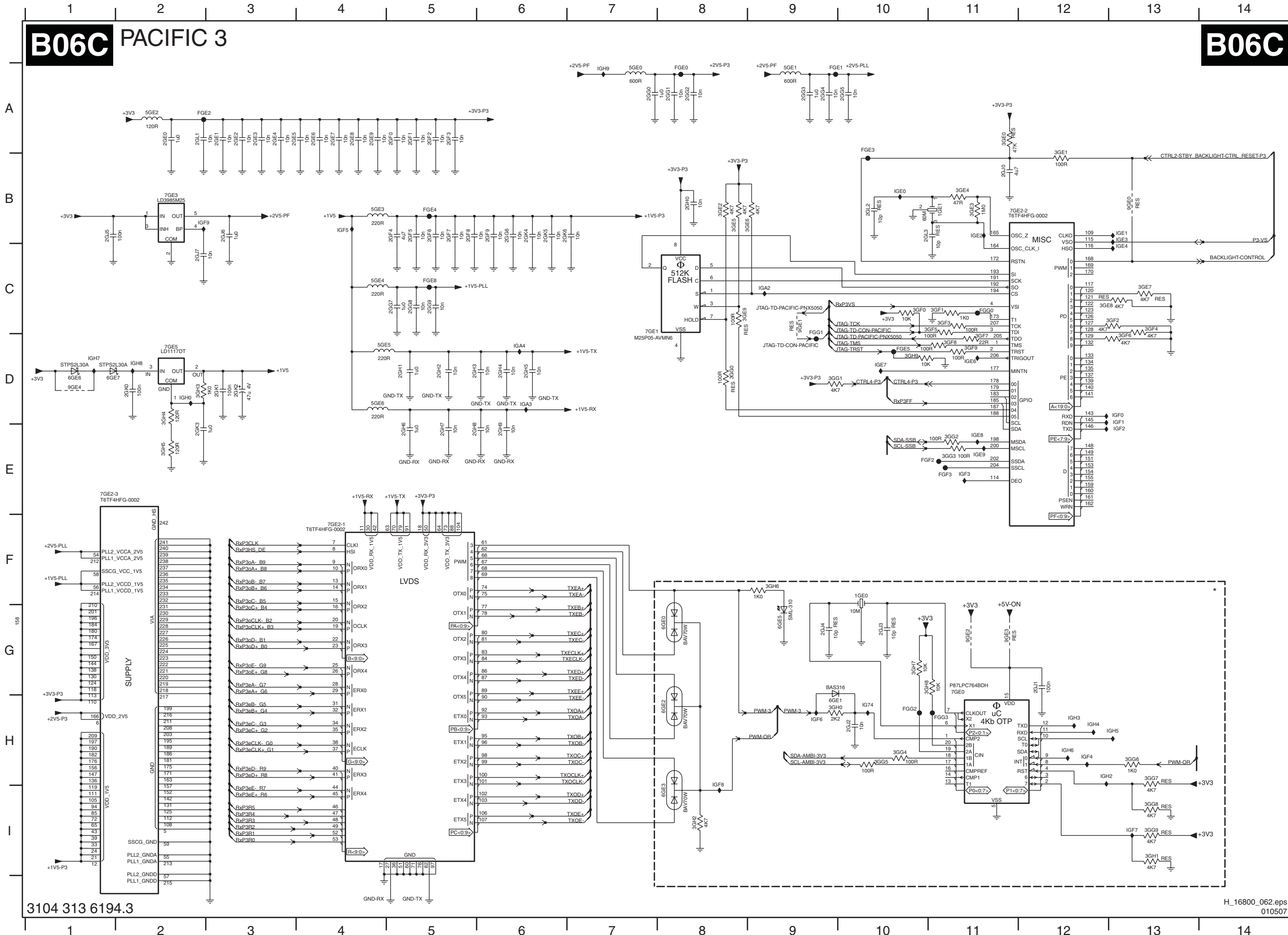
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- 2G55 E10
- 2G57 C2
- 2G58 G12
- 2G59 F12
- 2G60 C4
- 2G61 A11
- 2G62 A11
- 2G63 E4
- 2G64 E5
- 2G65 E5
- 2G66 E5
- 2G67 B12
- 2G68 C12
- 2G69 C6
- 2G70 C7
- 2G71 C8
- 2G72 C8
- 2G75 D13
- 2G76 B4
- 2G78 I11
- 2G79 I12
- 3G50 H4
- 3G51 H4
- 3G54 D10
- 3G55 E12
- 3G56 B4
- 3G57 E11
- 3G58 E10
- 3G59 C2
- 3G60 D3
- 3G61 E11
- 3G62 F11
- 3G63 C2
- 3G64 C3
- 3G65 F12
- 3G66 F13
- 3G67 C4
- 3G68 C5
- 3G69 F10
- 3G70 F11
- 3G71 D2
- 3G72 D4
- 3G73 G10
- 3G76 D4
- 3G77 D5
- 3G78 D5
- 3G79 D5
- 3G80 D3
- 3G81 E3
- 3G82 A11
- 3G83 A12
- 3G84 E3
- 3G85 E3
- 3G86 E3
- 3G87 A11
- 3G88 F3
- 3G89 B12
- 3G90 F3
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- 3G94 F4
- 3G95 B12
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- 3G97 G3
- 3G98 G3
- 3G99 G3
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- 3GA1 C12
- 3GA2 C12
- 3GA3 C12
- 3GA4 G11
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- 3GA9 B8
- 3GB0 C8
- 3GB5 B6
- 3GB7 H12
- 3GB8 H12
- 3GB9 H12
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- 3GC2 I12
- 3GC3 I13
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- 5G50 H2
- 5G51 H2
- 5G55 B4
- 5G56 B4
- 6G50 C2
- 6G51 B4
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- 6G55 D13
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- 7G51 D10
- 7G52 B2
- 7G53 E11
- 7G54 B3
- 7G55 C4
- 7G56 F11
- 7G57 G4
- 7G58-1 E13
- 7G58-2 E12
- 7G59-1 G11
- 7G59-2 G12
- 7G60-1 A12
- 7G60-2 A12
- 7G61-1 B12
- 7G61-2 C12
- 7G65 H12
- 7G66-1 H13
- 7G66-2 H13
- 9G48 I10
- 9G49 H10
- 9G50 H3
- 9G53 I3
- 9G54 I3
- 9G55 E10
- 9G56 F12
- 9G57 A12
- 9G58 B12
- 9G59 C12
- 9G60 C12
- 9G61 B2
- 9G77 B1
- 9G78 B1
- 9G79 C1
- 9G80 C1
- 9G82 I12
- 9G84 B4
- 9G85 D5
- 9G86 C5
- 9G87 B7
- 9G88 C7
- 9G89 D13
- 9G90 B6
- 9G91 I2
- 9G92 D10
- 9G93 C2
- 9G94 B2
- 9G95 A12
- 9G96 A12
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- 9G110 A12
- 9G111 A12
- 9G112 A12
- 9G113 A12
- 9G114 A12
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- 9G116 A12
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- 9G120 A12



Pacific 3

B06C PACIFIC 3

B06C



- 1GE0 F10
- 1GE1 B11
- 2GE0 A2
- 2GE1 A3
- 2GE2 A3
- 2GE3 A3
- 2GE4 A3
- 2GE5 A3
- 2GE6 A4
- 2GE7 A4
- 2GE8 A4
- 2GE9 A4
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- 2GF1 A5
- 2GF2 A5
- 2GF3 A5
- 2GF4 B5
- 2GF5 B5
- 2GF6 B5
- 2GF7 B5
- 2GF8 B5
- 2GG0 A7
- 2GG1 A7
- 2GG2 A8
- 2GG3 A9
- 2GG4 A9
- 2GG5 A10
- 2GG6 B6
- 2GG7 C5
- 2GG8 C5
- 2GG9 C5
- 2GH0 B8
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- 2GH2 D5
- 2GH3 D5
- 2GH4 D6
- 2GH5 D6
- 2GH6 E5
- 2GH7 E5
- 2GH8 E5
- 2GH9 E6
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- 2GJ2 H10
- 2GJ3 G10
- 2GJ4 G9
- 2GJ5 B1
- 2GJ6 B3
- 2GJ7 C2
- 2GK0 D2
- 2GK1 D3
- 2GK2 D3
- 2GK3 E2
- 2GK4 B6
- 2GK5 B6
- 2GK6 B6
- 2GL1 A2
- 2GL2 B10
- 2GL3 B10
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- 3GE4 B11
- 3GE5 B8
- 3GE6 B9
- 3GE7 C13
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- 3GE9 C8
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- 3GF1 C11
- 3GF2 C13
- 3GF3 C11
- 3GF4 C13
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- 3GG9 I3
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- 3GH3 D2
- 3GH4 D2
- 3GH5 E2
- 3GH6 F9
- 3GH7 G10
- 3GH8 G11
- 3GH9 D10
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- 5GE1 A9
- 5GE2 A2
- 5GE3 B4
- 5GE4 C4
- 5GE5 D4
- 5GE6 D4
- 6GE0 G8
- 6GE1 H9
- 6GE2 H8
- 6GE3 I8
- 6GE5 G9
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- 6GE7 D1
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- 7GE3 B2
- 7GE5 D2
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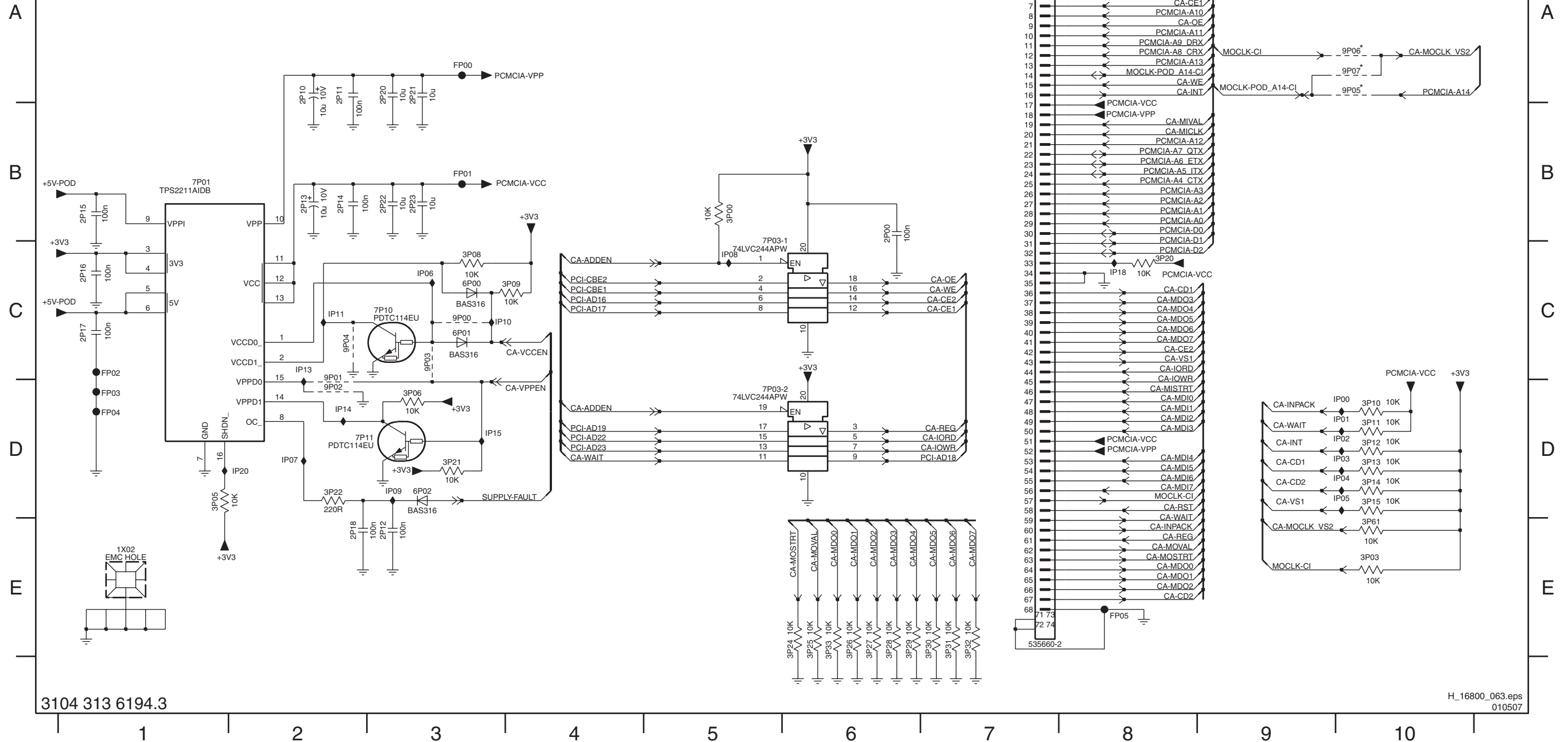
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1X02 E1	2P13 B2	2P18 E2	3P00 B5	3P09 C4	3P14 D10	3P24 E6	3P29 E6	3P61 E10	7P03-1 C6	9P01 D2	9P06 A10	FP03 D1	IP02 D10	IP07 D2	IP13 C2	
2P00 B6	2P14 B2	2P20 A3	3P03 E10	3P10 D10	3P15 D10	3P25 E6	3P30 E7	6P00 C3	7P03-2 D6	9P02 D2	9P07 A10	FP04 D1	IP03 D10	IP08 C5	IP14 D2	
2P10 A2	2P15 B1	2P21 A3	3P05 D1	3P11 D10	3P20 C8	3P26 E6	3P31 E7	6P01 C3	7P10 C3	9P03 C3	FP00 A3	FP05 E8	IP04 D10	IP09 D3	IP15 D3	
2P11 A2	2P16 C1	2P22 B3	3P06 D3	3P12 D10	3P21 D3	3P27 E6	3P32 E7	6P02 D3	7P11 D3	9P04 C2	FP01 B3	FP00 D10	IP05 D10	IP10 C3	IP18 C8	

**B07A** POD: SUPPLY / CONTROL  
(POD IS THE EQUIVALENT OF COMMON INTERFACE)

CABLE CARD INTERFACE

**B07A**



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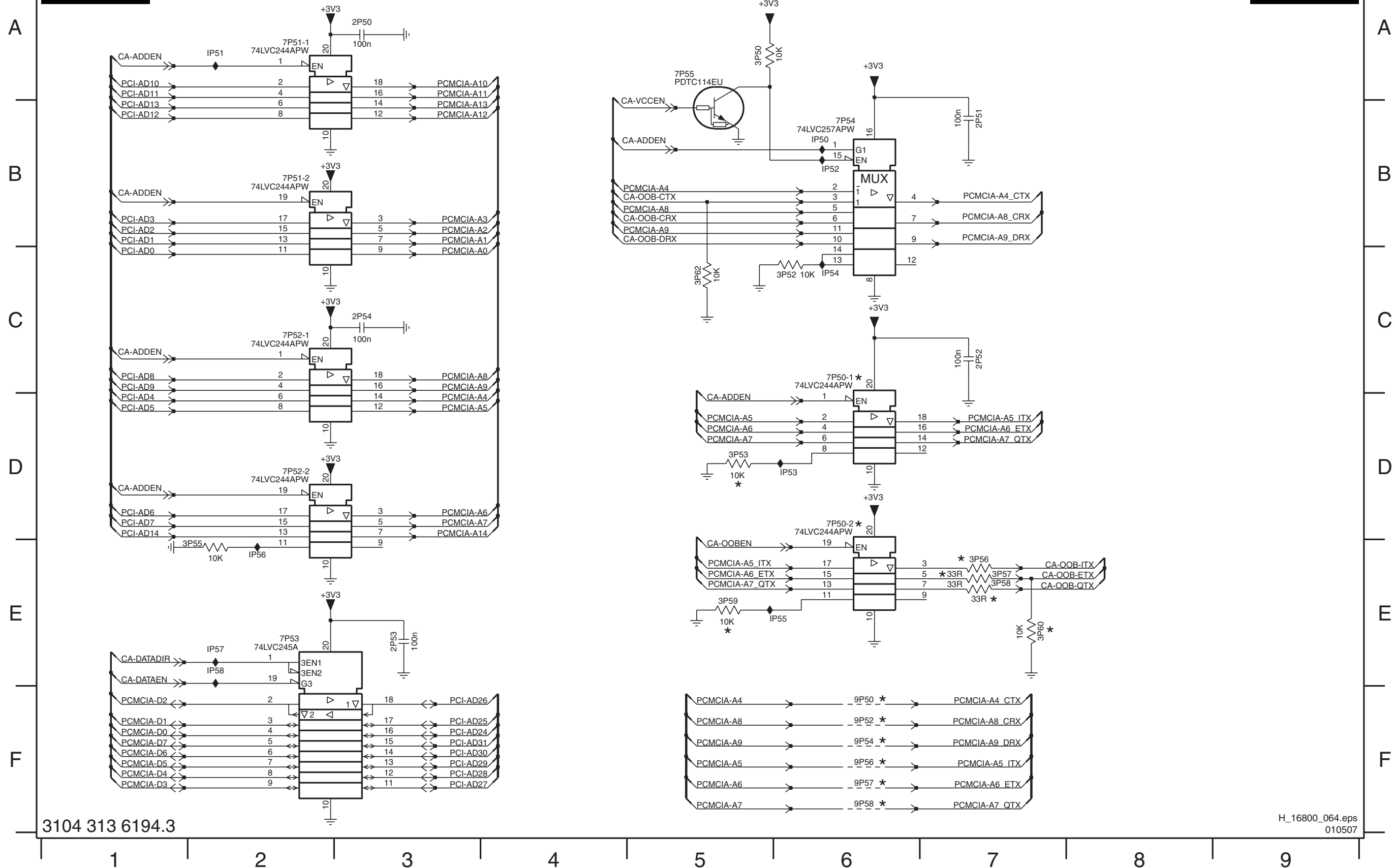
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POD: Buffering

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2P51 B7	2P54 C3	3P53 D5	3P57 E7	3P60 E7	7P50-2 D6	7P52-1 C2	7P54 B6	9P52 F6	9P57 F6	IP51 A2	IP54 C6	IP57 E2
2P52 C7	3P50 A5	3P55 E2	3P58 E7	3P62 C5	7P51-1 A2	7P52-2 D2	7P55 A5	9P54 F6	9P58 F6	IP52 B6	IP55 E6	IP58 E2

**B07B** POD: BUFFERING

**B07B**



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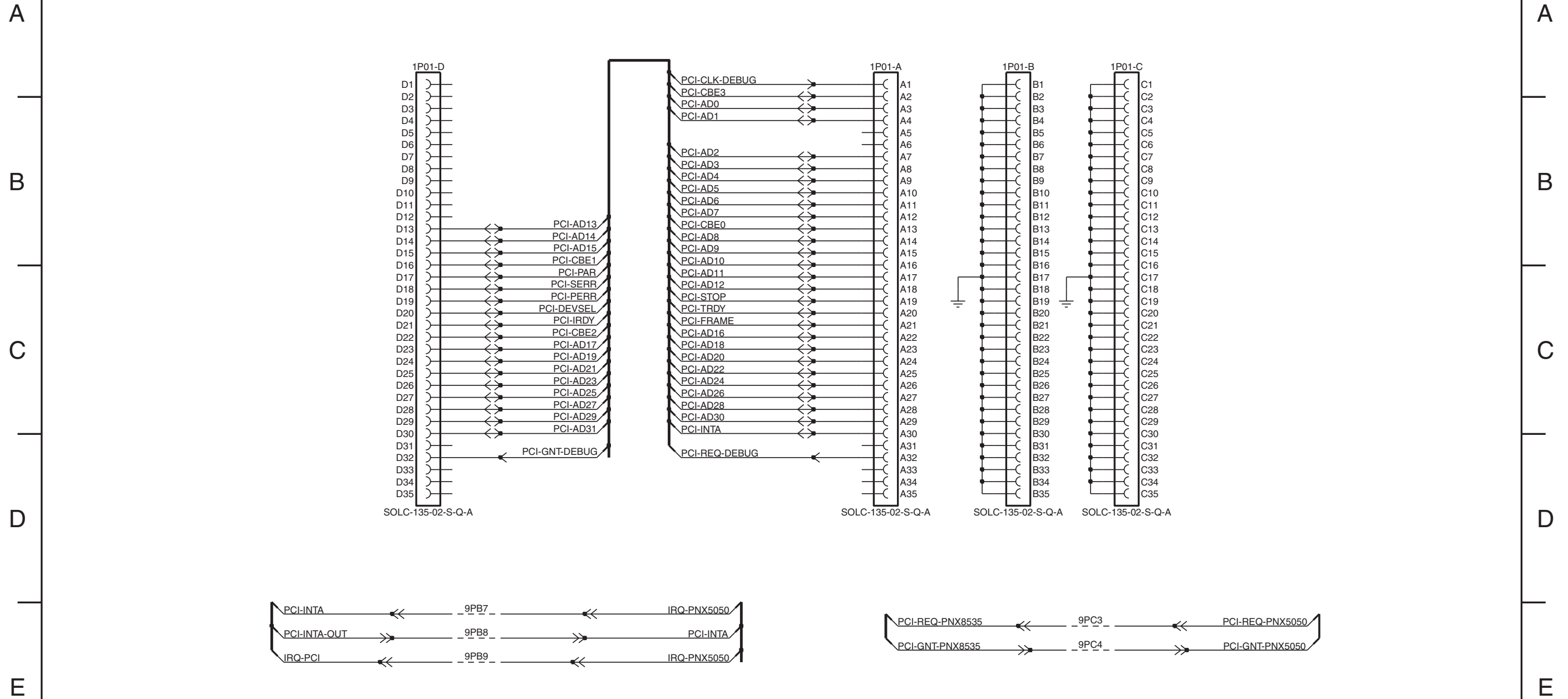
POD: Debug

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POD: DEBUG

B07C

B07C





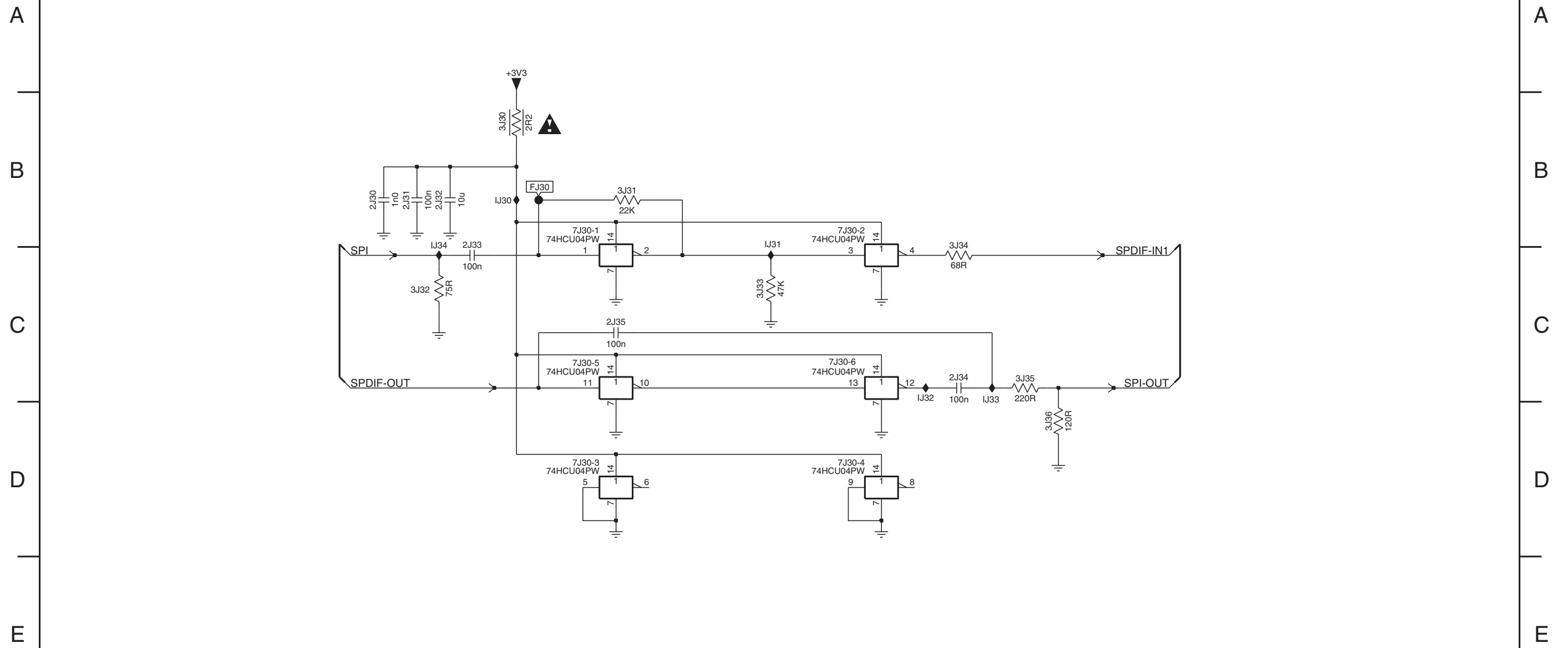


SSB: SPDIF

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2J31 B3	2J33 C3	2J35 C4	3J31 B4	3J33 C5	3J35 C7	7J30-1 B4	7J30-3 D4	7J30-5 C4	FJ30 B3	IJ31 B5	IJ33 C6	

**B08B** SPDIF

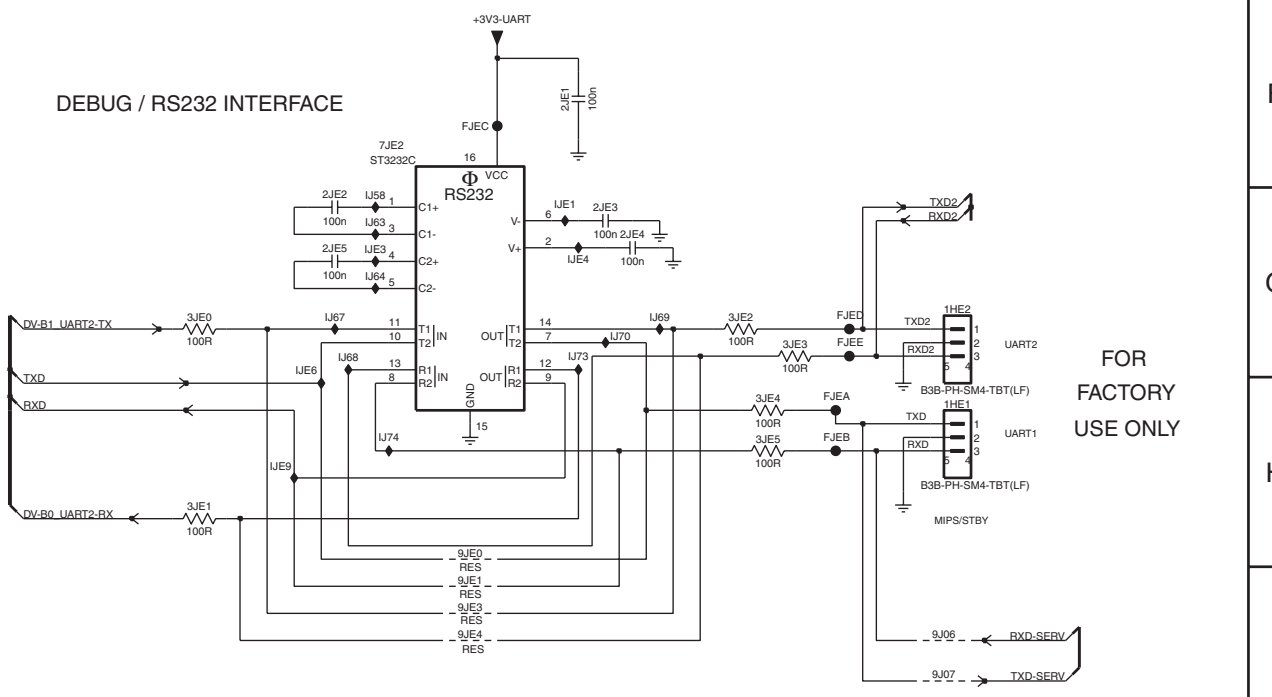
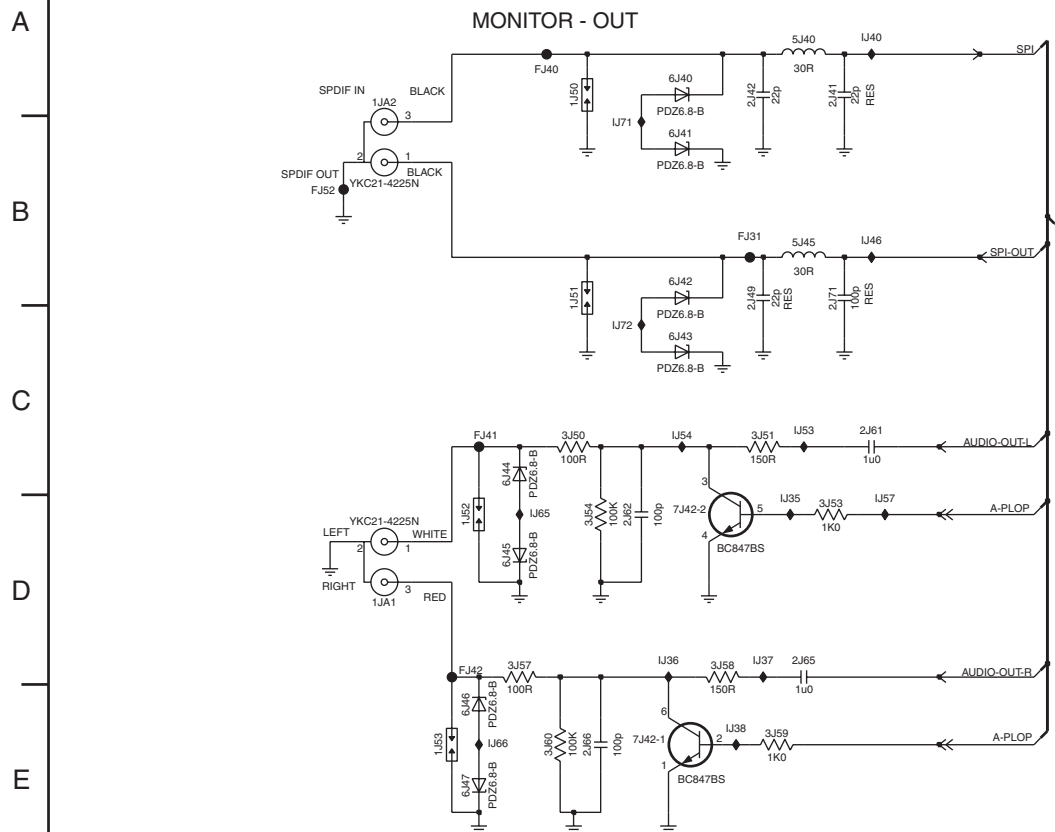
**B08B**



SSB: Externals A

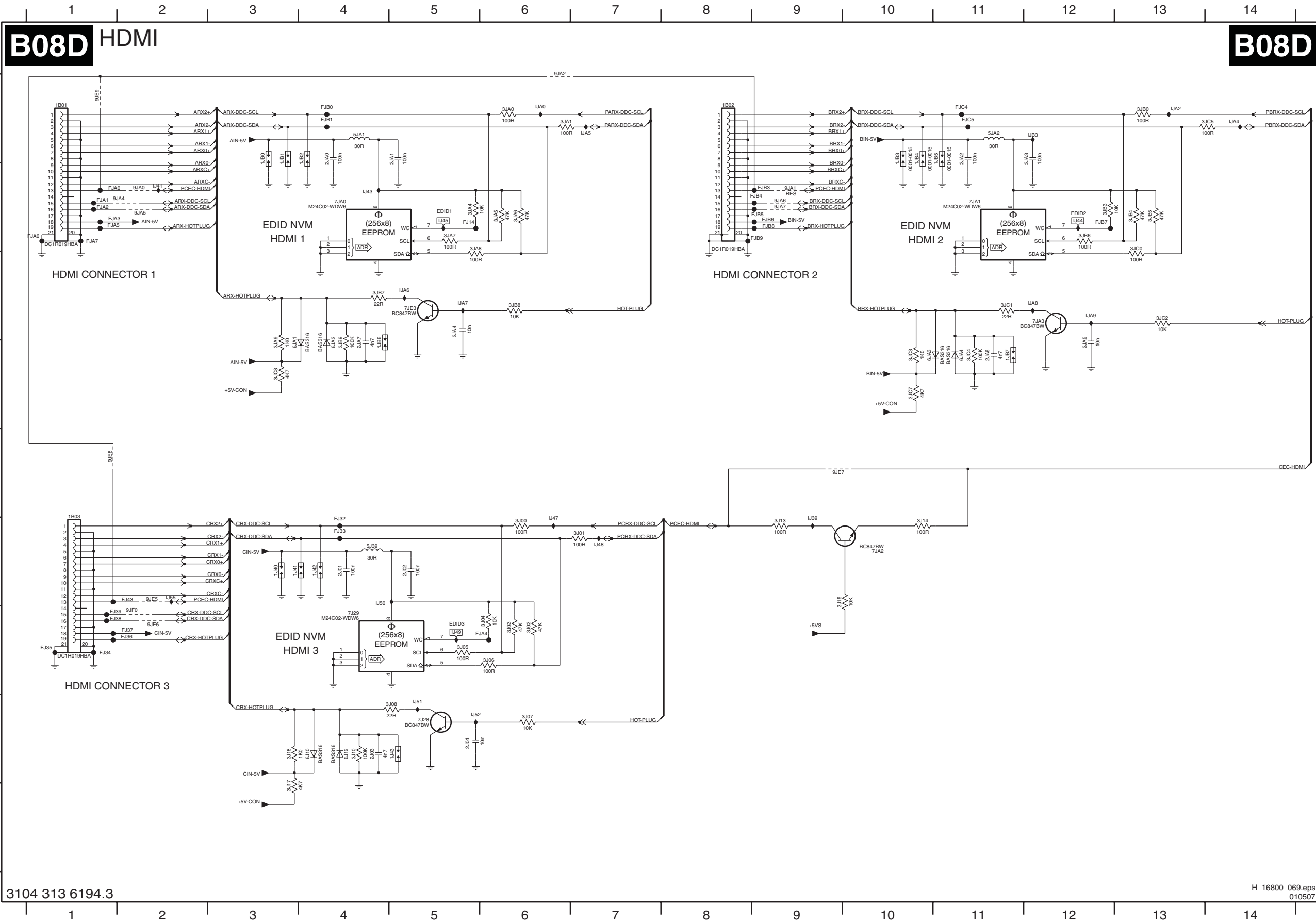
B08C EXTERNALS A

B08C



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- 1J51 B3
- 1J52 D2
- 1J53 E2
- 1JA1 D2
- 1JA2 A2
- 2J20 B7
- 2J41 A4
- 2J42 A4
- 2J49 B4
- 2J61 C4
- 2J62 D3
- 2J65 D4
- 2J66 E3
- 2J71 B4
- 2JE1 F10
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- 2JE3 G10
- 2JE4 G11
- 3J21 B8
- 3J22 B8
- 3J23 B7
- 3J24 B9
- 3J25 B7
- 3J50 C3
- 3J51 C4
- 3J53 D4
- 3J54 D3
- 3J57 D3
- 3J58 D4
- 3J59 E4
- 3J60 E3
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- 3J61 H8
- 3J62 G11
- 3J63 G11
- 3J64 H11
- 3J65 H11
- 5J40 A4
- 5J45 B4
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- 6J31 B6
- 6J40 A3
- 6J41 B3
- 6J42 B3
- 6J43 C3
- 6J44 C3
- 6J45 D3
- 6J46 E2
- 6J47 E2
- 7J42-1 E3
- 7J42-2 D4
- 7J50 B9
- 7J51 B8
- 7J52 F9
- 9J06 H12
- 9J07 H12
- 9J08 H10
- 9J10 H10
- 9J11 I10
- 9J12 I10
- 9J13 I10
- 9J14 I10
- FJ31 B4
- FJ40 A3
- FJ41 C2
- FJ42 D2
- FJ52 B2
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- FJEB H12
- FJEC F10
- FJED G12
- FJEE G12
- J35 D4
- J36 D3
- J37 D4
- J38 E4
- J40 A4
- J46 B4
- J53 C4
- J54 C3
- J56 B8
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- J58 G9
- J59 B8
- J60 B7
- J61 B7
- J62 A8
- J63 G9
- J64 G9
- J65 D3
- J66 E3
- J67 G9
- J68 G9
- J69 G11
- J70 G11
- J71 B3
- J72 C3
- J73 G10
- J74 H9
- JE1 G10
- JE3 G9
- JE4 G10
- JE6 G9
- JE9 H9

SSB: HDMI



- 1B01 A1
- 1B02 A8
- 1B03 F1
- 1J40 F3
- 1J41 F3
- 1J42 F4
- 1J43 H5
- 1J40 A3
- 1J41 A3
- 1J42 A4
- 1J43 A10
- 1J44 A10
- 1J45 B5
- 1J46 A10
- 1J47 F6
- 1J48 F7
- 1J49 G5
- 1J50 F4
- 2J01 F4
- 2J02 F5
- 2J03 H4
- 2J04 H5
- 2J05 A4
- 2J06 A11
- 2J07 A12
- 2J08 C5
- 2J09 D12
- 2J10 D11
- 2J11 D4
- 3J00 F6
- 3J01 F7
- 3J02 G6
- 3J03 G6
- 3J04 G6
- 3J05 G5
- 3J06 G6
- 3J07 H6
- 3J08 H5
- 3J09 H4
- 3J10 F9
- 3J11 F10
- 3J12 I3
- 3J13 H3
- 3J14 A6
- 3J15 A6
- 3J16 B5
- 3J17 B6
- 3J18 B5
- 3J19 D4
- 3J20 B13
- 3J21 C11
- 3J22 C13
- 3J23 D10
- 3J24 D11
- 3J25 A14
- 3J26 D10
- 3J27 D3
- 5J39 F4
- 5J40 A1
- 5J41 A11
- 6J10 H4
- 6J12 H4
- 6J13 D3
- 6J14 D4
- 6J15 D10
- 6J16 D11
- 7J28 H5
- 7J29 G4
- 7J30 B4
- 7J31 B11
- 7J32 F10
- 7J33 C12
- 7J34 C5
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- 9J41 B9
- 9J42 A6
- 9J43 B2
- 9J44 B2
- 9J45 B2
- 9J46 B9
- 9J47 B9
- 9J48 F2
- 9J49 G2
- 9J50 E9
- 9J51 E1
- 9J52 A1
- 9J53 G2
- 9J54 B5
- 9J55 F4
- 9J56 F4
- 9J57 G1
- 9J58 F2
- 9J59 B1
- 9J60 A4
- 9J61 A4
- 9J62 B9
- 9J63 B9
- 9J64 B9
- 9J65 B9

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SSB: HDMI Switch

B08E HDMI SWITCH

B08E

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A

B

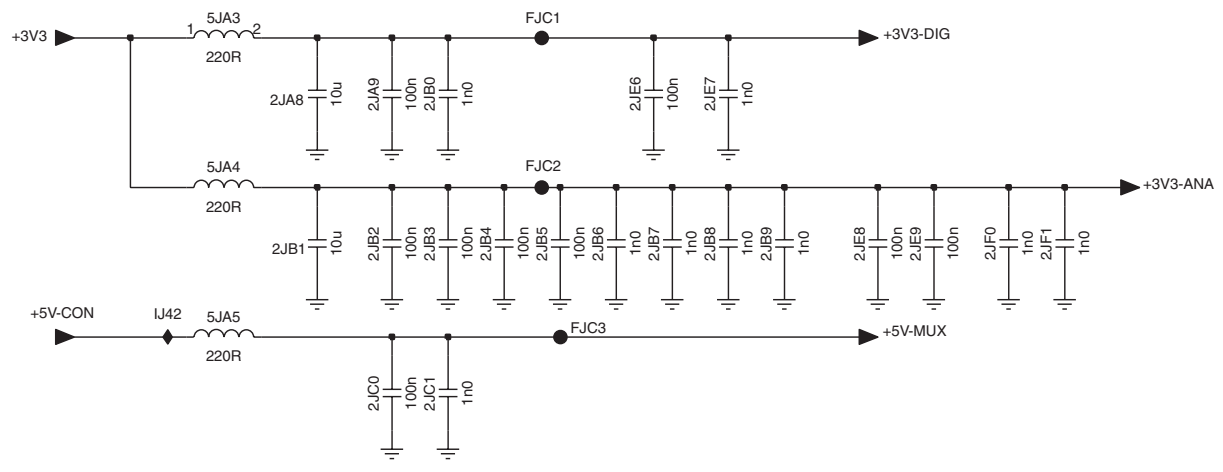
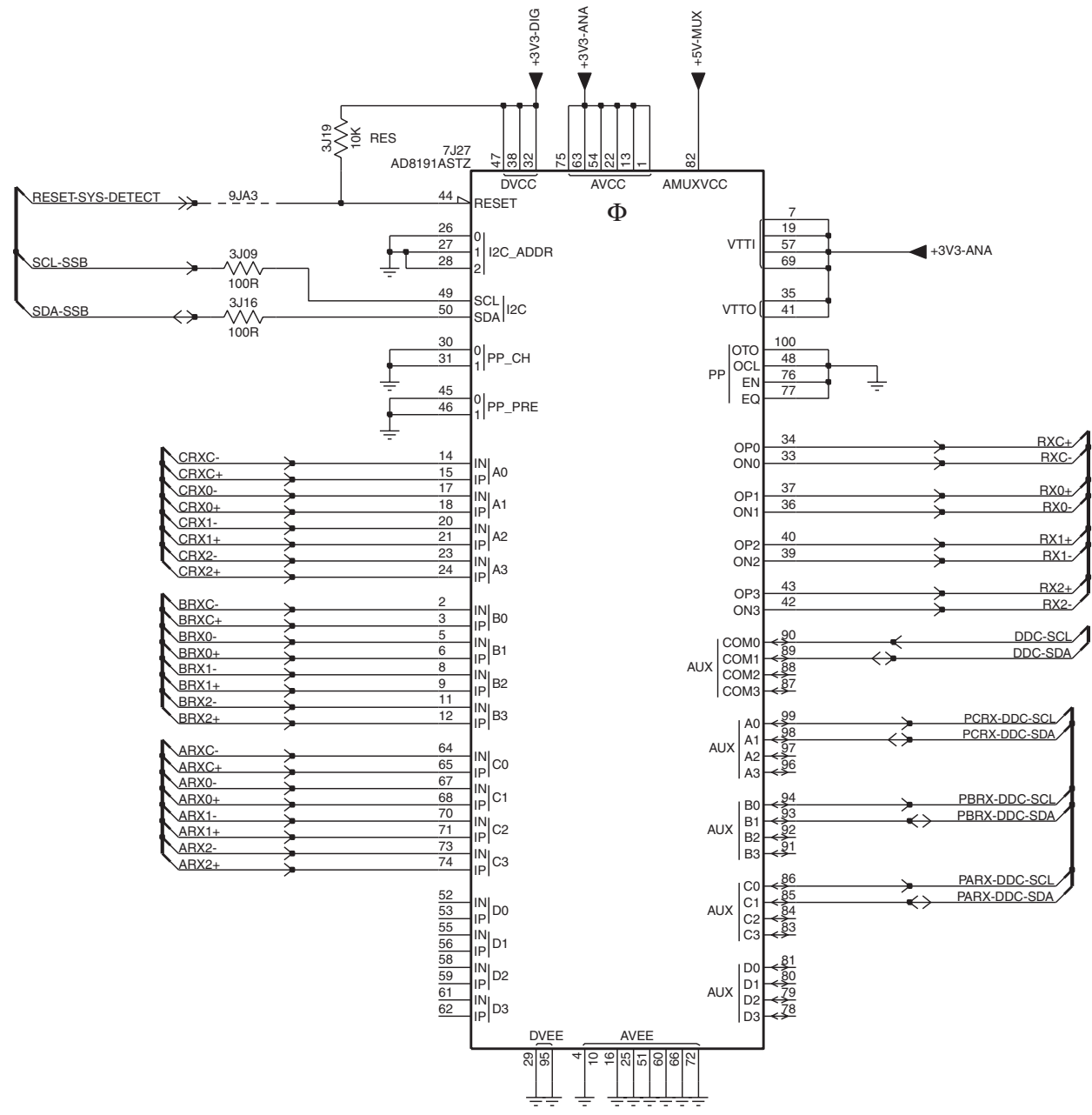
C

D

E

F

- 2JA8 D6
- 2JA9 D7
- 2JB0 D7
- 2JB1 D6
- 2JB2 D7
- 2JB3 D7
- 2JB4 D7
- 2JB5 D7
- 2JB6 D7
- 2JB7 D8
- 2JB8 D8
- 2JB9 D8
- 2JC0 E7
- 2JC1 E7
- 2JE6 D8
- 2JE7 D8
- 2JE8 D8
- 2JE9 D9
- 2JF0 D9
- 2JF1 D9
- 3J09 B1
- 3J16 C1
- 3J19 B2
- 5JA3 D6
- 5JA4 D6
- 5JA5 E6
- 7J27 B2
- 9JA3 B1
- FJC1 D7
- FJC2 D7
- FJC3 E7
- IJ42 E6



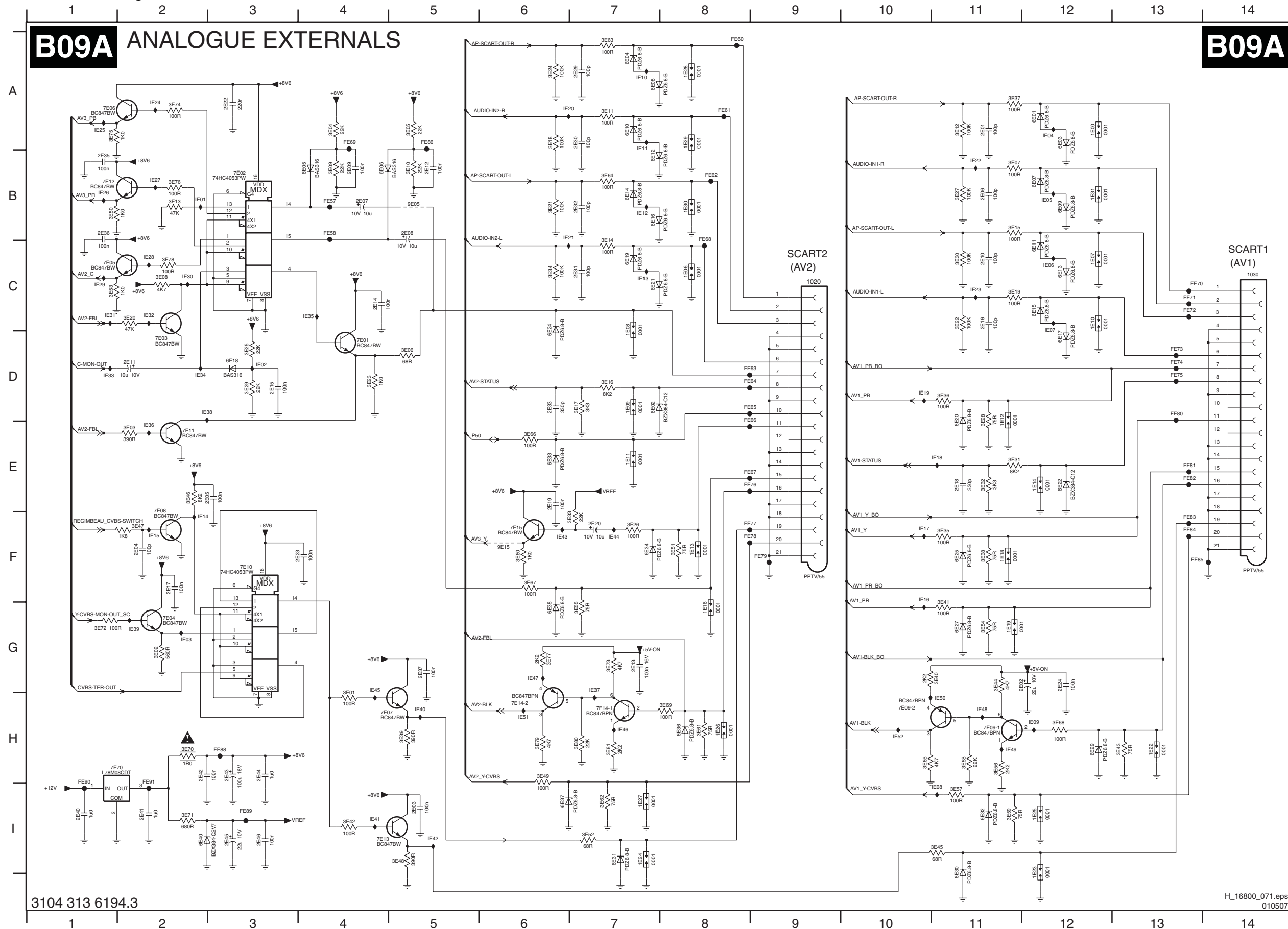
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SSB: Analogue Externals

B09A ANALOGUE EXTERNALS

B09A



1020 C9	3E55 G7	IE03 G2
1030 C14	3E56 H11	IE04 A12
1E00 A12	3E57 I11	IE05 B12
1E06 C8	3E58 H11	IE06 C12
1E07 C12	3E59 I11	IE07 C12
1E08 C7	3E60 F6	IE08 H11
1E09 D7	3E61 H8	IE09 H12
1E10 C12	3E62 I7	IE10 A7
1E11 E7	3E63 A7	IE11 A7
1E12 D11	3E64 B7	IE12 B7
1E13 F8	3E65 H10	IE13 C7
1E14 E12	3E66 E6	IE14 F2
1E16 G8	3E67 F6	IE15 F2
1E18 F11	3E68 H12	IE16 F10
1E19 G11	3E69 H8	IE17 F10
1E22 H13	3E70 H2	IE18 E11
1E23 I12	3E71 I2	IE19 D10
1E24 I7	3E72 G1	IE20 A6
1E25 I12	3E73 G7	IE21 B6
1E26 H8	3E74 A2	IE22 B11
1E27 I7	3E75 A1	IE23 C11
1E28 A8	3E76 B2	IE24 A2
1E29 A8	3E77 G6	IE25 A1
1E30 B8	3E78 C2	IE26 B1
1E31 B2	3E79 H6	IE27 B2
2E01 A11	3E80 H7	IE28 C2
2E02 G12	3E81 H7	IE29 C1
2E03 I5	6E01 A12	IE30 C2
2E04 F2	6E02 D7	IE31 C1
2E05 E3	6E03 A12	IE32 C2
2E06 B11	6E04 A7	IE33 D1
2E07 B4	6E05 B4	IE34 D2
2E08 B5	6E06 B4	IE35 C4
2E09 B4	6E07 B12	IE36 E2
2E10 C11	6E08 A7	IE37 G7
2E11 D2	6E09 B12	IE38 D2
2E12 B5	6E10 A7	IE39 G2
2E13 G7	6E11 C12	IE40 H5
2E14 C4	6E12 B7	IE41 I4
2E15 D3	6E13 C12	IE42 I5
2E16 C11	6E14 B7	IE43 F6
2E17 F2	6E15 C12	IE44 F7
2E18 E11	6E16 B7	IE45 G4
2E19 E6	6E17 D12	IE46 H7
2E20 F7	6E18 D3	IE47 G6
2E22 A3	6E19 C7	IE48 H11
2E23 F4	6E20 D11	IE49 H11
2E24 G12	6E21 C7	IE50 H11
2E29 A7	6E22 E12	IE51 H6
2E30 A7	6E24 C6	IE52 H10
2E31 C7	6E25 F11	
2E32 B7	6E27 G11	
2E33 D6	6E29 H12	
2E35 B1	6E30 I11	
2E36 B1	6E31 I7	
2E37 G5	6E32 I11	
2E40 I1	6E33 E6	
2E41 I2	6E34 F7	
2E42 H2	6E35 G6	
2E43 H3	6E36 H8	
2E44 H3	6E37 H6	
2E45 I3	6E40 I2	
2E46 I3	7E01 D4	
3E01 H4	7E02 B3	
3E02 G2	7E03 D2	
3E03 E2	7E04 G2	
3E04 A4	7E05 C1	
3E05 A5	7E06 A1	
3E06 D5	7E07 H4	
3E07 B11	7E08 F2	
3E08 C2	7E09 -4 H11	
3E09 B4	7E09-2 H10	
3E10 B5	7E10 F3	
3E11 A7	7E11 E2	
3E12 A11	7E12 B1	
3E13 B2	7E13 I4	
3E14 C7	7E14-1 H7	
3E15 B11	7E14-2 H6	
3E16 D7	7E15 F6	
3E17 D7	7E17 H1	
3E18 A6	9E05 B5	
3E19 C11	9E15 F6	
3E20 C2	FE57 B4	
3E21 B6	FE58 B4	
3E22 C11	FE60 A8	
3E23 D4	FE61 A8	
3E24 A6	FE62 B8	
3E25 D3	FE63 D8	
3E26 F7	FE64 D8	
3E27 B11	FE65 D8	
3E28 D11	FE66 E8	
3E29 D3	FE67 E8	
3E30 C11	FE68 C8	
3E31 E11	FE69 A4	
3E32 E11	FE70 C13	
3E33 F7	FE71 C13	
3E34 C6	FE72 C13	
3E35 F11	FE73 D13	
3E36 D11	FE74 D13	
3E37 A11	FE75 D13	
3E38 F11	FE76 E8	
3E39 H5	FE77 F8	
3E40 G11	FE78 F8	
3E41 G11	FE79 F9	
3E42 I4	FE80 D13	
3E43 H13	FE81 E13	
3E44 G11	FE82 E13	
3E45 I11	FE83 F13	
3E46 E2	FE84 F13	
3E47 F2	FE85 F13	
3E48 I5	FE86 A5	
3E49 H6	FE88 H3	
3E50 B1	FE89 I3	
3E51 F8	FE90 I1	
3E52 I7	FE91 I2	
3E53 C1	IE01 B2	
3E54 G11	IE02 D3	

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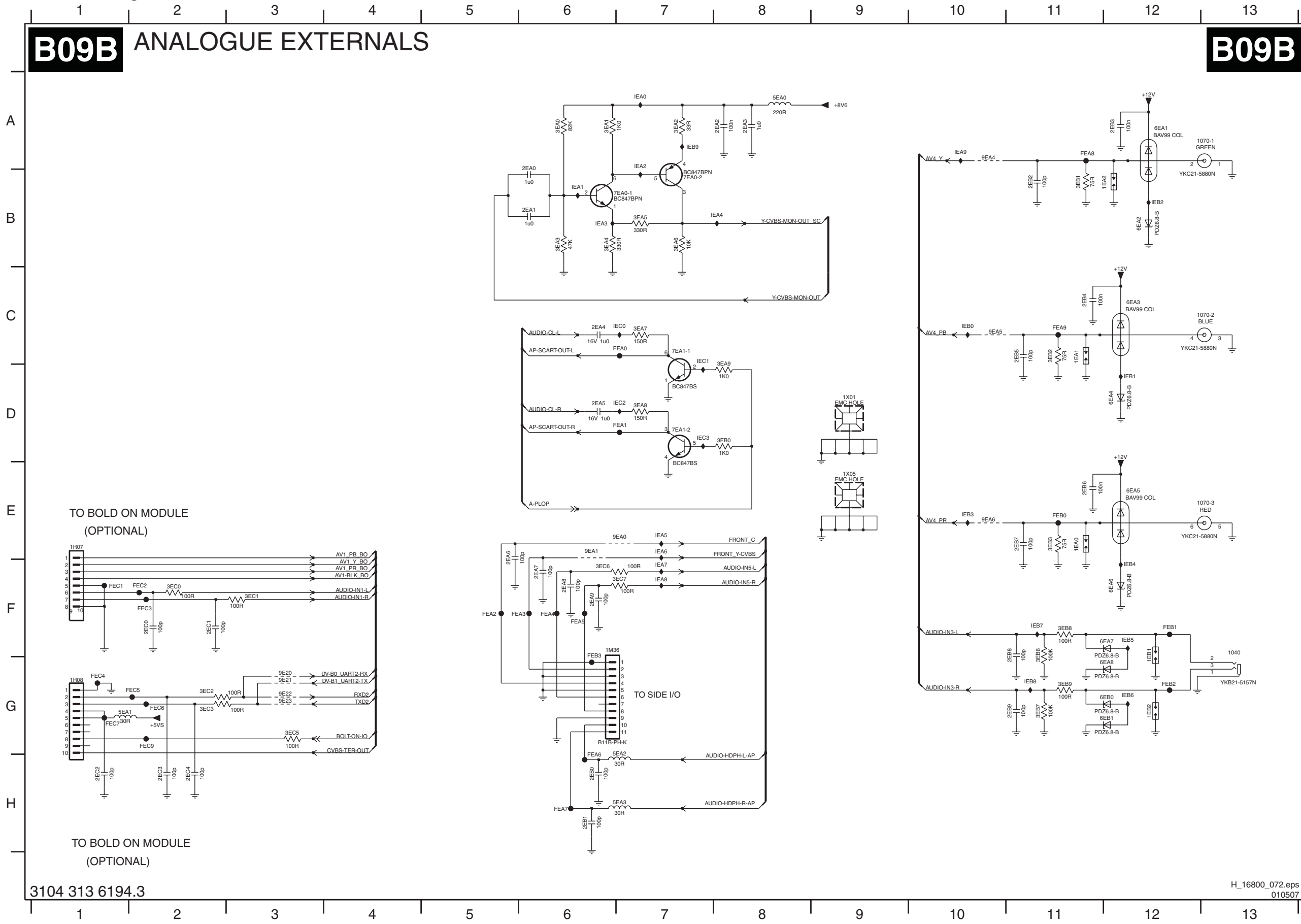
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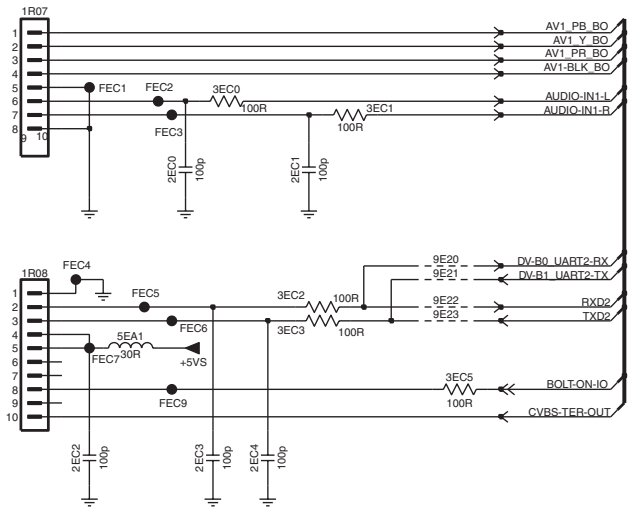
SSB: Analogue Externals

B09B ANALOGUE EXTERNALS

B09B



TO BOLD ON MODULE (OPTIONAL)



TO BOLD ON MODULE (OPTIONAL)

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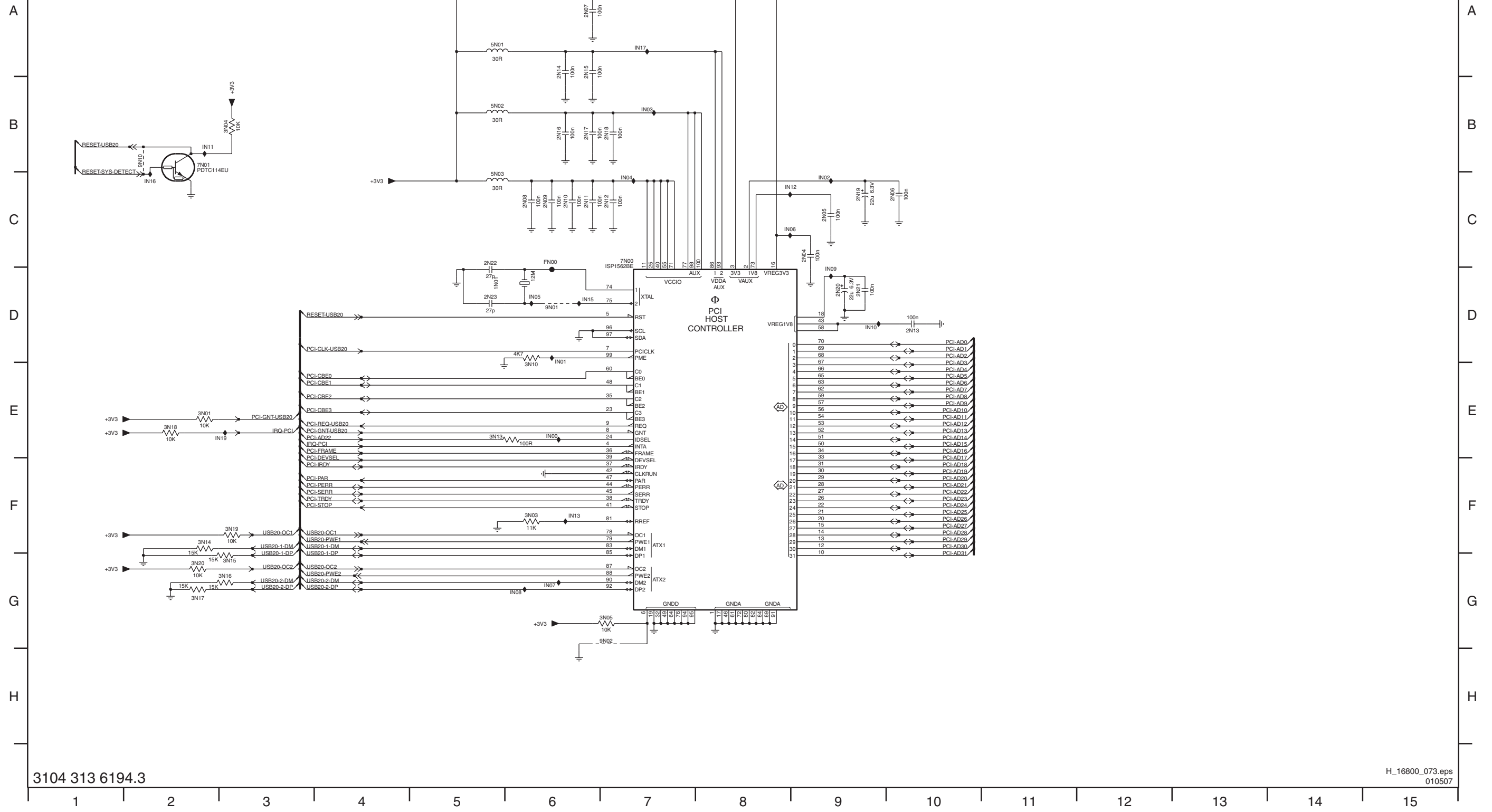
- 1040 F13
- 1070-1 A13
- 1070-2 C13
- 1070-3 E13
- 1EA0 E11
- 1EA1 C11
- 1EA2 B12
- 1EB1 F12
- 1EB2 G12
- 1M36 F6
- 1R07 E1
- 1R08 G1
- 1X01 D9
- 1X05 E9
- 2EA0 B6
- 2EA1 B6
- 2EA2 A8
- 2EA3 A8
- 2EA4 C6
- 2EA5 D6
- 2EA6 E5
- 2EA7 F6
- 2EA8 F6
- 2EA9 F6
- 2EB0 H6
- 2EB1 H6
- 2EB2 B11
- 2EB3 A12
- 2EB4 C11
- 2EB5 C11
- 2EB6 E11
- 2EB7 E11
- 2EB8 F11
- 2EB9 G11
- 2EC0 F2
- 2EC1 F2
- 2EC2 H1
- 2EC3 H2
- 2EC4 H2
- 3EA0 A6
- 3EA1 A6
- 3EA2 A7
- 3EA3 B6
- 3EA4 B6
- 3EA5 B7
- 3EA6 B7
- 3EA7 C7
- 3EA8 D7
- 3EA9 D8
- 3EB0 D8
- 3EB1 B11
- 3EB2 C11
- 3EB3 E11
- 3EB6 F11
- 3EB7 G11
- 3EB8 F11
- 3EB9 G11
- 3EC0 F2
- 3EC1 F3
- 3EC2 G2
- 3EC3 G2
- 3EC5 G3
- 3EC6 F6
- 3EC7 F7
- 5EA0 A8
- 5EA1 G1
- 5EA2 H7
- 5EA3 H7
- 6EA1 A12
- 6EA2 B12
- 6EA3 C12
- 6EA4 D12
- 6EA5 E12
- 6EA6 F12
- 6EA7 F12
- 6EA8 G12
- 6EB0 G12
- 6EB1 G12
- 7EA0-1 B6
- 7EA1-1 C7
- 7EA1-2 D7
- 9E20 G3
- 9E21 G3
- 9E22 G3
- 9E23 G3
- 9EA0 E7
- 9EA1 E6
- 9EA4 A10
- 9EA5 C10
- 9EA6 E10
- FEA0 C7
- FEA1 D7
- FEA2 F5
- FEA3 F5
- FEA4 F6
- FEA5 F6
- FEA6 H6
- FEA7 H6
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- FEA9 C11
- FEB0 E11
- FEB1 F12
- FEB2 G12
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- FEC9 G2
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- IEA4 B8
- IEA5 E7
- IEA6 E7
- IEA7 F7
- IEA8 F7
- IEA9 A10
- IEB0 C10
- IEB1 D12
- IEB2 E12
- IEB3 E10
- IEB4 F12
- IEB5 F12
- IEB6 G12
- IEB7 F11
- IEB8 G11
- IEB9 A7
- IEC0 C7
- IEC1 C7
- IEC2 D7
- IEC3 D7

SSB: USB 2.0

1N01 D5 2N05 C9 2N07 A6 2N09 C6 2N11 C6 2N13 D10 2N15 A6 2N17 B6 2N19 C9 2N21 D9 2N23 D5 3N03 F6 3N05 G7 3N13 E5 3N15 G3 3N17 G2 3N19 F3 5N00 A5 5N02 B5 5N04 A5 7N01 B2 9N02 G7 FN00 C6 IN01 D6 IN03 B7 IN05 D6 IN07 G6 IN09 D9 IN11 B2 IN13 F6 IN15 D6 IN17 A7  
 2N04 C9 2N06 C10 2N08 C6 2N10 C6 2N12 C7 2N14 A6 2N16 B6 2N18 B7 2N20 D9 2N22 C5 3N01 E2 3N04 B3 3N10 D6 3N14 F2 3N16 G3 3N18 E2 3N20 G2 5N01 A5 5N03 C5 7N00 C7 9N10 D6 9N10 B2 IN00 E6 IN02 C9 IN04 C7 IN06 C8 IN08 G6 IN10 D9 IN12 C8 IN14 A7 IN16 C2 IN19 E3

B10A USB 2.0

B10A



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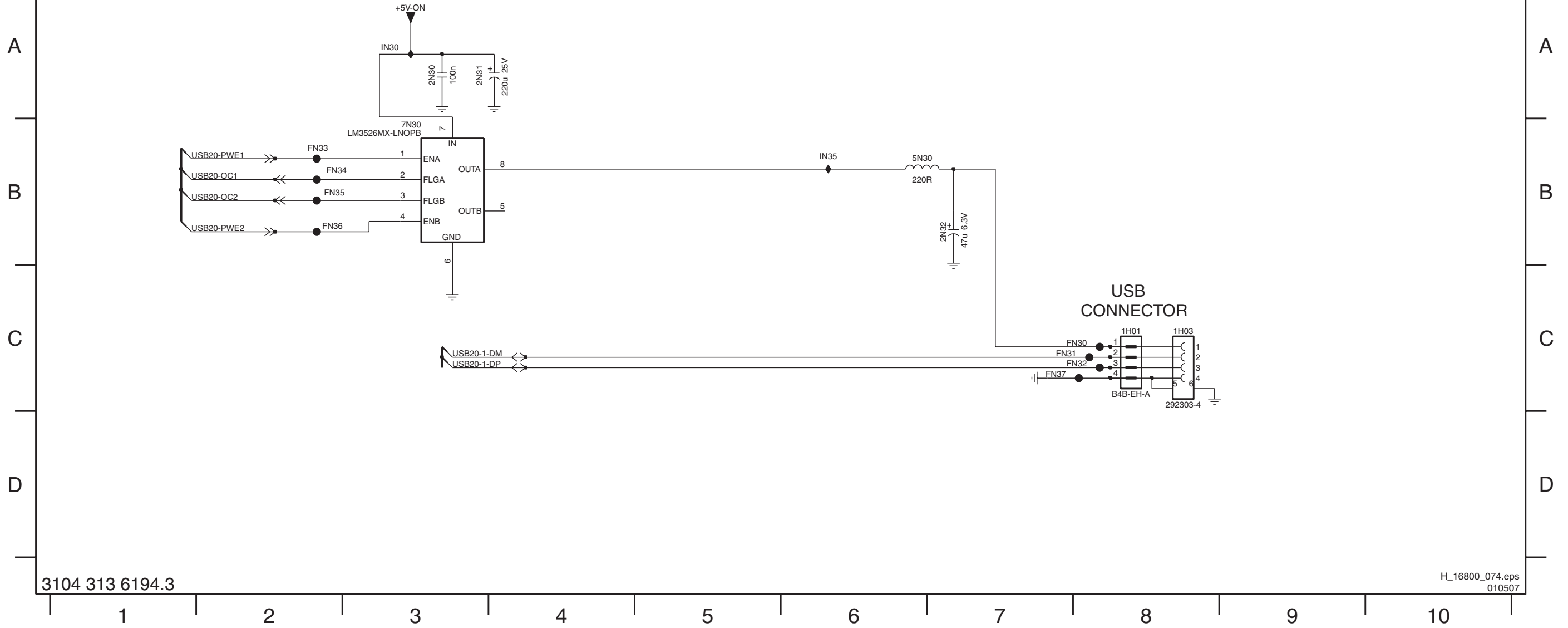
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SSB: USB 2.0

1H01 C8 1H03 C8 2N30 A3 2N31 A3 2N32 B7 5N30 B6 7N30 B3 FN30 C8 FN31 C7 FN32 C8 FN33 B2 FN34 B2 FN35 B2 FN36 B2 FN37 C7 IN30 A3 IN35 B6

**B10B** USB 2.0

**B10B**



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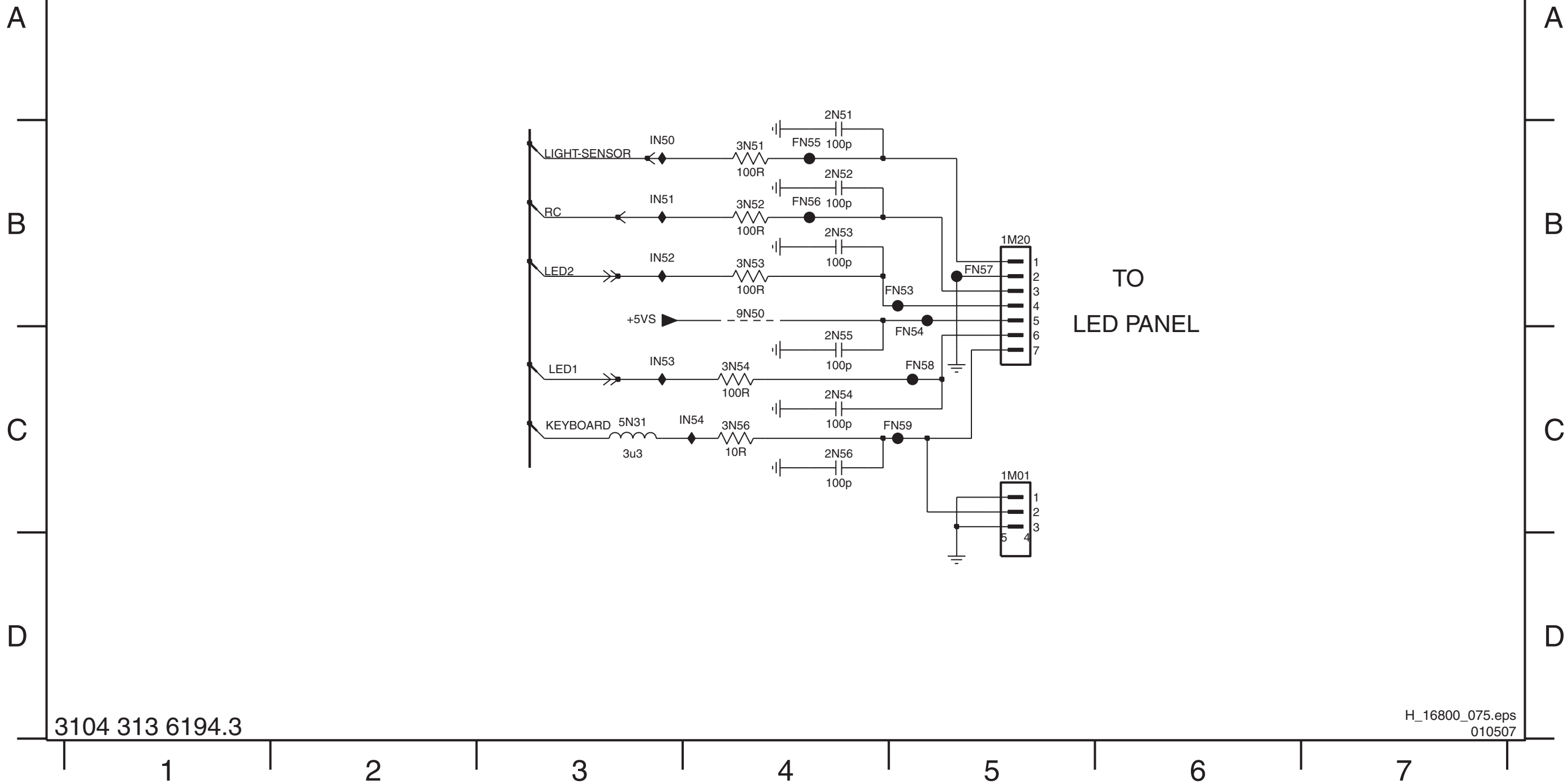
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SSB: LED Panel Connector

1M01 C5 2N51 A4 2N53 B4 2N55 C4 3N51 B4 3N53 B4 3N56 C4 9N50 B4 FN54 C5 FN56 B4 FN58 C5 IN50 B3 IN52 B3 IN54 C4  
 1M20 B5 2N52 B4 2N54 C4 2N56 C4 3N52 B4 3N54 C4 5N31 C3 FN53 B5 FN55 B4 FN57 B5 FN59 C5 IN51 B3 IN53 C3

**B10C** LED PANEL CONNECTOR

**B10C**



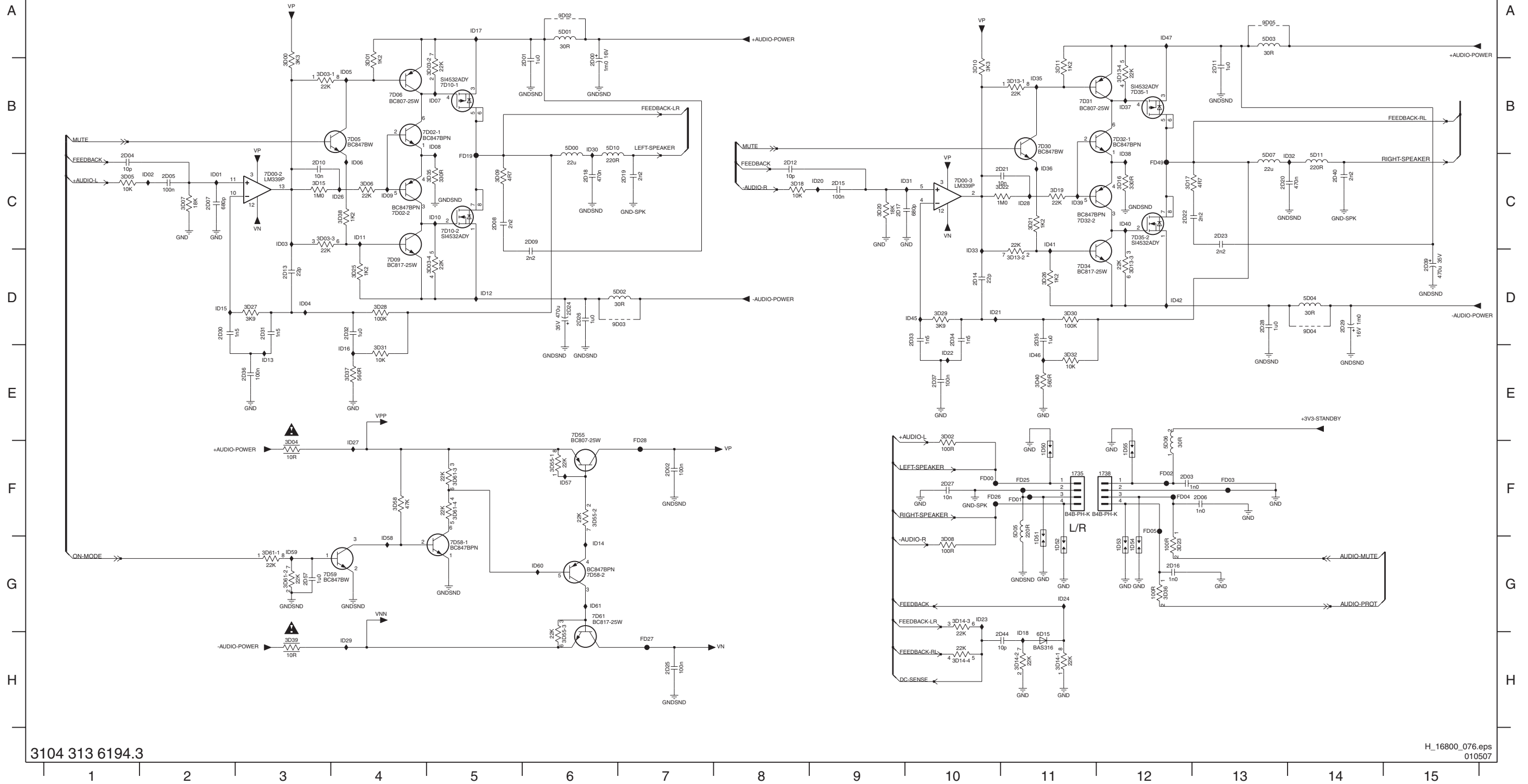
TO  
LED PANEL

SSB: Audio Left / Right

1735 F11	1D55 F12	2D06 F13	2D13 D3	2D20 C13	2D27 F10	2D34 D10	2D57 G3	3D03-4 D5	3D10 B10	3D14-2 H11	3D19 C11	3D27 D3	3D36 G12	3D55-3 H6	5D01 A6	5D10 B6	7D05 B4	7D32-1 B12	7D58-2 G6	FD00 F10	FD25 F11	ID03 C3	ID10 C5	ID17 A5	ID26 C4	ID33 D10	ID41 C11	ID59 G3
1738 F12	2D00 B6	2D07 C2	2D14 D10	2D21 C11	2D28 D13	2D35 D11	3D00 B3	3D04 F3	3D11 B11	3D14-3 G10	3D20 C9	3D28 D4	3D37 E4	3D58 F4	5D02 D7	5D11 C14	7D06 B4	7D32-2 C11	7D59 G3	FD01 F11	FD26 F10	ID04 D3	ID11 C4	ID18 H11	ID27 F4	ID35 B11	ID42 D12	ID60 G6
1D50 F11	2D01 B6	2D08 C5	2D15 C9	2D22 C12	2D29 D14	2D36 E3	3D01 B4	3D05 C1	3D13-1 B11	3D14-4 H10	3D21 C11	3D29 D10	3D38 C4	3D61-1 G3	5D03 A13	5D15 H11	7D09 D4	7D34 D11	7D61 G6	FD02 F12	FD27 H7	ID05 B4	ID12 D5	ID20 C9	ID28 C11	ID36 C11	ID45 D10	ID61 G6
1D51 G11	2D02 F7	2D09 C6	2D16 G12	2D23 C13	2D30 D2	2D37 E10	3D02 E10	3D06 C4	3D13-2 D11	3D15 C3	3D22 C11	3D30 D11	3D39 H3	3D61-2 G3	5D04 D14	7D00-2 C3	7D10-1 B5	7D35-1 B12	9D02 A6	FD03 F13	FD28 F7	ID06 C4	ID13 E3	ID21 D10	ID29 H4	ID37 B12	ID46 E11	
1D52 G11	2D03 F12	2D10 C3	2D17 C9	2D24 D7	2D31 D3	2D39 D15	3D03-1 B3	3D07 C2	3D13-3 D12	3D16 C12	3D23 G12	3D31 E4	3D40 E11	3D61-3 F5	5D05 F11	7D00-3 C10	7D10-2 C5	7D35-2 C12	9D03 D7	FD04 F12	FD49 C12	ID07 B5	ID14 G6	ID22 E10	ID30 B6	ID38 C12	ID47 A12	
1D53 G12	2D04 C1	2D11 B13	2D18 C6	2D25 H7	2D32 D4	2D40 C14	3D03-2 B5	3D08 G10	3D13-4 B12	3D17 C12	3D25 D4	3D32 E11	3D55-1 F6	3D61-4 F5	5D06 F12	7D02-1 B4	7D30 B11	7D55 E6	9D04 D14	FD05 F12	FD05 F12	ID08 B5	ID15 D2	ID23 G10	ID31 C10	ID39 C11	ID57 F6	
1D54 G12	2D05 C2	2D12 C8	2D19 C7	2D26 D6	2D33 D10	2D44 H11	3D03-3 C3	3D09 C5	3D14-1 H11	3D18 C8	3D26 D11	3D35 C5	3D55-2 F6	5D00 B6	5D07 C13	7D02-2 C4	7D31 B11	7D58-1 G5	9D05 A13	FD19 C5	ID02 C2	ID09 C4	ID16 E4	ID24 G11	ID32 C14	ID40 C12	ID58 G4	

B11A AUDIO LEFT / RIGHT

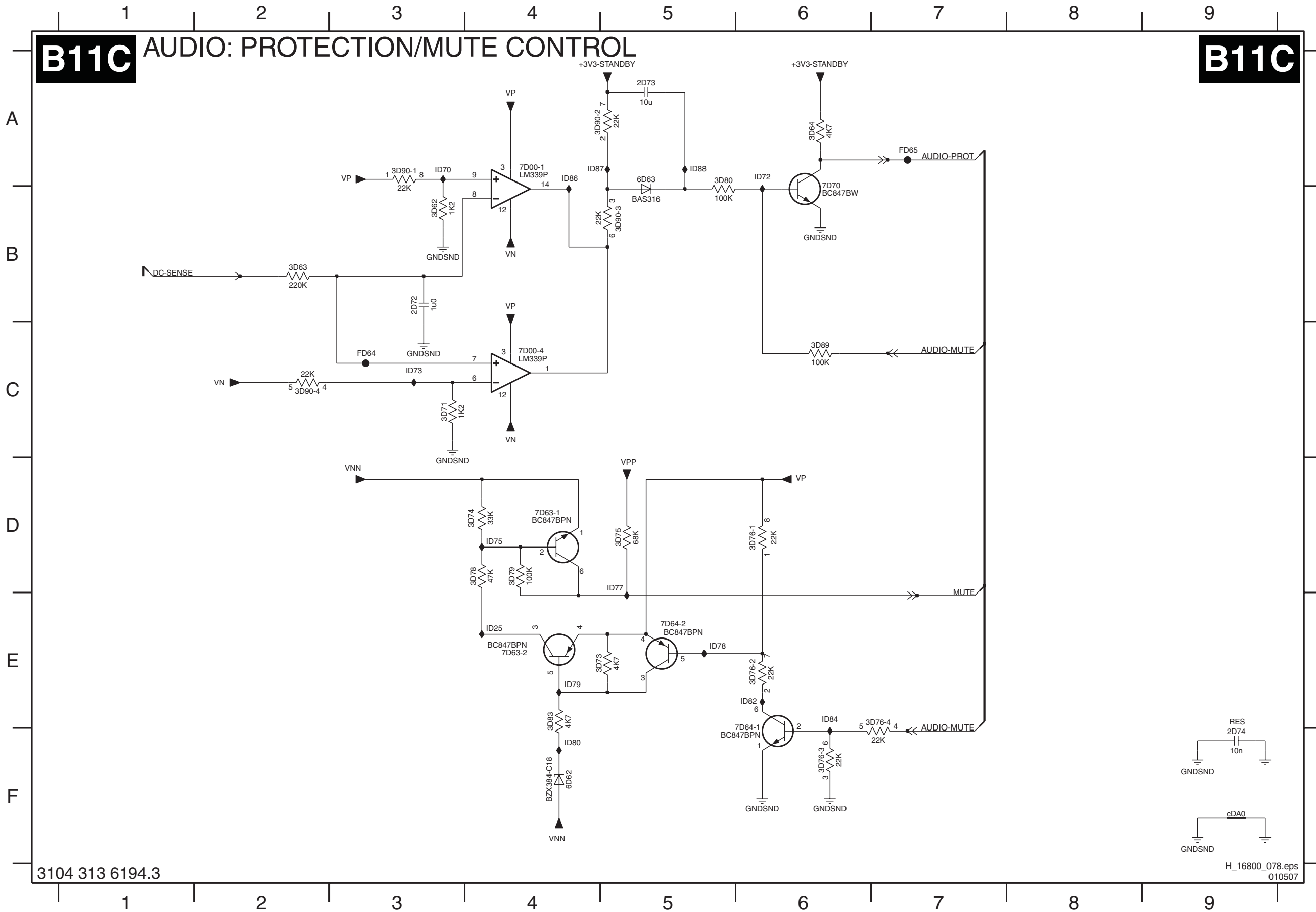
B11A



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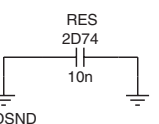
SSB: Audio Protection/Mute Control



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**B11C**

**AUDIO: PROTECTION/MUTE CONTROL**



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- 2D72 B3
- 2D73 A5
- 2D74 F9
- 3D62 B3
- 3D63 B2
- 3D64 A6
- 3D71 C3
- 3D73 E5
- 3D74 D4
- 3D75 D5
- 3D76-1 D6
- 3D76-2 E6
- 3D76-3 F6
- 3D76-4 E7
- 3D78 D4
- 3D79 D4
- 3D80 A5
- 3D83 E4
- 3D89 C6
- 3D90-1 A3
- 3D90-2 A4
- 3D90-3 B5
- 3D90-4 C2
- 6D62 F4
- 6D63 A5
- 7D00-1 A4
- 7D00-4 C4
- 7D63-1 D4
- 7D63-2 E4
- 7D64-1 F6
- 7D64-2 E5
- 7D70 B6
- FD64 C3
- FD65 A7
- ID25 E4
- ID70 A3
- ID72 A6
- ID73 C3
- ID75 D4
- ID77 D5
- ID78 E5
- ID79 E4
- ID80 F4
- ID82 E6
- ID84 E6
- ID86 A4
- ID87 A4
- ID88 A5
- cDA0 F9



SSB: Diversity List

Table with 4 columns: ITEM, DIAGRAM, and two columns of alphanumeric codes (e.g., 2003 B02A, 2004 B02A, etc.). The table lists a wide variety of part numbers and their corresponding diagram identifiers.





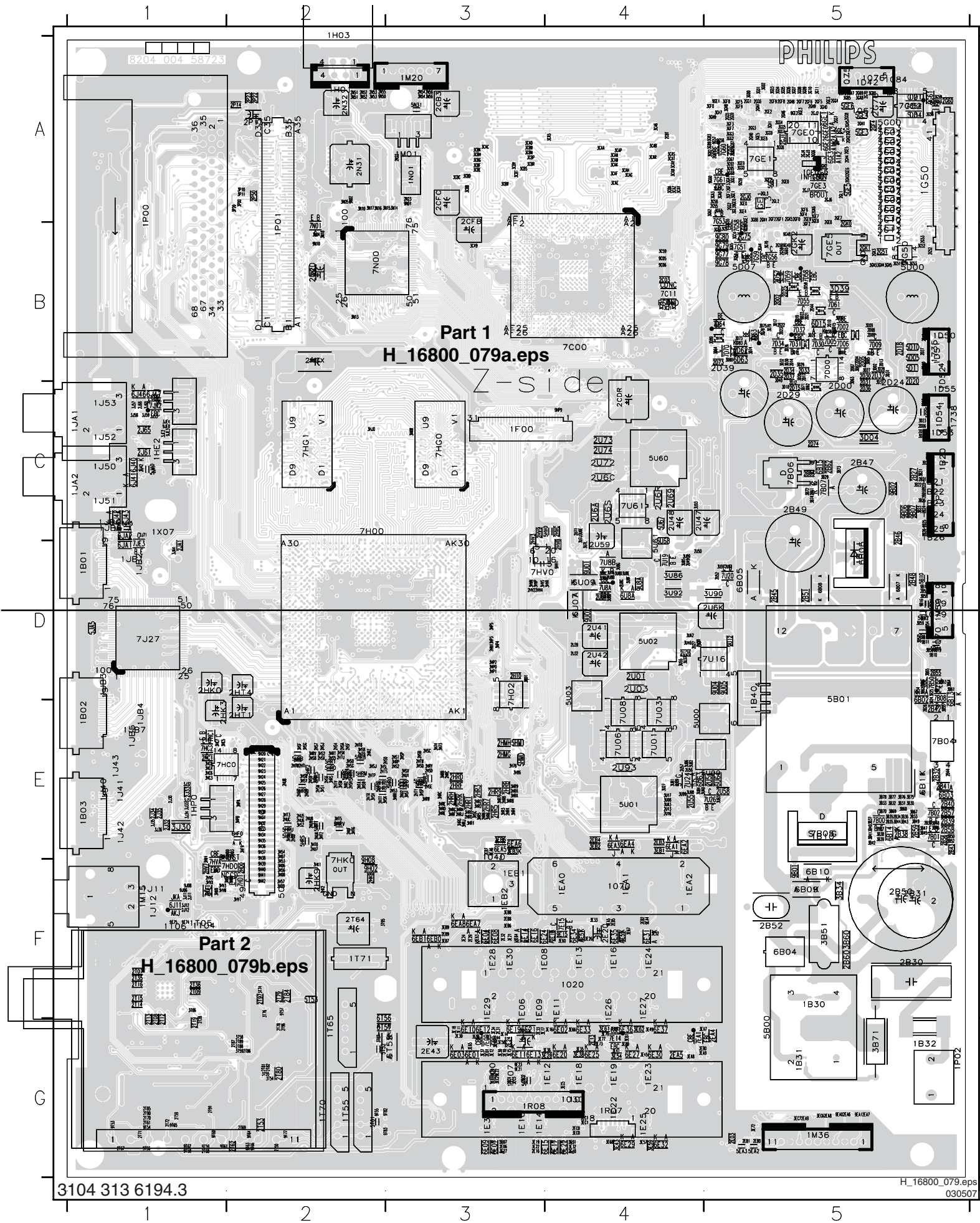


SSB: SRP List Part 2

Netname	Schematic	PC1-AD18	B10A (1x)	PC1-CBE2	B05A (1x)	PCMCIA-D7	B07A (1x)	RxP30B-_B7	B06A (1x)	SUPPLY-FAULT	B04A (2x)	VPP	B11A (1x)
MOCLK-CI	B07A (3x)	PC1-AD19	B04F (1x)	PC1-CBE2	B07A (1x)	PCMCIA-D7	B07B (1x)	RxP30B-_B7	B06C (1x)	SUPPLY-FAULT	B07A (1x)	VPP	B11C (1x)
MOCLK-POD_A14-CI	B07A (2x)	PC1-AD19	B05A (1x)	PC1-CBE2	B07C (1x)	PCMCIA-VCC	B07A (3x)	RxP30B+_B6	B06A (1x)	TDIF-AGC	B07C (1x)	VREF	B09A (2x)
MUTE	B11A (2x)	PC1-AD19	B07C (1x)	PC1-CBE3	B10A (1x)	PCMCIA-VPP	B07B (3x)	RxP30B+_B6	B06C (1x)	TDIF-AGC	B03B (1x)	VREF-DDR	B05B (3x)
MUTE	B11C (1x)	PC1-AD19	B07C (1x)	PC1-CBE3	B04F (1x)	PCRX-DDC-SCL	B08D (1x)	RxP30C+_B5	B05C (1x)	TDIF-IN-N	B03A (1x)	VREF-PNX5050	B05B (2x)
NAND-AD(0)	B04Q (2x)	PC1-AD2	B04F (1x)	PC1-CBE3	B05A (1x)	PCRX-DDC-SCL	B08E (1x)	RxP30C+_B5	B05C (1x)	TDIF-IN-N	B03B (1x)	VSW	B02A (1x)
NAND-AD(1)	B04Q (2x)	PC1-AD2	B04F (1x)	PC1-CBE3	B07C (1x)	PCRX-DDC-SDA	B08D (1x)	RxP30C+_B5	B06A (1x)	TDIF-IN-P	B03A (1x)	VSW	B02B (1x)
NAND-AD(2)	B04Q (2x)	PC1-AD2	B05A (1x)	PC1-CBE3	B10A (1x)	PCRX-DDC-SDA	B08E (1x)	RxP30C+_B5	B06C (1x)	TDIF-IN-P	B03B (1x)	V-SYNC-VGA	B04K (1x)
NAND-AD(3)	B04Q (2x)	PC1-AD2	B07B (1x)	PC1-CLK-DEBUG	B04E (2x)	PNX5050-GPI036	B05C (1x)	RxP30C+_B4	B05C (1x)	TUN-AGC	B03A (1x)	VTT-TERM-DDR	B04G (2x)
NAND-AD(4)	B04Q (2x)	PC1-AD2	B07C (1x)	PC1-CLK-OUT	B07C (1x)	PNX5050-GPI039	B05C (1x)	RxP30C+_B4	B06A (1x)	TUN-AGC	B03B (3x)	XIO-ACK	B04F (1x)
NAND-AD(5)	B04Q (2x)	PC1-AD2	B10A (1x)	PC1-CLK-OUT	B04E (3x)	PNX5050-GPI044	B04C (3x)	RxP30C+_B4	B06C (1x)	TUN-AGC-MON	B03A (1x)	XIO-ACK	B04Q (2x)
NAND-AD(6)	B04Q (2x)	PC1-AD20	B04F (1x)	PC1-CLK-PNX5050_CLK-MOP	B05A (1x)	PNX5050-RST-OUTn	B02C (2x)	RxP30CLK+_B2	B05C (1x)	TUN-AGC-MON	B03B (1x)	XIO-AD25	B04F (1x)
NAND-AD(7)	B04Q (2x)	PC1-AD20	B05A (1x)	PC1-CLK-PNX5050_CLK-MOP	B05A (1x)	POD-MODE	B02A (1x)	RxP30CLK+_B2	B06A (1x)	TUN-TDA-SCL	B03A (1x)	XIO-SEL-NAND	B04F (1x)
NAND-AL7	B04Q (2x)	PC1-AD20	B07C (1x)	PC1-CLK-PNX8535	B04E (2x)	POD-MODE	B02B (1x)	RxP30CLK+_B2	B06C (1x)	TUN-TDA-SCL	B03B (1x)	XIO-SEL-NAND	B04Q (2x)
NAND-CLE	B04Q (2x)	PC1-AD20	B10A (1x)	PC1-CLK-PNX8535	B04F (1x)	POD-MODE	B04B (1x)	RxP30CLK+_B3	B05C (1x)	TUN-TDA-SDA	B03A (1x)	Y-CVBS-MON-OUT	B04K (1x)
NAND-REN	B04Q (2x)	PC1-AD21	B04F (1x)	PC1-CLK-USB20	B04E (2x)	POD-MODE	B04D (1x)	RxP30CLK+_B3	B06A (1x)	TUN-TDA-SDA	B03B (1x)	Y-CVBS-MON-OUT	B09B (1x)
NAND-WEN	B04Q (2x)	PC1-AD21	B05A (1x)	PC1-CLK-USB20	B10A (1x)	POD-MODE	B06B (1x)	RxP30CLK+_B3	B06C (1x)	TUN-TDA-SDA	B04O (2x)	Y-CVBS-MON-OUT_SC	B09A (1x)
ON-MODE	B02B (1x)	PC1-AD21	B07C (1x)	PC1-DEVEL	B04F (2x)	POWER-OK-DISPLAY	B01A (1x)	RxP30D+_B1	B05C (1x)	Tx85350A+	B04O (2x)	Y-CVBS-MON-OUT_SC	B09B (1x)
ON-MODE	B02B (1x)	PC1-AD21	B10A (1x)	PC1-DEVEL	B05A (1x)	POWER-OK-DISPLAY	B04A (1x)	RxP30D+_B1	B06A (1x)	Tx85350B+	B04O (2x)		
ON-MODE	B04B (1x)	PC1-AD22	B04F (1x)	PC1-DEVEL	B07C (1x)	PROT-DC	B02A (1x)	RxP30D+_B1	B06C (1x)	Tx85350B+	B04O (2x)		
ON-MODE	B04D (1x)	PC1-AD22	B05A (1x)	PC1-DEVEL	B10A (1x)	PROT-DC	B02C (2x)	RxP30D+_B0	B05C (1x)	Tx85350C+	B04O (2x)		
ON-MODE	B06B (1x)	PC1-AD22	B07A (1x)	PC1-FRAME	B04F (2x)	PSEN	B04A (2x)	RxP30D+_B0	B06A (1x)	Tx85350C+	B04O (2x)		
ON-MODE	B11A (1x)	PC1-AD22	B07C (1x)	PC1-FRAME	B05A (1x)	PSEN	B04B (1x)	RxP30D+_B0	B06C (1x)	Tx85350CLK+	B04O (2x)		
OVP	B02A (1x)	PC1-AD22	B10A (2x)	PC1-FRAME	B07C (1x)	PWM-3	B06C (2x)	RxP30E+_G9	B05C (1x)	Tx85350CLK+	B04O (2x)		
OVP	B02C (1x)	PC1-AD23	B04F (1x)	PC1-FRAME	B10A (1x)	PWM-OR	B06C (2x)	RxP30E+_G9	B06A (1x)	Tx85350D+	B04O (2x)		
P0.0	B04A (2x)	PC1-AD23	B05A (1x)	PC1-GNT	B05A (1x)	RC	B04A (2x)	RxP30E+_G8	B06C (1x)	Tx85350D+	B04O (2x)		
P0.0	B04B (1x)	PC1-AD23	B07A (1x)	PC1-GNT-B	B04F (2x)	RC	B10C (1x)	RxP30E+_G8	B05C (1x)	Tx85350E+	B04O (2x)		
P0.1	B04A (2x)	PC1-AD23	B07C (1x)	PC1-GNT-DEBUG	B05A (1x)	REGIMBEAU_CVBS-SWITCH	B04B (1x)	RxP30E+_G8	B06A (1x)	Tx85350E+	B04O (2x)		
P0.1	B04B (1x)	PC1-AD23	B10A (1x)	PC1-GNT-DEBUG	B07C (1x)	REGIMBEAU_CVBS-SWITCH	B09A (1x)	RxP30E+_G8	B06C (1x)	TXD	B04C (1x)		
P0.2	B04A (2x)	PC1-AD24	B04F (2x)	PC1-GNT-PNX5050	B05A (1x)	REGIMBEAU_CVBS-SWITCH	B04B (1x)	RxP30E+_G8	B05C (1x)	TXD	B08A (1x)		
P0.2	B04B (1x)	PC1-AD24	B04Q (1x)	PC1-GNT-PNX5050	B07C (1x)	RESET-NVM_WP-NANDFLASH	B04C (1x)	RxP30R0	B06C (1x)	TXD	B08A (1x)		
P0.3	B04A (2x)	PC1-AD24	B05A (1x)	PC1-GNT-PNX5050	B04F (1x)	RESET-NVM_WP-NANDFLASH	B04Q (1x)	RxP30R1	B05C (1x)	TXD2	B08C (1x)		
P0.3	B04B (1x)	PC1-AD24	B07B (1x)	PC1-GNT-PNX8535	B07C (1x)	RESET-NVM_WP-NANDFLASH	B04Q (2x)	RxP30R1	B06C (1x)	TXD2	B09B (1x)		
P0.4	B04A (2x)	PC1-AD24	B07C (1x)	PC1-GNT-USB20	B04F (1x)	RESET-NVM_WP-NANDFLASH-INV	B04B (1x)	RxP30R2	B05C (1x)	TXD-MIPS	B04C (1x)		
P0.4	B04B (1x)	PC1-AD24	B10A (1x)	PC1-GNT-USB20	B05A (1x)	RESET-NVM_WP-NANDFLASH-INV	B04D (1x)	RxP30R2	B06C (1x)	TXD-MIPS	B04E (1x)		
P0.5	B04A (2x)	PC1-AD25	B04F (1x)	PC1-GNT-USB20	B10A (2x)	RESET-NVM_WP-NANDFLASH-INV	B04A (1x)	RxP30R3	B05C (1x)	TXD-SERV	B08A (1x)		
P0.5	B04B (1x)	PC1-AD25	B04Q (1x)	PC1-GNT-USB20	B07C (3x)	RESET-NVM_WP-NANDFLASH-INV	B04B (1x)	RxP30R3	B06C (1x)	TXD-SERV	B08C (1x)		
P0.6	B04A (2x)	PC1-AD25	B05A (2x)	PC1-INTA-OUT	B04F (1x)	RESET-SYS-DETECT	B03A (1x)	RxP30R4	B05C (1x)	TXD-UP	B04A (2x)		
P0.6	B04B (1x)	PC1-AD25	B07B (1x)	PC1-INTA-OUT	B07C (1x)	RESET-SYS-DETECT	B04A (2x)	RxP30R4	B06C (1x)	TXD-UP	B04C (1x)		
P0.7	B04A (2x)	PC1-AD25	B07C (1x)	PC1-IRDY	B04F (2x)	RESET-SYS-DETECT	B04E (2x)	RxP30R5	B05C (1x)	TXEA-	B06A (1x)		
P0.7	B04B (1x)	PC1-AD25	B10A (1x)	PC1-IRDY	B05A (1x)	RESET-SYS-DETECT	B05A (2x)	RxP30R5	B06C (1x)	TXEA-	B06C (1x)		
P2.0	B04A (2x)	PC1-AD26	B04F (1x)	PC1-IRDY	B07C (1x)	RESET-SYS-DETECT	B08E (1x)	RxP30V5	B05C (1x)	TXEA+	B06A (1x)		
P2.0	B04B (1x)	PC1-AD26	B04Q (1x)	PC1-IRDY	B10A (1x)	RESET-SYS-DETECT	B10A (1x)	RxP30V5	B06C (1x)	TXEA+	B06C (1x)		
P2.1	B04A (2x)	PC1-AD26	B05A (1x)	PC1-PAR	B04F (1x)	RESET-USB20	B10A (2x)	SCL1	B04E (2x)	TXEB-	B06A (1x)		
P2.1	B04B (1x)	PC1-AD26	B07B (1x)	PC1-PAR	B05A (1x)	RIGHT-SPEAKER	B11A (2x)	SCL2	B04E (2x)	TXEB-	B06C (1x)		
P2.2	B04A (2x)	PC1-AD26	B07C (1x)	PC1-PAR	B07C (1x)	RIGHT-SPEAKER	B04H (1x)	SCL3	B04E (2x)	TXEB+	B06A (1x)		
P2.2	B04B (1x)	PC1-AD26	B10A (1x)	PC1-PAR	B10A (1x)	RREF-PNX8535	B04F (2x)	SCL3	B04E (2x)	TXEB+	B06C (1x)		
P2.3	B04A (2x)	PC1-AD27	B04F (1x)	PC1-PERR	B04F (2x)	RREF-PNX8535	B04F (2x)	SCL3	B04E (2x)	TXEB+	B06A (1x)		
P2.3	B04B (1x)	PC1-AD27	B04Q (1x)	PC1-PERR	B05A (1x)	RX0	B04H (1x)	SCL-AMBI	B01A (1x)	TXEC-	B06A (1x)		
P2.4	B04A (2x)	PC1-AD27	B05A (1x)	PC1-PERR	B07C (1x)	RX0	B08E (1x)	SCL-AMBI-3V3	B06B (1x)	TXEC-	B06C (1x)		
P2.4	B04B (1x)	PC1-AD27	B07B (1x)	PC1-PERR	B10A (1x)	RX0+	B04H (1x)	SCL-AMBI-3V3	B06C (1x)	TXEC+	B06A (1x)		
P2.5	B04A (2x)	PC1-AD27	B07C (1x)	PC1-REQ	B04F (2x)	RX0+	B08E (1x)	SCL-AMBI-3V3	B06C (1x)	TXEC+	B06C (1x)		
P2.5	B04B (1x)	PC1-AD27	B10A (1x)	PC1-REQ	B04F (2x)	RX0+	B08E (1x)	SCL-AMBI-3V3	B06C (1x)	TXEC+	B06C (1x)		
P2.6	B04A (2x)	PC1-AD28	B04F (1x)	PC1-REQ-B	B05A (1x)	RX1	B04H (1x)	SCL-I2C4-DISP	B06A (1x)	TXECLK-	B06A (1x)		
P2.6	B04B (1x)	PC1-AD28	B04Q (1x)	PC1-REQ-DEBUG	B04F (1x)	RX1-	B08E (1x)	SCL-I2C4-DISP	B06B (2x)	TXECLK-	B06C (1x)		
P2.7	B04A (2x)	PC1-AD28	B05A (1x)	PC1-REQ-DEBUG	B07C (1x)	RX1-	B08E (1x)	SCL-MUX2-BUF	B03B (2x)	TXECLK+	B06A (1x)		
P2.7	B04B (1x)	PC1-AD28	B10A (1x)	PC1-REQ-PNX5050	B05A (1x)	RX1+	B08E (1x)	SCL-MUX2-BUF	B03B (2x)	TXECLK+	B06C (1x)		
P3-VS	B06B (1x)	PC1-AD28	B07B (1x)	PC1-REQ-PNX5050	B07C (1x)	RX2-	B04H (1x)	SCL-PNX5050	B05C (1x)	TXED-	B06A (1x)		
P3-VS	B06C (1x)	PC1-AD28	B07C (1x)	PC1-REQ-PNX8535	B07C (1x)	RX2-	B08E (1x)	SCL-SLAVE	B04E (2x)	TXED-	B06C (1x)		
P50	B09A (1x)	PC1-AD29	B10A (1x)	PC1-REQ-USB20	B07C (1x)	RX2+	B04H (1x)	SCL-SSB	B04E (4x)	TXED+	B06C (1x)		
P50	B09B (1x)	PC1-AD29	B04F (1x)	PC1-REQ-USB20	B04F (1x)	RX2+	B08E (1x)	SCL-SSB	B05A (1x)	TXEE-	B06A (1x)		
P50	B09C (1x)	PC1-AD29	B04Q (1x)	PC1-REQ-USB20	B05A (1x)	RXC-	B04H (1x)	SCL-SSB	B06C (1x)	TXEE-	B06C (1x)		
P50	B09D (1x)	PC1-AD29	B05A (1x)	PC1-REQ-USB20	B10A (1x)	RXC-	B08E (1x)	SCL-SSB	B08E (1x)	TXEE+	B06A (1x)		
P50	B09E (1x)	PC1-AD29	B07B (1x)	PC1-REQ-USB20	B04F (2x)	RXC+	B04H (1x)	SCL-SSB1	B03A (2x)	TXEE+	B06C (1x)		
P50	B09F (1x)	PC1-AD29	B07C (1x)	PC1-SERR	B05A (1x)	RXC+	B08E (1x)	SCL-SSB1	B03B (1x)	TXEA-	B06A (1x)		
P50	B09G (1x)	PC1-AD29	B10A (1x)	PC1-SERR	B07C (1x)	RXD	B04C (1x)	SCL-SSB-MUX1	B04E (2x)	TXEA-	B06C (1x)		
P50	B09H (1x)	PC1-AD3	B04F (1x)	PC1-SERR	B10A (1x)	RXD	B08A (1x)	SCL-SSB-MUX1	B06B (1x)	TXEA+	B06A (1x)		
P50	B09I (1x)	PC1-AD3	B05A (1x)	PC1-STOP	B04F (1x)	RXD	B08B (1x)	SCL-SSB-MUX2	B08C (1x)	TXEA+	B06C (1x)		
P50	B09J (1x)	PC1-AD3	B07B (1x)	PC1-STOP	B05A (1x)	RXD2	B08C (1x)	SCL-SSB-MUX2	B04E (2x)	TXOB-	B06A (1x)		
P50	B09K (1x)	PC1-AD3	B07C (1x)	PC1-STOP	B07C (1x)	RXD2	B09B (1x)	SCL-UP-MIPS	B04A (3x)	TXOB-	B06C (1x)		
P50	B09L (1x)	PC1-AD3	B10A (1x)	PC1-STOP	B10A (1x)	RXD-MIPS	B04C (1x)	SCL-UP-MIPS	B04C (1x)	TXOB+	B06A (1x)		
P50	B09M (1x)	PC1-AD30	B04F (1x)	PC1-TRDY	B04F (2x)	RXD-MIPS	B04E (1x)	SCL-UP-MIPS	B04E (2x)	TXOB+	B06C (1x)		
P50	B09N (1x)	PC1-AD30	B04Q (1x)	PC1-TRDY	B05A (1x)	RXD-SERV	B08A (1x)	SDA1	B04E (2x)	TXOC-	B06A (1x)		
P50	B09O (1x)	PC1-AD30	B05A (1x)	PC1-TRDY	B07C (1x)	RXD-SERV	B08A (1x)	SDA2	B04E (2x)	TXOC-	B06C (1x)		
P50	B09P (1x)	PC1-AD30	B07B (1x)	PC1-TRDY	B10A (1x)	RXD-UP	B04C (2x)	SDA2	B04E (2x)	TXOC+	B06A (1x)		
P50	B09Q (1x)	PC1-AD30	B07C (1x)	PC1-TRDY	B07A (1x)	RXD-UP	B04C (2x)	SDA3	B04E (2x)	TXOC+	B06C (1x)		
P50	B09R (1x)	PC1-AD30	B10A (1x)	PCMCIA-A0	B07A (1x)	RxP3CLK	B05C (1x)	SDA-AMBI	B01A (1x)	TXOCLK-	B08A (1x)		
P50	B09S (1x)	PC1-AD31	B04F (1x)	PCMCIA-A1	B07A (1x)	RxP3CLK	B06C (1x)	SDA-AMBI	B06B (2x)	TXOCLK-	B08C (1x)	</	



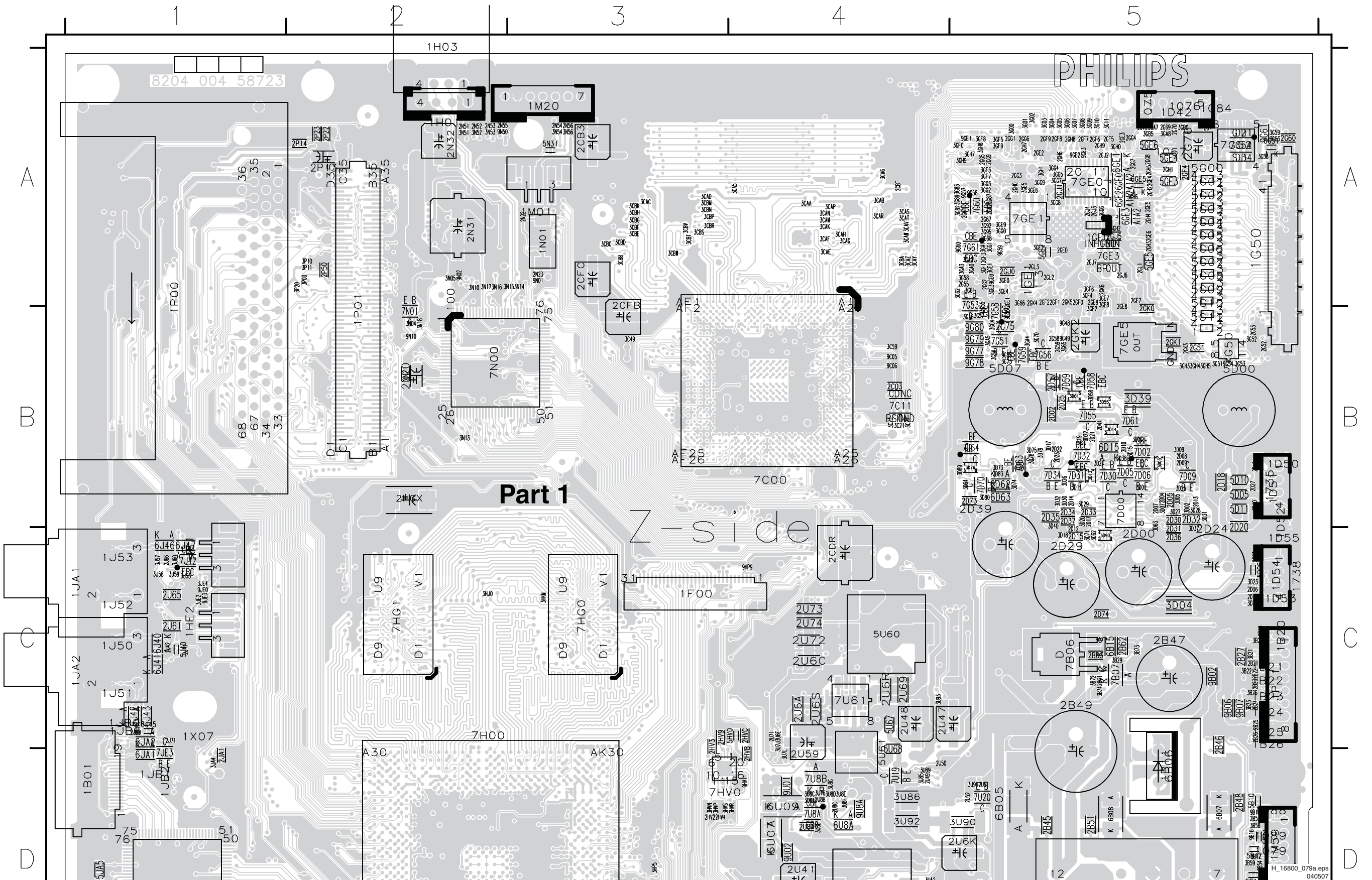
Layout Small Signal Board (Overview Top Side)



1020	F4	2D12	C5	2G9F	A5	2HTU	E2	2U27	D4	3CAM	A4	3E45	G4	3GF4	A5	3HSQ	E3	5B01	E5	6E15	G4	7HD1	F2	9T55	G2
1030	G4	2D13	B5	2GG1	A5	2HTV	E2	2U28	D4	3CAN	A4	3E49	G4	3GF5	A5	3HST	E3	5B10	D5	6E16	F3	7HG0	C2	9T56	G1
1040	E3	2D14	B5	2GG2	A5	2HTWE	E3	2U32	D4	3CAP	A4	3E51	F4	3GF6	A5	3HSV	E3	5B11	D5	6E17	G4	7HG1	C2	9T57	F2
1070	F4	2D15	C5	2GG3	A5	2HTY	E3	2U39	D4	3CAR	A4	3E52	F4	3GF7	A5	3HSW	E2	5B12	D5	6E19	G3	7HK0	E2	9T58	G1
1735	B5	2D16	C5	2GG4	A5	2HU1	E3	2U41	D4	3CAS	A4	3E54	G4	3GF8	A5	3HSY	E2	5B13	D5	6E20	G4	7HV0	D3	9T59	G1
1738	C5	2D17	B5	2GG5	A5	2HU2	E3	2U42	D4	3CAT	A4	3E55	F4	3GF9	A5	3HSZ	E2	5B14	D5	6E21	G3	7HV1	F1	9T60	G1
1801	D1	2D18	B5	2GG6	A5	2HU3	E2	2U47	C5	3CAV	A4	3E56	G3	3GG0	A5	3HT0	E2	5D00	B5	6E22	G4	7HVA	E1	9T61	G1
1B02	E1	2D19	B5	2GG7	A5	2HU4	E2	2U48	C4	3CAW	A4	3E57	G4	3GG1	A5	3HT1	E2	5D05	B5	6E24	F3	7J27	D1	9T62	G1
1B03	E1	2D20	B5	2GG8	A5	2HU5	E2	2U49	D4	3CAY	A4	3E58	G3	3GG2	A5	3HT2	E2	5D06	C5	6E25	G4	7J42	C1	9T63	G2
1B10	E5	2D21	B5	2GG9	A5	2HU6	E2	2U50	D4	3CAZ	A4	3E59	G4	3GG3	A5	3HT3	E2	5D07	B5	6E27	G4	7JE3	D1	9T64	G2
1B11	D5	2D22	B5	2GH0	A5	2HU7	E2	2U51	D5	3CBA	A4	3E60	F4	3GG4	A5	3HT4	E2	5D10	B5	6E29	G4	7N00	B3	9T65	G1
1B32	G5	2D23	B5	2GH1	A5	2HU8	E2	2U52	D5	3CBB	A3	3E61	G4	3GG5	A5	3HT5	E2	5D11	B5	6E30	G4	7N01	A2	9T66	G2
1B40	D5	2D24	C5	2GH2	A5	2HU9	E2	2U54	E5	3CBC	A3	3E62	G4	3GG6	A5	3HT6	E2	5EA1	G3	6E31	F4	7U01	E4	9T74	F1
1D42	A5	2D25	B5	2GH3	A5	2HUA	E2	2U55	E4	3CBD	A3	3E63	F3	3GG7	A5	3HV3	F1	5EA2	G5	6E32	G4	7U03	E4	9T75	F1
1F00	C3	2D27	B5	2GH4	A5	2HUB	E2	2U56	E4	3CBE	A3	3E64	F3	3GG8	A5	3HV4	F1	5EA3	G5	6E34	F4	7U06	E4	9T77	G2
1G50	A5	2D29	C5	2GH5	A5	2HUC	E2	2U57	E4	3CBF	A3	3E65	G3	3GG9	A5	3HV7	F1	5G00	A5	6E34	F4	7U08	D4	9T82	G2
1G60	A5	2D30	B5	2GH6	A5	2HUD	E2	2U58	E5	3CBG	A3	3E66	G4	3GH0	A5	3HV8	F1	5G01	A5	6E35	F4	7U16	D4	9T83	G2
1GE1	A5	2D31	C5	2GH7	A5	2HUE	E2	2U59	D4	3CBH	A3	3E67	F4	3GH1	A5	3HVN	D3	5G02	A5	6E36	G4	7U19	D4	9U01	D4
1H01	A2	2D32	B5	2GH8	A5	2HUF	E2	2U69	C4	3CBK	A3	3E68	G4	3GH2	A5	3HVP	D3	5G03	A5	6E37	G4	7U20	D5	9U02	D4
1H03	A3	2D33	B5	2GH9	A5	2HV2	D3	2U6A	C4	3CBM	A3	3E69	G4	3GH3	B5	3HVR	D4	5G04	A5	6E41	E4	7U24	E4	9U04	D5
1H10	F2	2D34	B5	2GU0	A5	2HV3	C3	2U6C	C4	3CBN	A3	3E72	G5	3GH4	B5	3HVS	D3	5G05	A5	6EA2	E4	7U26	E5	9U05	D5
1HE1	B1	2D35	B5	2GU1	A5	2HV4	D3	2U6K	D4	3CBP	A3	3E73	G4	3GH5	B5	3HVT	F2	5G06	A5	6EA3	E4	7U61	C4	9U8A	D4
1HE2	C1	2D36	C5	2GU2	A5	2HV7	F1	2U6R	C4	3CBR	A3	3E77	G4	3GH6	A5	3HVV	F1	5G07	A5	6EA4	E4	7U8A	D4		
1HP0	E1	2D37	B5	2GU3	A5	2HV9	C3	2U6S	C4	3CBS	A3	3E79	G4	3GH7	A5	3HVX	F1	5G08	A5	6EA5	E3	7U8B	D4		
1JA1	C1	2D39	C5	2GU4	A5	2HVB	D4	2U71	C4	3CBT	A3	3E80	G4	3GH8	A5	3J11	F1	5G09	A5	6EA6	E3	9B01	F5		
1JA2	C1	2D40	B5	2GU5	A5	2HVC	A4	2U72	C4	3CBV	A3	3E81	G4	3GH9	A5	3J12	F1	5G10	B5	6EA7	F3	9B02	C5		
1M01	A2	2D44	B5	2GU6	A5	2HVG	F1	2U73	C4	3CBWA	A3	3E87	G4	3H01	D3	3J30	E1	5G11	B5	6EA8	F3	9B06	C5		
1M15	F1	2D57	B5	2GU7	A5	2J11	F1	2U74	C4	3D01	B5	3E8A	G4	3H06	F2	3J31	E1	5GE2	A5	6EB0	F3	9B07	C5		
1M20	A3	2D73	B5	2GK0	B5	2J12	F1	2U8A	D4	3D02	B5	3E89	G4	3H07	E2	3J32	E1	5GE3	A5	6EB1	F3	9B10	C5		
1M36	G5	2D74	C5	2GK1	B5	2J30	E1	2U8B	D4	3D03	B5	3E90	G4	3H08	F2	3J33	E1	5GE4	A5	6G51	A5	9B11	D5		
1M59	D5	2E01	G3	2GK2	B5	2J32	E1	2U8C	D4	3D04	C5	3E91	E4	3H09	F2	3J34	E1	5GE5	A5	6G50	A5	9B15	D5		
1N01	A3	2E02	G3	2GK3	B5	2J33	E1	2U93	E4	3D05	B5	3E92	E4	3H53	F2	3J35	E1	5GE6	A5	6G51	A5	9B16	D5		
1P00	B2	2E03	G3	2GK4	A5	2J34	E1	2B20	C5	3D06	B5	3E93	E3	3H54	E2	3J36	E1	5HM0	E3	6G62	A5	9B17	D5		
1P01	B2	2E06	G3	2GK5	A5	2J35	E1	2B21	C5	3D07	B5	3E96	F3	3H56	F2	3J53	C1	5HM1	E3	6G63	A5	9B18	D5		
1P02	G5	2E10	G3	2GK6	A5	2J41	C1	2B22	C5	3D09	B5	3E97	F3	3H57	F2	3J57	C1	5HR0	E3	6G65	A5	9B19	D5		
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1R07	G4	2E16	G3	2GL2	A5	2J49	C1	2B24	E5	3D13	B5	3E99	F3	3H74	E3	3J59	C1	5HR2	E2	6J11	F1	9C06	B4		
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1T06	F1	2E24	G3	2HD0	F2	2J71	C1	2B30	E5	3D18	C5	3EC3	G3	3HC4	E1	3J2E	C1	5HR8	E2	6J43	C1	9E22	G3		
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2B22	C5	2E44	G5	2HMH	E3	2N32	A2	2B37	E5	3D26	B5	3G03	A5	3HD3	E2	3N16	A2	5HRH	E2	6T57	G2	9E48	B5		
2B23	C5	2E45	G4	2HN1	D3	2N51	A2	2B38	E5	3D27	B5	3G04	A5	3HF9	E2	3N17	A2	5HRJ	E3	6U07	D4	9G49	B5		
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2B26	C5	2E48	G5	2HR2	E3	2N54	A3	2B41	E5	3D30	B5	3G07	A5	3HK2	F2	3N52	A2	5HRN	E3	6U64	E5	9G56	B5		
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2B31	F5	2E52	F4	2HR6	E3	2P14	A2	2B45	E5	3D37	C5	3G11	A5	3HP2	D3	3P00	A2	5HRT	E2	7B02	E5	9G60	A5		
2B32	E5	2E53	E4	2HR7	E3	2P22	A2	2B46	E5	3D38	B5	3G51	B5	3HP3	D3	3P10	A2	5HRU	E2	7B04	E5	9G61	A5		
2B33	E5	2E54	E4	2HR8	E3	2P23	A2	2B47	E5	3D39	B5	3G52	B5	3HP4	D3	3P11	A2	5HRV	E2	7B05	E5	9G77	B5		
2B34	E5	2E55	F4	2HR9	E3	2P50	A2	2B48	E5	3D40	B5	3G53	B5	3HP5	D3	3P20	A2	5HRWE	E2	7B06	C5	9G78	B5		
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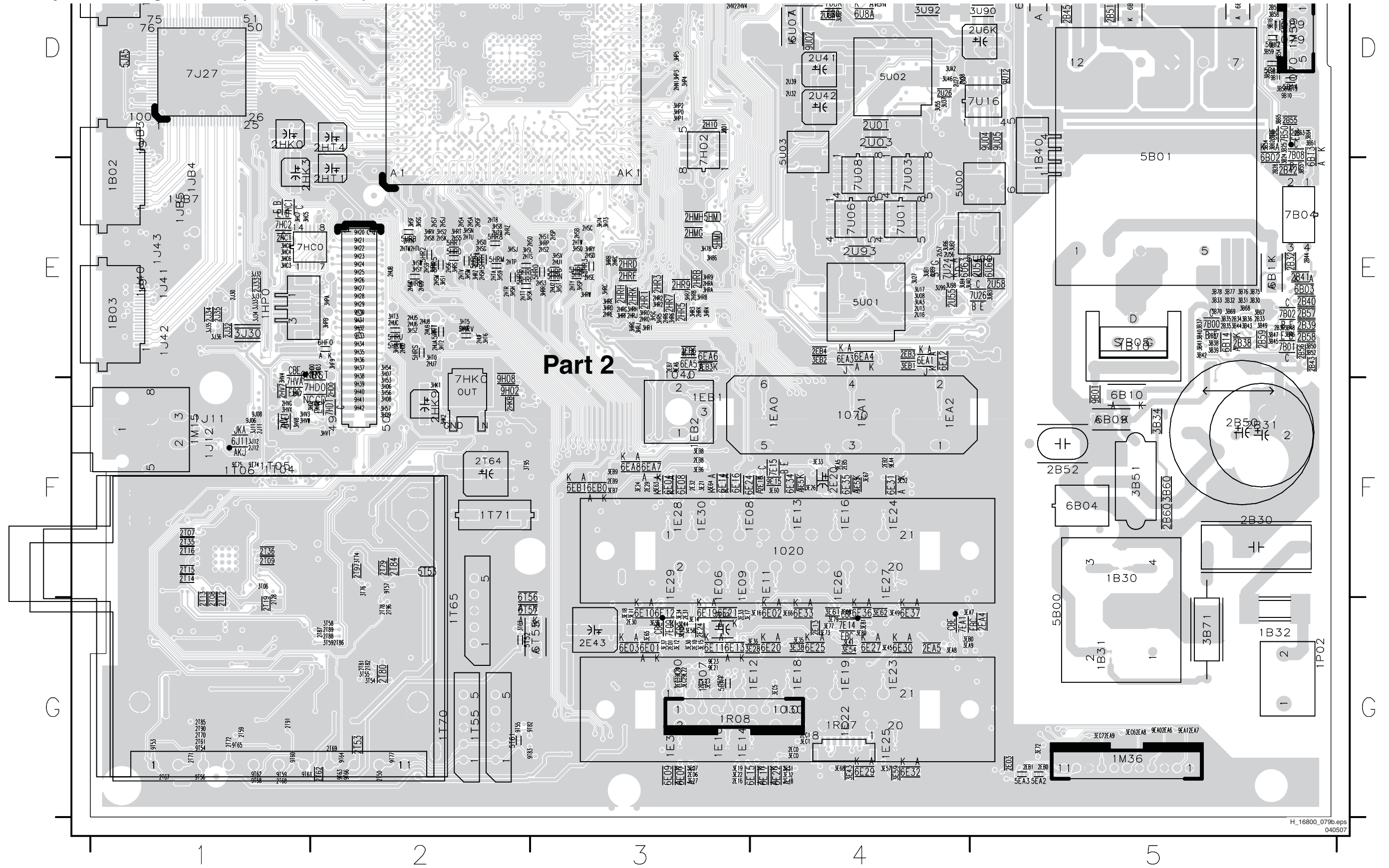


Layout Small Signal Board (Part 1 Top Side)



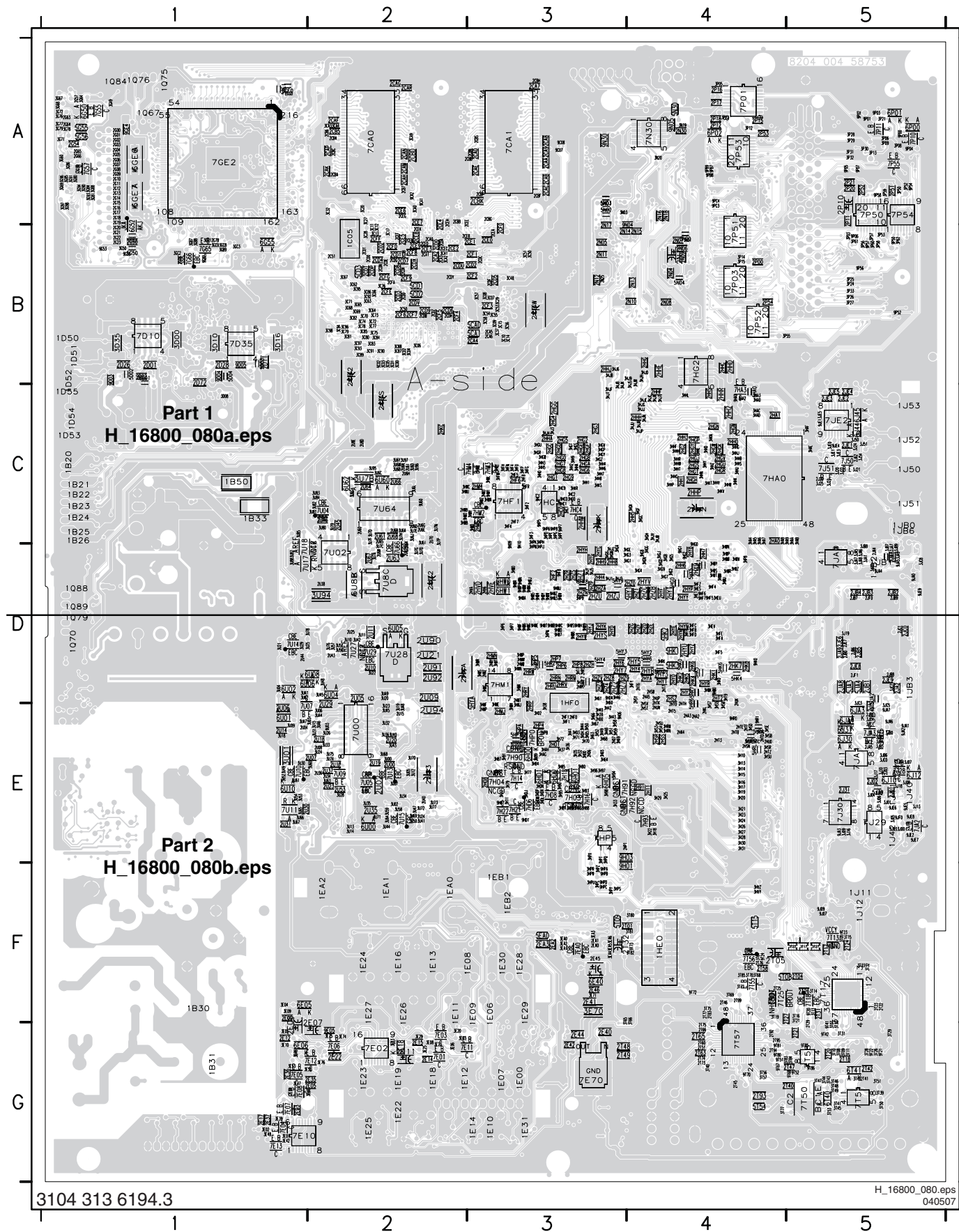


Layout Small Signal Board (Part 2 Top Side)





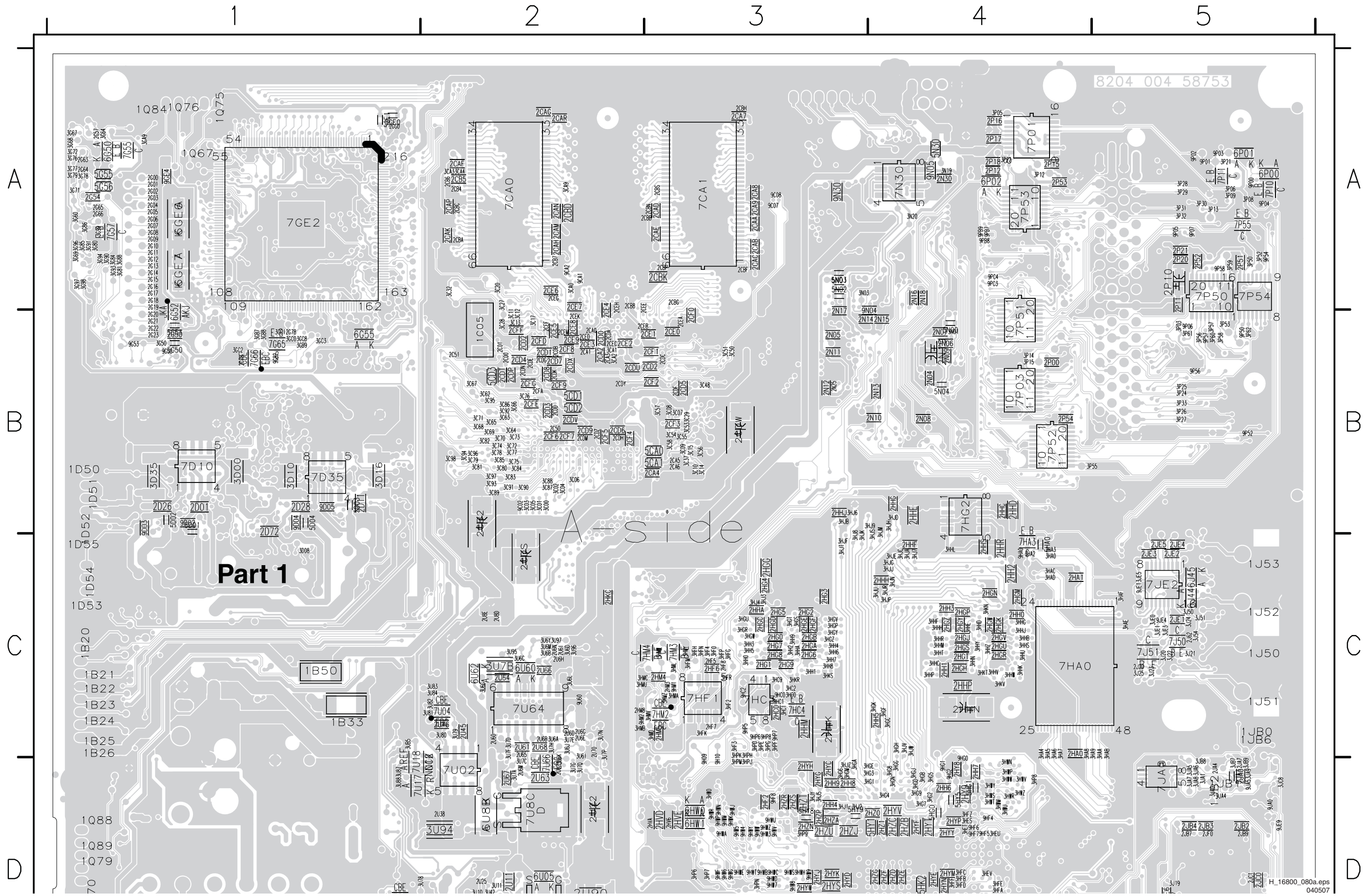
Layout Small Signal Board (Overview Bottom Side)



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1B50	C1	2CF0	B3	2HF6	C3	2HST	E4	2JA9	D5	2U04	E1	3C71	B2	3GA9	A1	3HAC	C4	3SHV	C4	3HT7	D3	3P55	B5	3U41	E2	5HK2	D4	7G65	B1	9HA0	C4	9T23	F5
1C05	B2	2CF1	B3	2HF7	C3	2HSU	E4	2JB0	D5	2U05	D2	3C72	B2	3GB7	B1	3HAD	C4	3SHW	C4	3HT8	D3	3P56	B5	3U42	D2	5HK3	D4	7G66	B1	9HC0	C3	9T24	F4
1HE0	F4	2CF2	B3	2HF8	C3	2HSV	E4	2JB1	D5	2U06	E1	3C73	B2	3GB8	B1	3HAE	C5	3SHY	C4	3HT9	D4	3P57	B5	3U43	E2	5HK4	D4	7G67	A1	9HC1	C3	9T41	G5
1HF0	D3	2CF3	B3	2HF9	C3	2HSZ	E4	2JB2	D5	2U07	E2	3C74	B2	3GB9	B1	3HAF	C5	3SHZ	C4	3HVY	F4	3P58	B5	3U44	E2	5HK5	D4	7H03	E3	9HC2	C3	9T42	G5
2C00	B2	2CF4	B2	2HG1	C3	2HSY	D4	2JB3	D5	2U08	E2	3C75	B2	3GC0	B1	3HAG	C5	3SHJ	D3	3HVZ	F4	3P59	A5	3U45	D2	5HK6	D4	7H04	E3	9HD1	E3	9T51	G4
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2C02	B2	2CF6	B2	2HG3	C3	2HT0	E4	2JB5	D5	2U10	D2	3C77	B2	3GC2	B1	3HCO	C3	3SHJ	C3	3HWK	D4	3P61	B5	3U48	D2	5HP2	D3	7H16	E3	9HF5	D4	9T67	G4
2C50	B2	2CF7	B2	2HG4	C3	2HT2	D4	2JB6	D5	2U11	D2	3C78	B2	3GC3	B1	3HAI	E3	3SHJ	C3	3HWM	D4	3P62	B5	3U49	E1	5HR6	E4	7H18	E3	9HF6	D4	9T68	G5
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2CA1	B2	2CFA	B2	2HG7	C3	2HT6	D4	2JB9	D5	2U17	E2	3C81	B2	3H02	E3	3HDJ	E3	3SHJ	C4	3HWR	D4	3T08	G5	3U52	E1	5HRA	E4	7H90	E3	9HG0	D4	9T73	F4
2CA2	B2	2CFD	B2	2HG8	C3	2HT7	D4	2JC0	D5	2U18	E2	3C82	B2	3H03	E3	3HDK	E3	3SHJ	C3	3HWS	D4	3T09	G5	3U53	E1	5HRL	E4	7H91	E3	9HG1	D4	9T80	G4
2CA3	B2	2CFE	B2	2HG9	C3	2HT8	E4	2JC1	D5	2U19	E2	3C83	B2	3H04	E3	3HDL	E3	3SHJ	B4	3HWT	D4	3T10	G5	3U54	E1	5HRZ	D3	7H92	E4	9HG2	D4	9T81	G4
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2CA7	A3	2D11	B1	2HGD	C3	2HTF	E4	2JE4	C5	2U23	E2	3C87	B2	3H12	E3	3HES	D4	3HJD	B4	3HWY	D4	3T15	F5	3U59	E2	5HY3	D3	7HC3	C3	9HP5	C3	9U60	C2
2CA8	A3	2D26	B1	2HGE	C3	2HTG	E4	2JE5	C5	2U24	E2	3C88	B2	3H13	E4	3HET	D4	3HJE	C4	3HWZ	D4	3T16	F5	3U60	E2	5HY4	D3	7HC4	C3	9HP6	C3		
2CA9	A3	2D28	B1	2HGF	C3	2HTH	E4	2JE6	D5	2U25	D2	3C89	B2	3H14	E4	3HEU	D4	3HJF	C3	3J00	E5	3T17	F5	3U61	E1	5HY5	D4	7HD6	E3	9HP7	C3		
2CAA	A3	2D72	B1	2HGG	C3	2HTK	D4	2JE7	D5	2U29	E2	3C90	B2	3H15	E4	3HEV	D4	3HJG	C4	3J01	E5	3T19	F4	3U62	D1	5HY6	D4	7HD8	E3	9HP8	C3		
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2CAH	A2	2E12	G1	2HGR	C4	2HVA	D3	2N06	B4	2U37	E2	3C97	B2																				



Layout Small Signal Board (Part 1 Bottom Side)

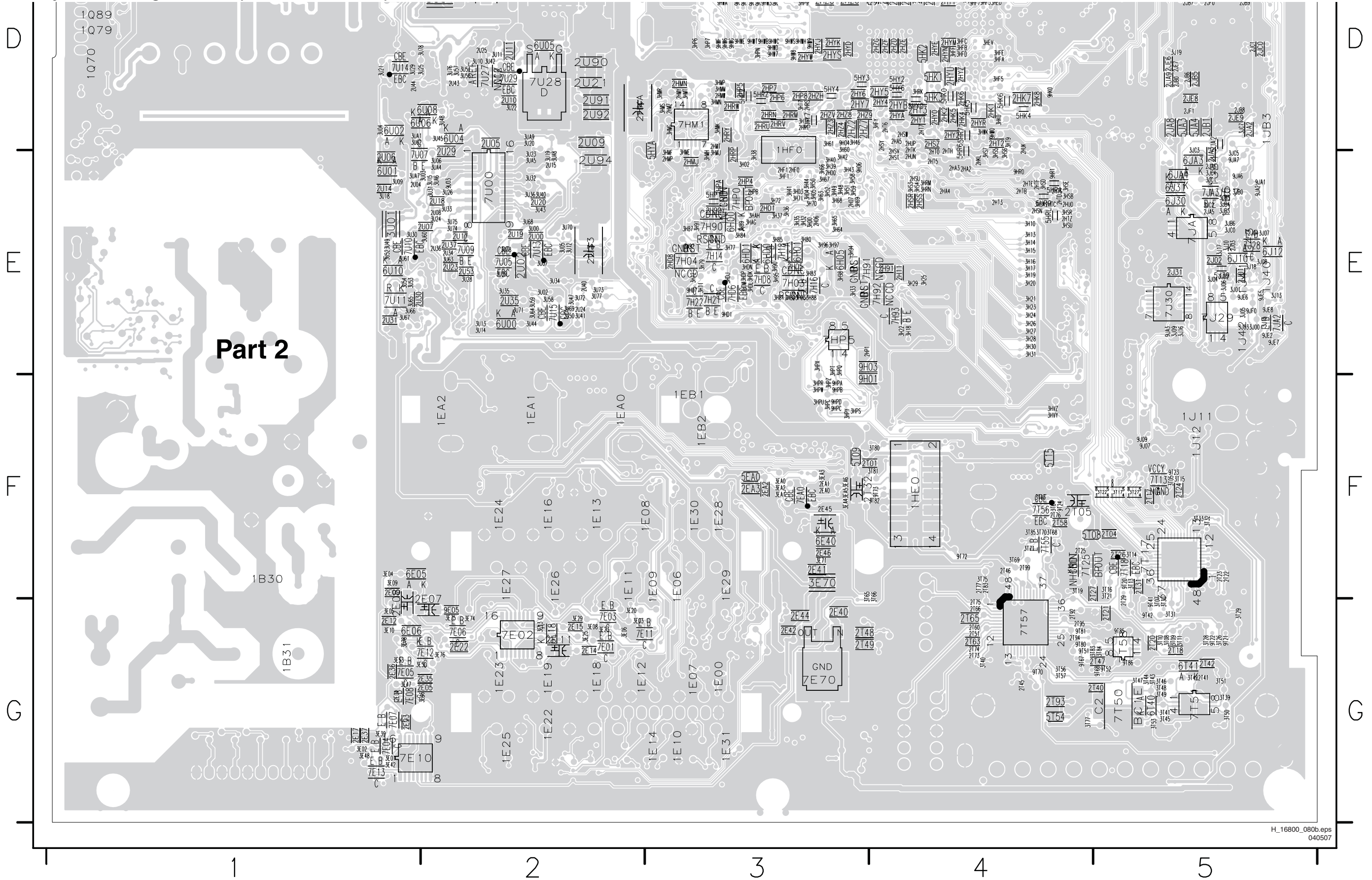


Part 1

A - Side

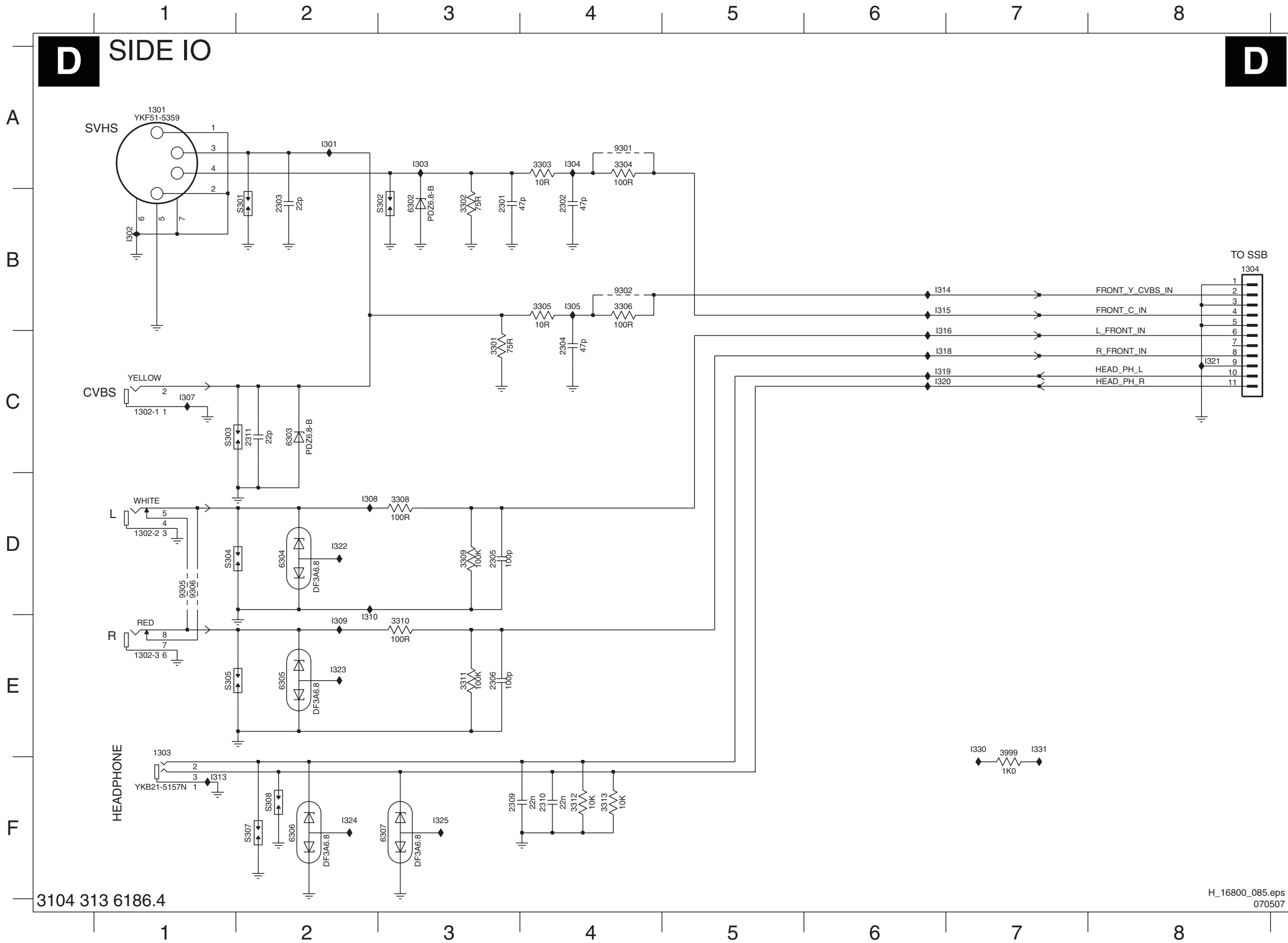
8204 004 58753

Layout Small Signal Board (Part 2 Bottom Side)





Side I/O Panel



- 1301 A1
- 1302-1 C1
- 1302-2 D1
- 1302-3 E1
- 1303 E1
- 1304 B8
- 2301 B3
- 2302 B4
- 2303 B2
- 2304 C4
- 2305 D3
- 2306 E3
- 2309 F3
- 2310 F4
- 2311 C2
- 3301 C3
- 3302 B3
- 3303 A4
- 3304 A4
- 3305 B4
- 3306 B4
- 3308 D3
- 3309 D3
- 3310 E3
- 3311 E3
- 3312 F4
- 3313 F4
- 3999 E7
- 6302 B3
- 6303 C2
- 6304 D2
- 6305 E2
- 6306 F2
- 6307 F3
- 9301 A4
- 9302 B4
- 9305 D1
- 9306 D1
- I301 A2
- I302 B1
- I303 A3
- I304 A4
- I305 B4
- I307 C1
- I308 D2
- I309 E2
- I310 E2
- I313 F1
- I314 B6
- I315 B6
- I316 B6
- I318 C6
- I319 C6
- I320 C6
- I321 C8
- I322 D2
- I323 E2
- I324 F2
- I325 F3
- I330 E7
- I331 E7
- S301 B2
- S302 B3
- S303 C1
- S304 D1
- S305 E1
- S307 F2
- S308 F2

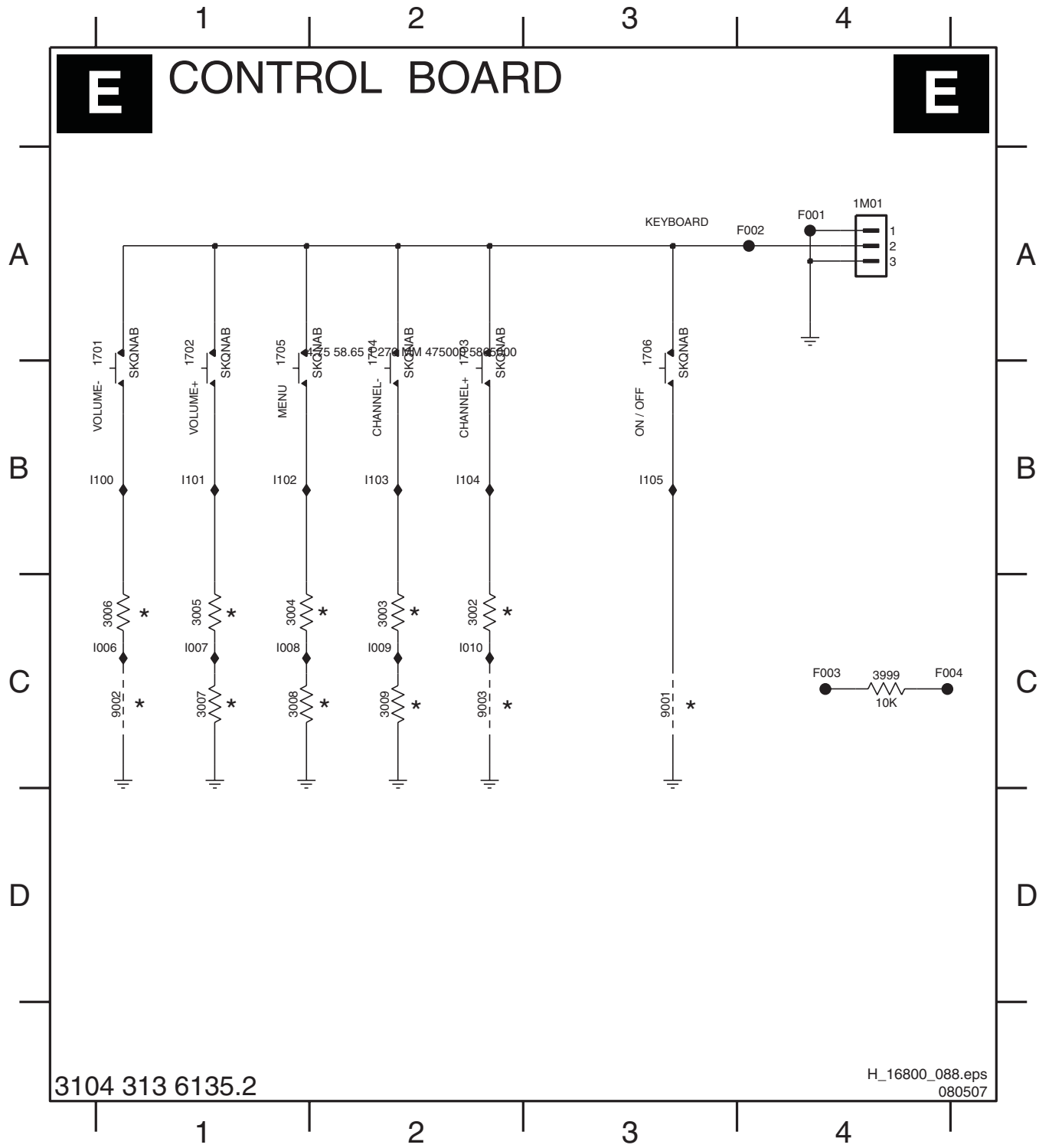
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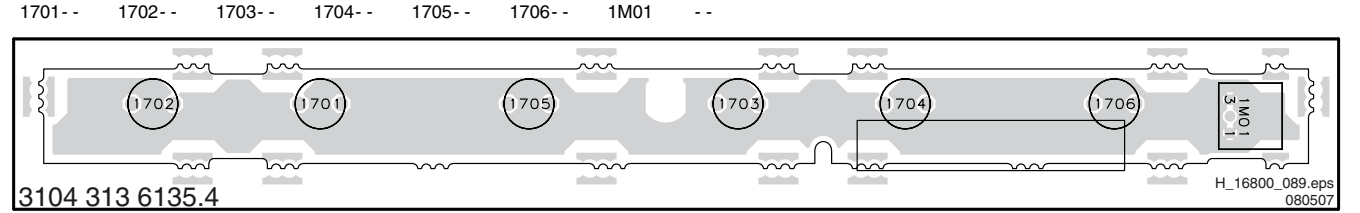




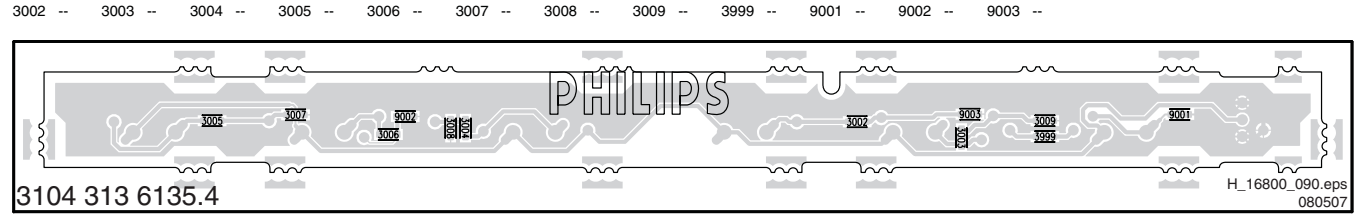
Keyboard Control Panel



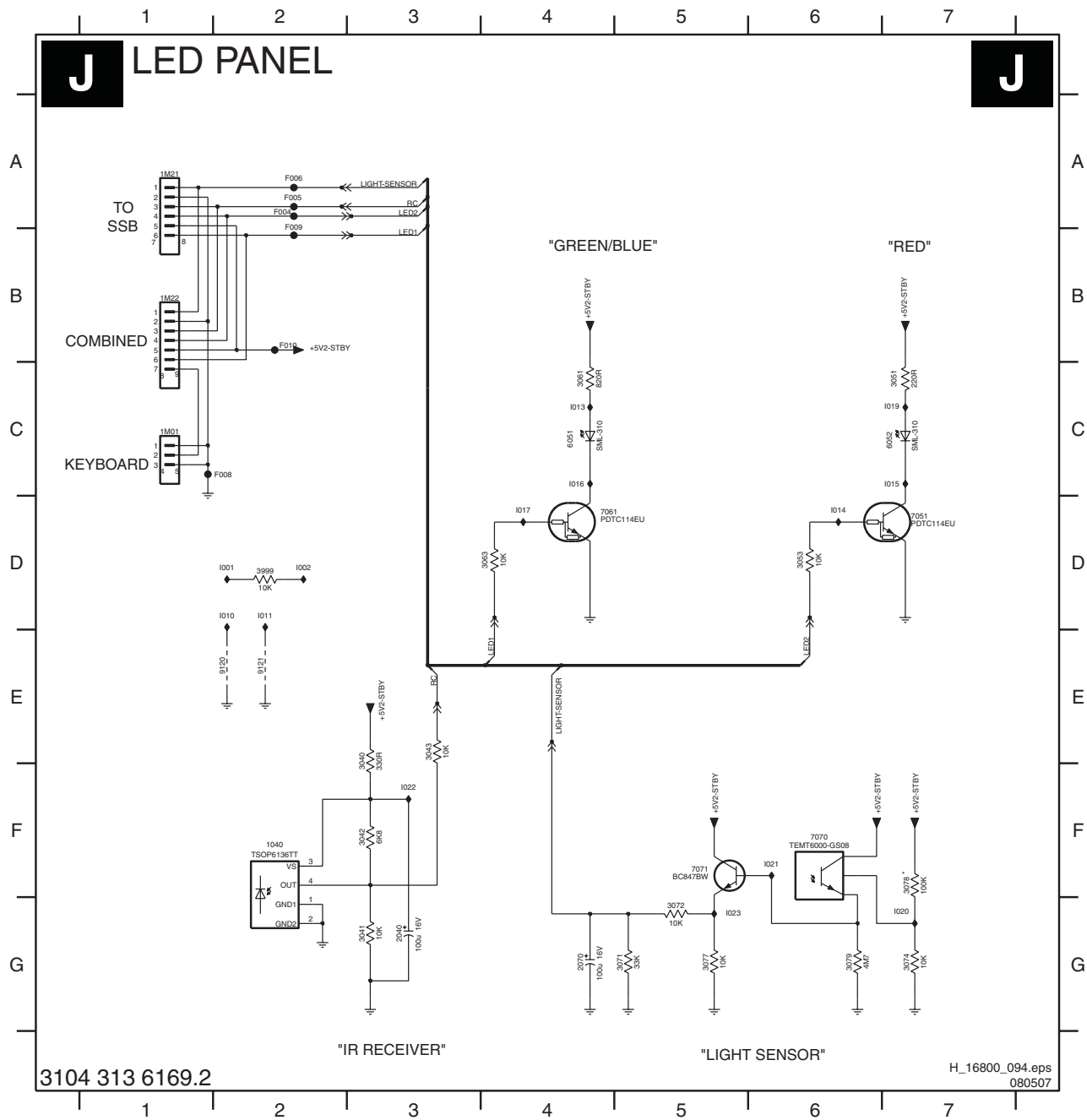
Layout Keyboard Control Panel (Top Side)



Layout Keyboard Control Panel (Bottom Side)



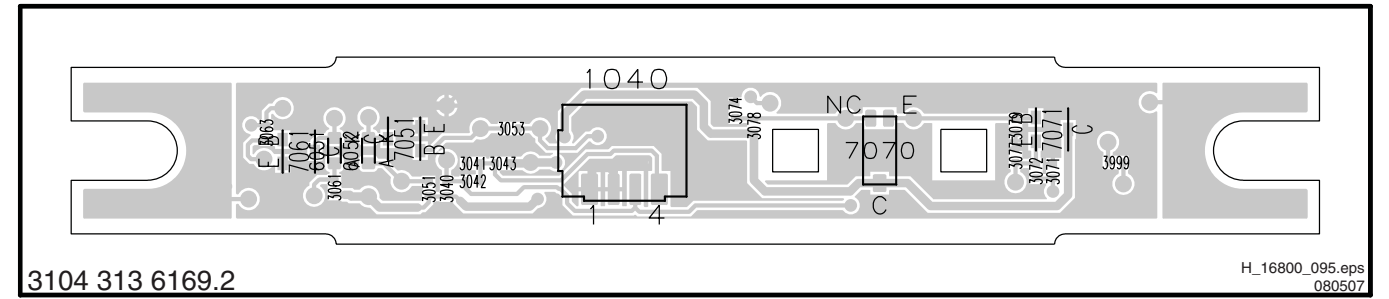
IR & LED Panel



- 1040 F2
- 1M01 C1
- 1M21 A1
- 1M22 B1
- 2040 G3
- 2070 G4
- 3040 E3
- 3041 G3
- 3042 F3
- 3043 E3
- 3051 C7
- 3053 D6
- 3061 C4
- 3063 D4
- 3071 G5
- 3072 G5
- 3074 G7
- 3075 G5
- 3078 F7
- 3079 G6
- 3999 D2
- 6051 C4
- 6052 C7
- 7051 D7
- 7061 D4
- 7070 F6
- 7071 F5
- 9120 E2
- 9121 E2
- F004 A2
- F005 A2
- F006 A2
- F008 C2
- F009 B2
- F010 B2
- I001 D2
- I002 D2
- I010 D2
- I011 D2
- I013 C4
- I014 D6
- I015 C7
- I016 C4
- I017 D4
- I019 C7
- I020 G7
- I021 F6
- I022 F3
- I023 G5

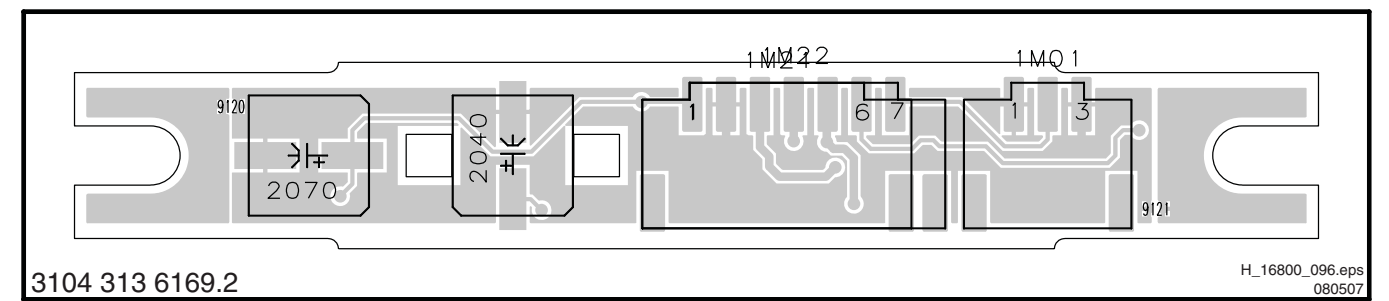
Layout IR & LED Panel (Top Side)

- 1040 --
- 3042 --
- 3053 --
- 3071 --
- 3077 --
- 3999 --
- 7051 --
- 7071 --
- 3040 --
- 3043 --
- 3061 --
- 3072 --
- 3078 --
- 6051 --
- 7061 --
- 3041 --
- 3051 --
- 3063 --
- 3074 --
- 3079 --
- 6052 --
- 7070 --



Layout IR & LED Panel (Bottom Side)

- 1M01 --
- 1M21 --
- 1M22 --
- 2040 --
- 2070 --
- 9120 --
- 9121 --





## 8. Alignments

### Index of this chapter:

- 8.1 General Alignment Conditions
- 8.2 Hardware Alignments
- 8.3 Software Alignments
- 8.4 Option Settings

**Note:** The Service Default Mode (SDM) and Service Alignment Mode (SAM) are described in chapter 5. Menu navigation is done with the CURSOR UP, DOWN, LEFT or RIGHT keys of the remote control transmitter.

### 8.1 General Alignment Conditions

Perform all electrical adjustments under the following conditions:

- Power supply voltage (depends on region):
    - AP-NTSC: 120 V<sub>AC</sub> or 230 V<sub>AC</sub> / 50 Hz (± 10%).
    - AP-PAL-multi: 120 - 230 V<sub>AC</sub> / 50 Hz (± 10%).
    - EU: 230 V<sub>AC</sub> / 50 Hz (± 10%).
    - LATAM-NTSC: 120 - 230 V<sub>AC</sub> / 50 Hz (± 10%).
    - US: 120 V<sub>AC</sub> / 60 Hz (± 10%).
  - Connect the set to the mains via an isolation transformer with low internal resistance.
  - Allow the set to warm up for approximately 15 minutes.
  - Measure voltages and waveforms in relation to correct ground (e.g. measure audio signals in relation to AUDIO\_GND).
- Caution:** It is not allowed to use heatsinks as ground.
- Test probe: Ri > 10 Mohm, Ci < 20 pF.
  - Use an isolated trimmer/screwdriver to perform alignments.

#### 8.1.1 Alignment Sequence

- First, set the correct options:
  - In SAM, select "Options", and then "Option numbers".
  - Fill in the option settings for "Group 1" and "Group 2" according to the set sticker (see also paragraph "Option Settings").
  - Press OK on the remote control **before** the cursor is moved to the left.
  - In submenu "Option numbers" select "Store" and press OK on the RC.
- OR:**
  - In main menu, select "Store" again and press OK on the RC.
  - Switch the set to Stand-by.
- Warming up (>15 minutes).

### 8.2 Hardware Alignments

Not applicable.

### 8.3 Software Alignments

Put the set in SAM mode (see Chapter 5 "Service Modes, Error Codes and Fault Finding"). The SAM menu will now appear on the screen. Select ALIGNMENTS and go to one of the sub menus. The alignments are explained below.

The following items can be aligned:

- Whitepoint.
- Black Level (BL) offset Red and Green.

To store the data:

- Press OK on the RC **before** the cursor is moved to the left.
- In main menu select "Store" and press OK on the RC.

- Press MENU on the RC to switch back to the main menu.
- Switch the set to stand-by mode.

For the next alignments, supply the following test signals via a video generator to the RF input:

- **EU/AP-PAL** models: a PAL B/G TV-signal with a signal strength of at least 1 mV and a frequency of 475.25 MHz
- **US/AP-NTSC** models: an NTSC M/N TV-signal with a signal strength of at least 1 mV and a frequency of 61.25 MHz (channel 3).
- **LATAM** models: an NTSC M TV-signal with a signal strength of at least 1 mV and a frequency of 61.25 MHz (channel 3).
- **DVB-T** models: see table "SDM default settings" in chapter 5.

#### 8.3.1 White Point

- Set "Active control" to "Off".
- Choose "TV menu", "TV Settings" and then "Picture" and put:
  - "Dynamic contrast" to "Off".
  - "Colour enhancement" to "Off".
  - "Light sensor" to "Off".
  - "Clear LCD" to "On" where applicable.
  - "Colour" to "0".
  - "Contrast" to "100".
- Go to the SAM and select "Alignments"-> "Whitepoint".

#### White point alignment LCD screens:

- Use a 100% white screen as input signal and set the following values:
  - "Colour temperature": "Normal".
  - All "Whitepoint" values to: "127".
  - "Red BL offset" values to "8".
  - "Green BL offset" values to "8".

#### In case you have a colour analyser:

- Measure with a calibrated (phosphor- independent) colour analyser in the centre of the screen. Consequently, the measurement needs to be done in a dark environment.
- Adjust the correct x,y coordinates (while holding one of the White point registers R, G or B on 127) by means of decreasing the value of one or two other white points to the correct x,y coordinates (see table "White D alignment values"). Tolerance: dx: ± 0.004, dy: ± 0.004.
- Repeat this step for the other colour temperatures that need to be aligned.
- When finished press OK on the RC and then press STORE (in the SAM root menu) to store the aligned values to the NVM.
- Restore the initial picture settings after the alignments.

**Table 8-1 White D alignment values**

Value	Cool (11000 K)	Normal (9000 K)	Warm (6500 K)
x	0.276	0.287	0.313
y	0.282	0.296	0.329

**If you do not have a colour analyser**, you can use the default values. This is the next best solution. The default values are average values coming from production (statistics).

- Select a COLOUR TEMPERATURE (e.g. COOL, NORMAL, or WARM).
- Set the RED, GREEN and BLUE default values according to the values in the "Tint settings" table.
- When finished press OK on the RC, then press STORE (in the SAM root menu) to store the aligned values to the NVM.
- Restore the initial picture settings after the alignments.



Table 8-2 Tint settings

Colour Temp.	R	G	B
Cool	122	120	121
Normal	125	118	108
Warm	127	110	75

## 8.4 Option Settings

### 8.4.1 Introduction

The microprocessor communicates with a large number of I<sup>2</sup>C ICs in the set. To ensure good communication and to make digital diagnosis possible, the microprocessor has to know

Table 8-3 Dealer options

Menu item	Subjects	Options	Description
Personal Options	Picture Mute	On	Picture is muted / not muted in case no input signal is detected at input connectors
		Off	
	Virgin Mode	On	TV starts up / does not start up (once) with a language selection menu after the Mains switch is turned "on" for the first time (virgin mode)
		Off	

### 8.4.3 (Service) Options

Select the sub menu's to set the initialization codes (options) of the set via text menus.

Table 8-4 Service options

Menu-item	Subjects	Options	Description
PIP/DS	Dual Screen	None	No DS
		One tuner dual screen	One tuner DS
		Two tuner dual screen	Two tuner DS
Display	Screen	"Value"	Used screen size, type, and resolution (see figure "Display option code overview" in chapter 5)
	Dimming Backlight	On / Off	Feature present / not present
Video Repro	Perfect Pixel	On / Off	Perfect Pixel On / Off
	Ambient Light	Off / Mono / Stereo/Triple / Quad	Inverter not present / one inverter / two inverters / three inverters / four inverters
	Ambient Light technology	CCFL / LED	CCFL / LED
	Ambient Light driver	Pacific 3 / MOP / DFI	Ambient Light driver
	MOP	Present / Not present	MOP present / not present
	Light sensor	Present / Not present	MOP present / not present
Source selection	HDMI 3	Present / Not present	HDMI 3 Present / Not present
	HDMI CEC	On / Off	HDMI CEC On/ Off
Audio Repro	Acoustic System (Cabinet design, used for setting dynamic audio parameters)	None	
		Top A 2k7	
		MS7 model A 2k7	
		MS7 model B 2k7	
		ME7 32" 2k7	
		ME7 model A 2k7	
		ME7 model B 2k7	
		Step 63" Combat Coscone 2k7	
Aura			
Miscellaneous	Tuner Type	TD1736 / TD1716	TD1736 = US, TD1716 = Europe
	Nyquist SAW filter	On / Off	SAW filter on/off
	I <sup>2</sup> C configuration	with PCA9540 / with PCA9515 / via channel decoder	
	Upgrade assistant	Present / Not present	
Opt. no.	Group 1		xxxxx xxxxx xxxxx xxxxx (see set sticker)
	Group 2		xxxxx xxxxx xxxxx xxxxx (see set sticker)
	Store	Store	

which ICs to address. The presence / absence of these specific ICs (or functions) is made known by the option codes.

#### Notes:

- After changing the option(s), save them by pressing the OK button on the RC **before** the cursor is moved to the left, select STORE in the SAM root menu and press OK on the RC.
- The new option setting is only active after the TV is switched "off" / "stand-by" and "on" again with the Mains switch (the NVM is then read again).

### 8.4.2 Dealer Options

For dealer options, in SAM select "Dealer options" and then "Personal options".

#### 8.4.4 Opt. No. (Option numbers)

Select this sub menu to set all options at once (expressed in two long strings of numbers).

An option number (or "option byte") represents a number of different options. When you change these numbers directly, you can set all options very quickly. All options are controlled via eight option numbers.

When the NVM is replaced, all options will require resetting. To be certain that the factory settings are reproduced exactly, you must set both option number lines. You can find the correct option numbers on a sticker inside the TV set and in Table "Option code overview".

**Example:** The options sticker gives the following option numbers:

- 04368 00005 01066 08707
- 00000 00032 00512 00000

The first line (group 1) indicates hardware options 1 to 4, the second line (group 2) indicates software options 5 to 8. Every 5-digit number represents 16 bits (so the maximum value will be 65536 if all options are set).

When all the correct options are set, the sum of the decimal values of each Option Byte (OB) will give the option number. See table "Option code overview" for the options.

**Table 8-5 Option code overview**

CTN	Options Group 1	Options Group 2	Displ. (code)
32PFL9632D/10	41641 05122 04439 45160	10046 00066 00000 00000	62
47PFL9532D/10	41641 04099 04439 45160	10065 00064 00000 00000	81

**Important:** after having edited the option numbers as described above, you **must** press OK on the remote control **before** the cursor is moved to the left!

#### 8.5 Reset of Repaired SSB

A very important issue towards a repaired SSB from a service repair shop, implies the reset of the NVM on the SSB.

A repaired SSB in service should get the service Set type "00PF0000000000" and Production code "00000000000000". Also the virgin bit is to be set. To set all this, you can use the ComPair tool.

## 9. Circuit Descriptions, Abbreviation List, and IC Data Sheets

### Index of this chapter:

- 9.1 Introduction
- 9.2 Display Supply
- 9.3 On-Board Platform Supply
- 9.4 On-board DC/DC Converters
- 9.5 Front-End
- 9.6 PNX8535
- 9.7 Back-end
- 9.8 Ambient Light
- 9.9 Abbreviation List
- 9.10 IC Data Sheets

### Notes:

- Only **new** circuits (circuits that are not published recently) are described.
- Figures can deviate slightly from the actual situation, due to different set executions.
- For a good understanding of the following circuit descriptions, please use the wiring, block (chapter 6) and circuit diagrams (chapter 7). Where necessary, you will find a separate drawing for clarification.

### 9.1 Introduction

This chassis (development name "TV520") is the successor of the BJx.x and FJx.x chassis (development name "Jaguar").

The platform is built around the PNX8535 "System on Chip" (SoC), which performs the video and audio processing. The SoC **integrates** the functionality of ICs that were previously used in the Jaguar platform: PNX2015 (AVIP/COLUMBUS), PNX3000 (MPIF), TDA9975 (HDMI receiver), T6TE0TBG (SPIDER), and PNX8550 (VIPER).

Remaining additional key components in this chassis are:

- PNX5050.
- PACIFIC 3 (T6TF4HFG).

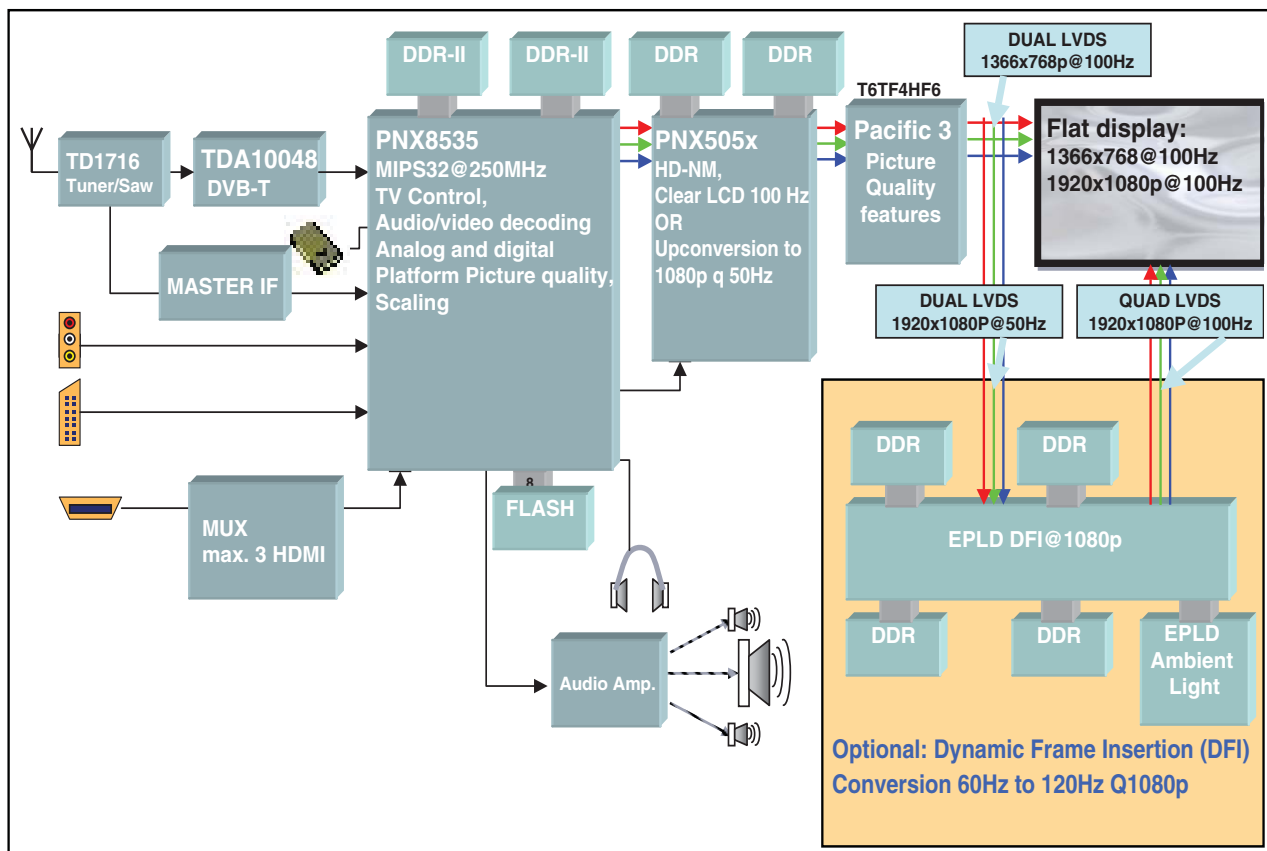
#### 9.1.1 Features

The main features for this chassis are:

- 1080p resolution @ 50 Hz (in some sets)
- Perfect Pixel HD Engine as the successor of Pixel Plus. With this technology, each pixel of the incoming picture is enhanced to better match the surrounding pixels, resulting in a more natural picture. Artifacts and noise in all sources from multimedia to standard TV to highly-compressed high-definition (HD) are detected and reduced. This results in a clean and razor sharp image.
- The introduction of LED AmbiLight.
- ClearLCD, a technology that uses scanning and backlight dimming technology to reduce the motion blur on an LCD screen, caused by the slow response time and the "sample and hold" characteristic of LCD.
- Dynamic Frame Insertion (DFI) (in some sets) resulting in a 1080p resolution @ 100 Hz, thus eliminating the film flicker caused by the frame rate of 24 frames per second of cinematic film.
- Digital Natural Motion (DNM) to compensate the judder (perceived when watching a moving object on the screen), caused as side-effect during frame insertion (DFI) as described above.

#### 9.1.2 TV520 Architecture Overview

For details about the chassis block diagrams refer to chapter "Block diagrams, Test Point Overview, and Waveforms". An



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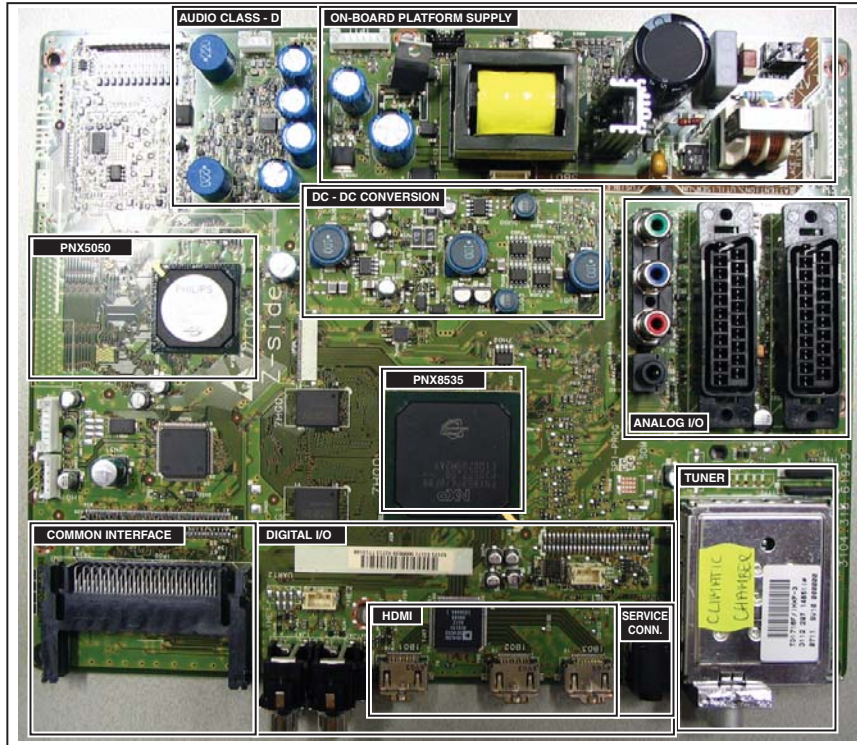
Figure 9-1 Architecture of TV520 platform

Sets with all resolutions @ 50 Hz use the PNX8535 SoC, the PNX5050 Video Back-end Processor and the T6TF4HFG PACIFIC 3 for video processing. With the same configuration, a resolution of 1366x768p @ 100 Hz or 1920x1080p @ 50 Hz can be achieved. To achieve 1920x1080p **in combination with** 100 Hz however, an additional panel is used for Dynamic

Frame Insertion (DFI). This DFI panel also contains an extra EPLD to drive the AmbiLight units.

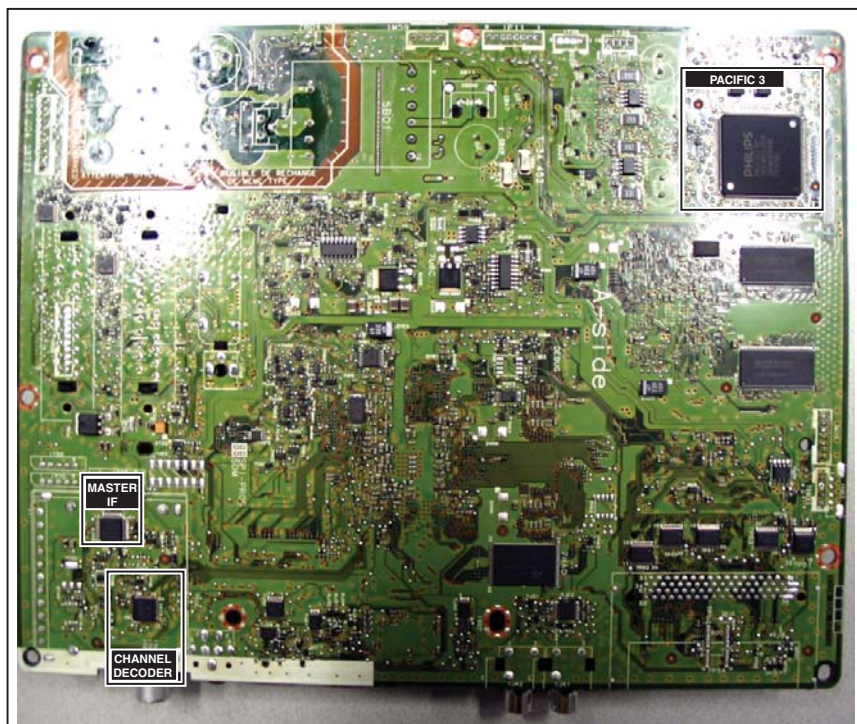
Refer to section “Video Processing” in this chapter for more details.

9.1.3 SSB Cell Layout



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Figure 9-2 SSB top view



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Figure 9-3 SSB bottom view

9.2 Display Supply

For a block diagram see figure "Block diagram Display Supply 32 and 37".

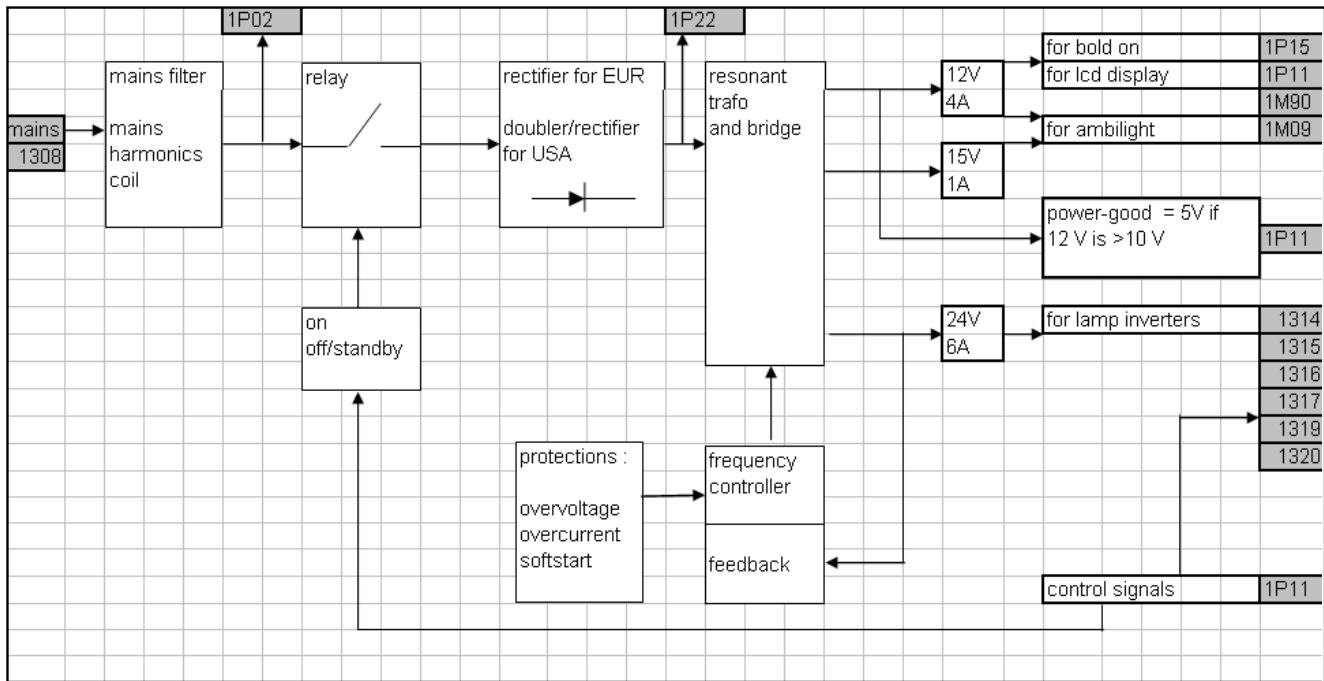
9.2.1 32 and 37" Sets

The 32 and 37" sets in this chassis come with a serviceable Display Supply Unit. This supply unit delivers the power for the display, AmbiLight unit and the optional panel (bold-on) for Dynamic Frame Insertion (DFI). Refer to chapter "Circuit Diagrams and PWB Layouts", diagrams A1 and A2.

The power supply for the SSB and Audio part are located on the SSB itself (on-board Platform Supply). For a description, refer to Section "On-board Platform Supply".

The supply unit delivers the following voltages to the chassis:

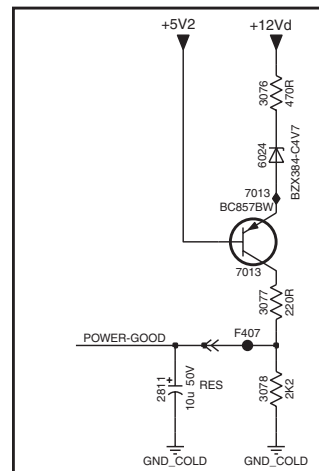
- 12 V for bold-on panel (optional), LCD panel and AmbiLight (optional)
- 15 V for AmbiLight
- 24 V (or 295 V in case of scanning backlight) for lamp inverters.



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Figure 9-4 Block diagram Display Supply 32 and 37"

A Power-Good circuitry is built around items 7013 and 6024. The so-called POWER-GOOD line indicates the correct functioning of the 12 V supply. The voltage on this line is 5 V if the voltage on the 12 V output is > 10 volt. Refer to figure "Power-Good circuitry".



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Figure 9-5 Power-Good circuitry



The output voltages of +12 V, +15 V, and +24 V (or +295 V in case of scanning backlight) are generated through a so-called resonant DC-DC power converter (LLC-converter) (item no. 7005 to 7008 in diagram A2).

An over-voltage protection circuitry is built around item no. 6020 and 6009 and will activate if the feedback circuitry is defective (see diagram A2).

A primary over-current protection circuitry is built around item no. 3055 and 2047 and will activate in case the MOSFETS (item no. 7005 and 7006) are short-circuited. A secondary over-current protection circuitry is built around item no. 2014 and 3021 and will activate a soft-start if a short-circuit is detected in the 12 V and 24 V secondary circuits. See diagram A2 for details.

The following signals interface with the SSB via connector 1P11 (see diagram A1):

- DIM-control: adjusts backlight intensity (pulse-width modulated signal of 3.3 V).
- Power-Good.
- Light on\_off: switches the LCD backlight (0 - 3.3 V).
- Boost (fixed: 3.3 V or left "open" depending on commercial request or display specification).
- Stand-by: stand-by signal and 5.2 V from SSB to switch relay on Display Supply Unit on/off.

### 9.2.2 47" Sets

The 47" sets in this chassis come with a buy-in Sanken Display Supply unit and is a black-box for Service. When defective, a new panel must be ordered and the defective panel must be sent for repair, unless the main fuse of the panel is broken. Always replace a defective fuse with one with the correct specifications! This part is available in the regular market. The supply unit works together with the on-board platform supply (see section "On-Board Platform Supply").

Two versions of this supply unit can be used in this platform:

- Output voltage of +295 V for the scanning backlight displays (not in US region)
- Output voltage of +24 V for all other displays.

Always refer to the Spare Parts list for the correct order number!

The supply unit delivers the following voltages to the chassis:

- Mains voltage (filtered) for SSB (connector 1P02)
- +395 V DC (connector 1P22)
- Scanning backlight display: +295 V DC (connectors 1P05/1P06)
- Non scanning backlight display: +24 V DC (connectors 1319/1316)
- +12 V DC on connectors 1M09, 1M12 and 1P15.

When all these voltages are present, the Power-Good signal becomes "high" (+5 V) (connectors 1P10.2 and 1P11.2).

## 9.3 On-Board Platform Supply

In this platform, an on-board platform supply has been foreseen. This means that the mains voltage, after filtering, is fed to the SSB.

The supply is a Self Oscillating Power Supply (SOPS) and working according to the Quasi Resonant Conversion (QRC) principle. Refer to diagrams B01A and B01B for details. For the on-board DC/DC converters refer to diagrams B02A, B02B and B02C.

### 9.3.1 Start-up sequence

When the Platform Supply is switched "on", the voltage across capacitor 2B50 increases. This will trigger the gate of MOSFET

7B05 (via resistors 3B60 and 3B34). When the voltage on pin 1 of the MOSFET reaches the threshold level, the MOSFET starts conducting. As result, current will flow in the primary winding of transformer 5B01. The output voltage increases but the supply will not start oscillating because the auxiliary voltage is still too "low" to drive the MOSFET autonomously. Oscillating will start only when then auxiliary voltage across capacitor 2B32 is high enough to drive the gate of the MOSFET. This brings the supply in SOPS mode (self-oscillating).

### 9.3.2 Reduced / Maximum Power Mode

When there is no overload and when the supply has reached SOPS mode, the start-up power consumption is limited to appr. 1 A in order to ensure a slow start current control across the MOSFET. This will last as long as diode 6B14 is not (yet) conducting while transistor 7B01 is conducting, thus keeping the supply in Reduced Power Mode.

In general, the "on" time of the transistor 7B05 is a function of the output current. Resistor 3B51 can be seen as sense resistor with a voltage of  $V_{drop}$ . If there is a demand for more power, the negative voltage created from the auxiliary winding and diode 6B11 will cause diode 6B14 and transistor 7B01 to conduct. Transistor 7B01 will put resistor 3B49 in parallel to resistors 3B50 and 3B51. This will result in  $V_{drop}$  across resistor 3B51 to be lower, which causes the "on" time of transistor 7B05 to be longer, which enables the supply to deliver more power. This brings the supply in Maximum Power Mode.

### 9.3.3 Peak current control

The peak start-up current flowing across the MOSFET also influences the voltage across the sense resistor 3B51 and will cause transistors 7B02 and 7B00 to conduct (via resistor 3B50). The voltage across this resistor is sensed in order to control the maximum power.

### 9.3.4 Output voltage control

The voltage at the +12 V output supply line will increase until the zener voltage of diodes 6B02 and 6B13 is reached. The output voltage is controlled via a feedback-loop formed by components 3B54, 6B02, 6B13, 7B04, 3B47, 7B02 and 7B00. When the voltage exceeds 12.6 V the zener diodes 6B02 and 6B13 will conduct and will trigger opto-coupler 7B04. After a while transistors 7B02 and 7B00 will start to conduct and this will switch "off" MOSFET 7B05. The feedback-loop will become stable after a while, thus controlling the output voltage.

### 9.3.5 Over-voltage protection

In case of malfunctioning of the output voltage control feedback-loop as described above, the supply goes into over-voltage protection. When the negative primary auxiliary voltage (present at the anode of diode 6B11) reaches the zener voltage of diode 6B03, MOSFET 7B05 will switch off. This causes transistors 7B02 and 7B00 to conduct which will result in an output voltage drop.

### 9.3.6 Audio protection / DC protection

When a fault occurs in the audio amplifiers (e.g.a short-circuit), a voltage is sent via the AUDIO-PROT line which will trigger thyristor 7B50. This will cause the +12 V output line to drop to appr. +3 V. The only way to reset the thyristor is to disconnect the set from mains. After re-connect to mains, the supply will restart normally, if the defective audio amplifier has been repaired.

### 9.3.7 Service Tips

After replacing some components in the primary circuit of the Platform Supply, a variable transformer has to be used to ramp up the mains voltage from 0 to 30 V. This will result in the Platform Supply to start-up. Monitor the +12 V output voltage and increase the mains voltage until the regulation and feedback loop are working.

Connector 1B40 can be used by Service to power the SSB directly by an external power supply. This enables the SSB to start-up without the use of the Display Supply. The power consumption of the SSB is:

- 1.5 to 2.0 A for the +12 V line.
- 2.0 A for the +5 V line.

It should be noted that, by using this method, the audio amplifier and the audio protection are not tested!

## 9.4 On-board DC/DC Converters

In this platform, on-board DC/DC converters have been foreseen. See also diagrams B02A, B02B and B02C.

### 9.4.1 PSU Start-up Sequence

1. If the input voltage of the DC/DC converters is around 12 V (measured on the decoupling capacitors 2U01/2U02/2U93) and the ENABLE signals are "low" (active), then the output voltages should have their normal values.
2. First, the Stand-by Processor activates the +1V2 supply (via ENABLE-1V2).
3. Then, after this voltage becomes present and is detected OK (about 100 ms), the other voltage of +3V3 will be activated (via ENABLE-3V3).
4. The current consumption of controller IC 7U00 is around 20 mA (that means around 200 mV drop voltage across resistor 3U01).

### 9.4.2 +12V Switch

- The +12V switch is activated when the POD-MODE signal is "low".
- The rise time of the output voltage is set by components 2U12, 3U42, and 3U64 and is about 30 ms.
- The switch "off" is fast, because there can be fault currents that must be interrupted.
- When the input voltage (+12VS) is higher than 12.6 V, the switch is disabled via circuit 6U01, 3U09, 3U18, 2U14, and 7U14-2.

### 9.4.3 Internal Protection

- Provides a SUPPLY-FAULT signal (active "low"), when the output voltage of any DC/DC converter is out of its limits ( $\pm 10\%$  of the normal value). In such cases, the Stand-by Processor will immediately stop the supplies by sending a "high" control signal towards the external and internal supplies: ENABLE-xVx, POD-MODE, ON-MODE, and STAND-BY.  
**Note:** The SUPPLY-FAULT control signal is "low" when any DC/DC converter is disabled by its control signal (ENABLE-xVx) and +12VSW is present, therefore it is ignored during start-up!
- The internal protection works together with the output over-voltage detector transistors 7U15-1, 7U15-2, 7U29-1, and 7U29-2.

### 9.4.4 1.2V and 3.3V DC/DC Converters

#### Introduction

The circuit used is a so-called "synchronous buck converter". Some characteristics:

- Switching frequency: approx. 250 kHz.
- Efficiency: approx. 90%.
- Built-in output over-voltage and over-current protections
- Soft start.
- Software controlled "on/off" (via ENABLE line).

#### Block diagram

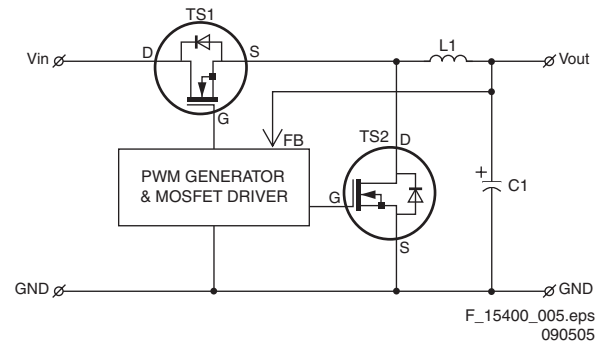


Figure 9-6 Block diagram synchronous buck converter.

The advantage of a "synchronous buck converter" over a "classical buck converter" is its better efficiency (about 90%). The difference between the two is that in a synchronous buck converter the "low-side" diode is replaced by a MOSFET TS2 (item 7U03). This, because the voltage drop across a MOSFET is smaller than the forward voltage drop of a diode. This second MOSFET TS2 conducts current during the "off" times of the first MOSFET TS1 (item 7U01 at the input side). The upper MOSFET TS1 conducts, to transfer energy from the input to the inductor  $L_1$  and load  $R_L$ , while the lower MOSFET TS2 conducts to circulate the inductor current (free wheel). The synchronous PWM control block regulates the output voltage by modulating the conduction intervals of the upper and lower MOSFETs.

#### PWM Generator and MOSFET Drivers

This circuit is a one-chip solution (item 7U00). It contains all the circuitry for two independent buck regulators (3V3 and 1V2). The MOSFETs 7U01, 7U06, 7U03 and 7U08 are the switching transistors, they are conducting alternatively.

- Time sequence 1: 7U01/7U06 is conducting; energy is stored in coil 5U01/5U02. The current is flowing from the +12VSW power supply source.
- Time sequence 2: 7U01/7U06 is blocked; energy is stored in coil 5U01/5U02.
- Time sequence 3: 7U03/7U08 is conducting, and the current circuit is now closed via 7U03/7U06, 5U01, 5U02, 2U09/2U94/2U21/2U90/2U921/2U92, and the load. So the energy stored in the coil during time sequence T1 is consumed during sequence T3. The signal on the gate 7U03/7U08 is 180 degrees turned compared with the signal on the gate 7U01/7U06.

#### Voltage Booster

This circuit is build around capacitors 2U29 and 2U26, resistor 3U62/3UA1, diodes 6U06 and 6U04, and transistor 7U07. It generates the +18 V boost voltage on pin 4 of item 7U00, to drive the "high-side" power MOS-FET 7U01/7U06. The voltage is generated only during normal operation of the converter; therefore, any drop in its value means an internal fault condition, which is sensed by the internal protection circuit. The AC component of the voltage on the source of transistor 7U01/7U06 is rectified by the diodes and added to the input voltage, resulting into the boost voltage. The resistor 3U62/3UA1 limits the peak current through the rectifier diodes.

#### Over-current Detection

Over-current detection is done via components 3U33, 3U26, 3U27, 3U31, and 2U18 for the 3.3 V converter and 3U43, 3U36, 3U37, 3U40, and 2U20 for the 1.2 V converter.

#### Under-voltage Detection

There is an additional circuit (7U10 and 7U11) to switch "off" the 3.3 V converter in case the +12VS drops below 9 V.

**Service Tips**

- When a power MOS-FET is found defective, replace the other power MOS-FET as well.
- For a normal operation of the converter, it is important to check the switching frequency and the value of the boost voltage.

- I<sup>2</sup>C address 0x10.
- Decoding from low-IF to MPEG transport stream.
- During decoding: de-modulation, de-interleaving and error correction.
- External clock buffer required.
- No start-up requirements.
- AGC monitor.

**9.5 Front-End**

Refer to figure “Architecture of TV520 platform” earlier in this chapter for details. Refer also to block diagrams B03A, B03B and B04.

**9.5.1 Device specifications****Tuner (TD1716)**

The tuner has the following specifications:

- Hybrid tuner with symmetrical IF output.
- Down conversion from RF to IF frequency (picture carrier 39.875 MHz at analogue reception, centre frequency 36.166 MHz at digital reception).
- AGC control signal is coming from master IF device (TDA9898).
- Only 5 V external supply needed (internal DC-DC conversion to 3.3 V).
- 4 MHz output is used by channel decoder (TDA10048) and master IF device (TDA9898).

The application in this chassis is as follows:

- I<sup>2</sup>C address C0.
- Broadband AGC, no IF section.
- I<sup>2</sup>C communication buffered via MUX.
- Gain to obtain optimised Master IF input level; AGC control is completely inside the tuner.
- Output level ca. 110 dB $\mu$ V (for strong input signal).

**Repair tip:** after replacement of the tuner, the option code should be checked, even when the set appears to function correctly! Refer also to chapter 5 “Service Modes, Error Codes, and Fault Finding”.

**Master IF (TDA9898)**

- Down conversion from IF to low-IF frequency.
- Down conversion from IF to SIF.
- CVBS output.

The application in this chassis is as follows:

- I<sup>2</sup>C address 0x86.
- Down conversion from IF to low-IF frequency (5.166 MHz centre frequency).
- Advanced filtering (for further rejection of adjacent channels).
- Gain to obtain optimised channel decoder level. Control signal is coming from channel decoder.

**SAW filter****X6766D or X6872D**

- Analogue sound for BG, I, DK, L, L'.
- DVB-T (digital reception sound **and** video).

For digital reception, the application in this chassis is as follows:

- Rejection of adjacent channels.
- Switching is done by Master IF (3 inputs).
- One SAW covering both 7 and 8 MHz channels.

**X6768D**

- Analogue video for BG, I, DK, L, L'.

**Channel decoder (TDA10048)**

The channel decoder has the following specifications:

**9.5.2 Analogue signal processing (front-end)**

Refer to figure “PAL/SECAM video broadcast reception block diagram” and “PAL/SECAM audio broadcast reception block diagram” for details of analogue signal processing.

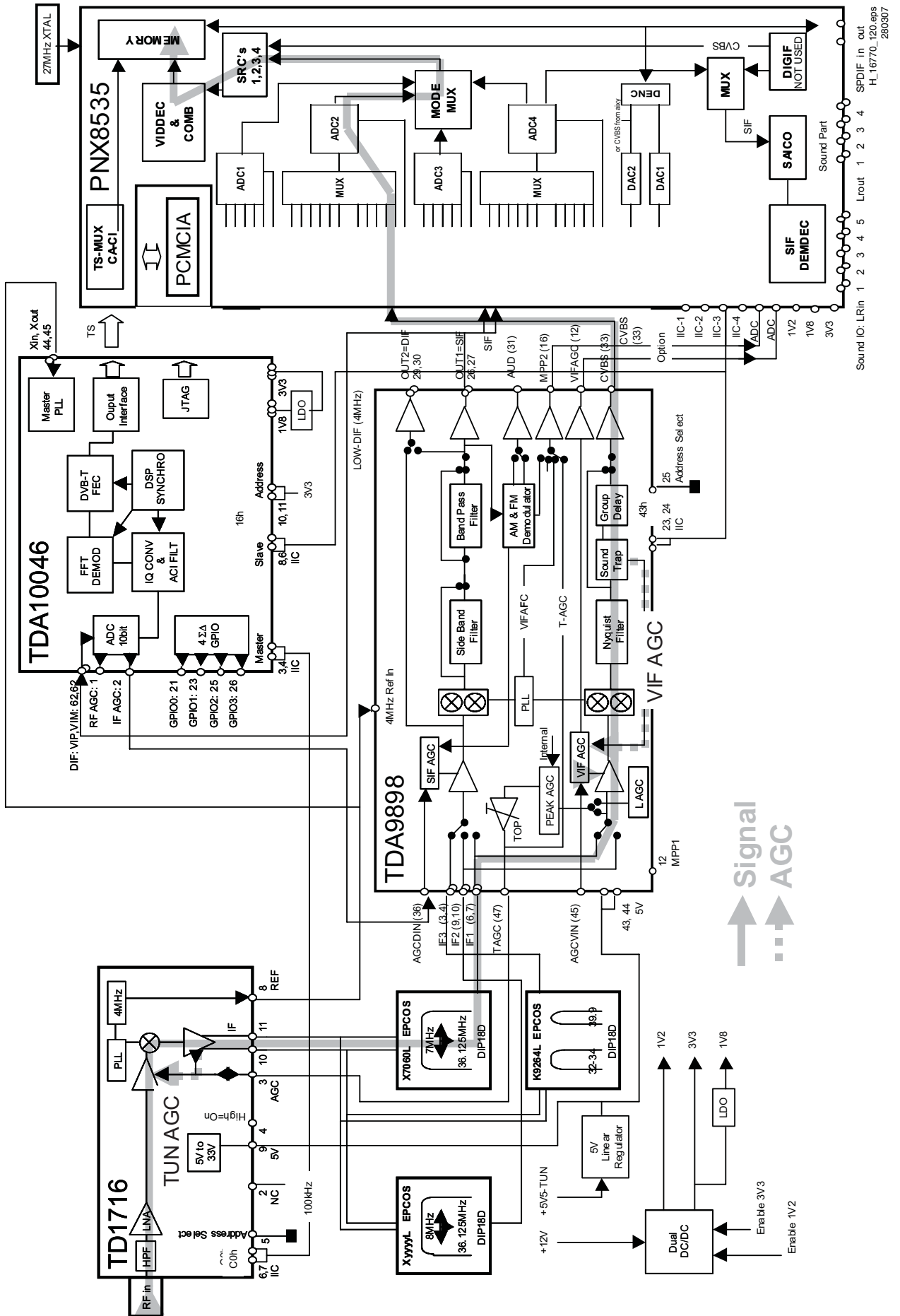
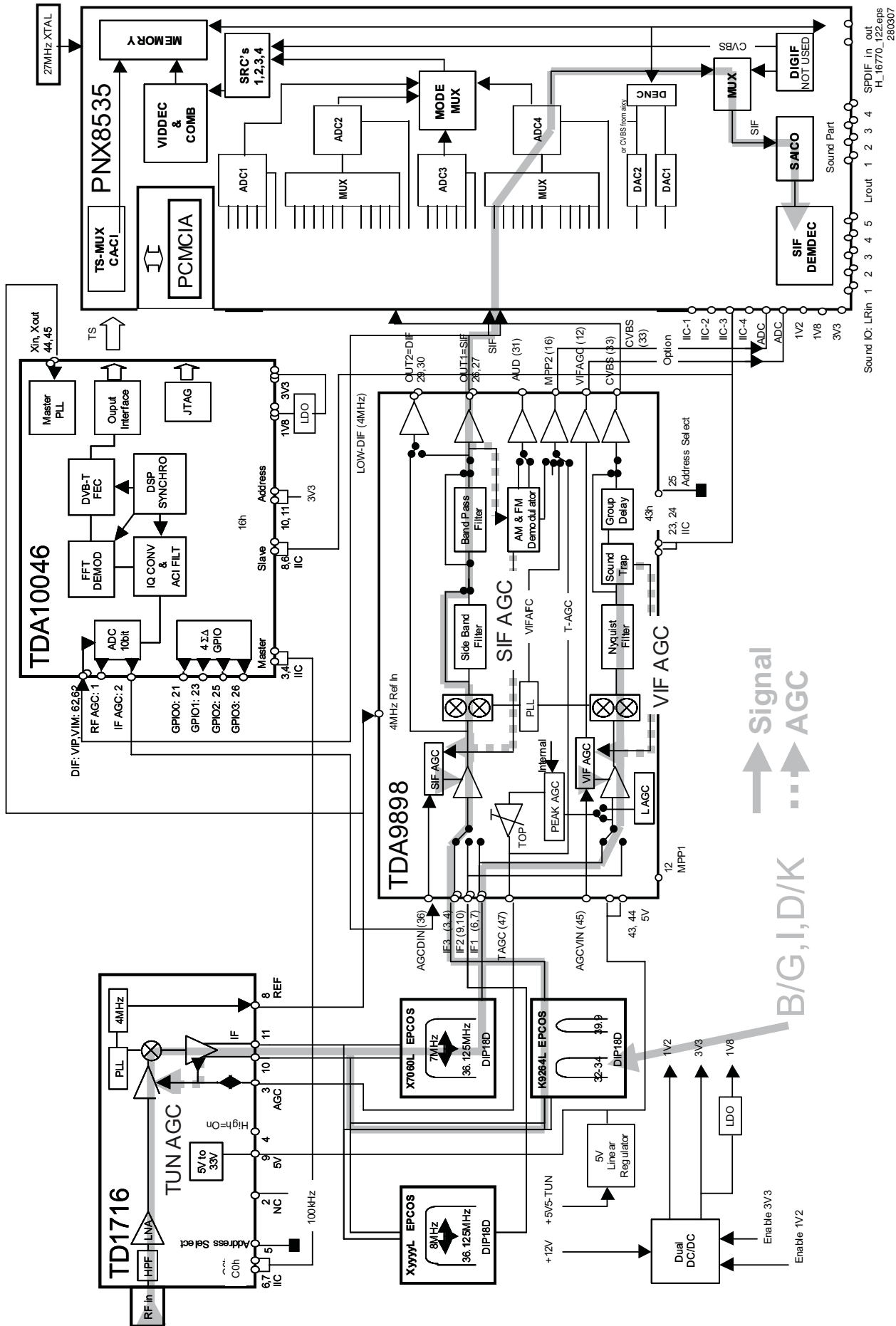


Figure 9-7 PAL/SECAM video broadcast reception block diagram

Sound ID: LFRin 1 2 3 4 5 Lrout 1 2 3 4 SPDIF in out H\_16770\_120.eps 280307





Sound ID: LRin 1 2 3 4 5 Lrout 1 2 3 4 SPDIF in out  
 H\_16770\_122.eps 280307

Figure 9-8 PAL/SECAM audio broadcast reception block diagram

9.5.3 Digital signal processing (front-end)

Refer to figure “DVB-T signal broadcast reception block diagram” for details of digital signal processing.

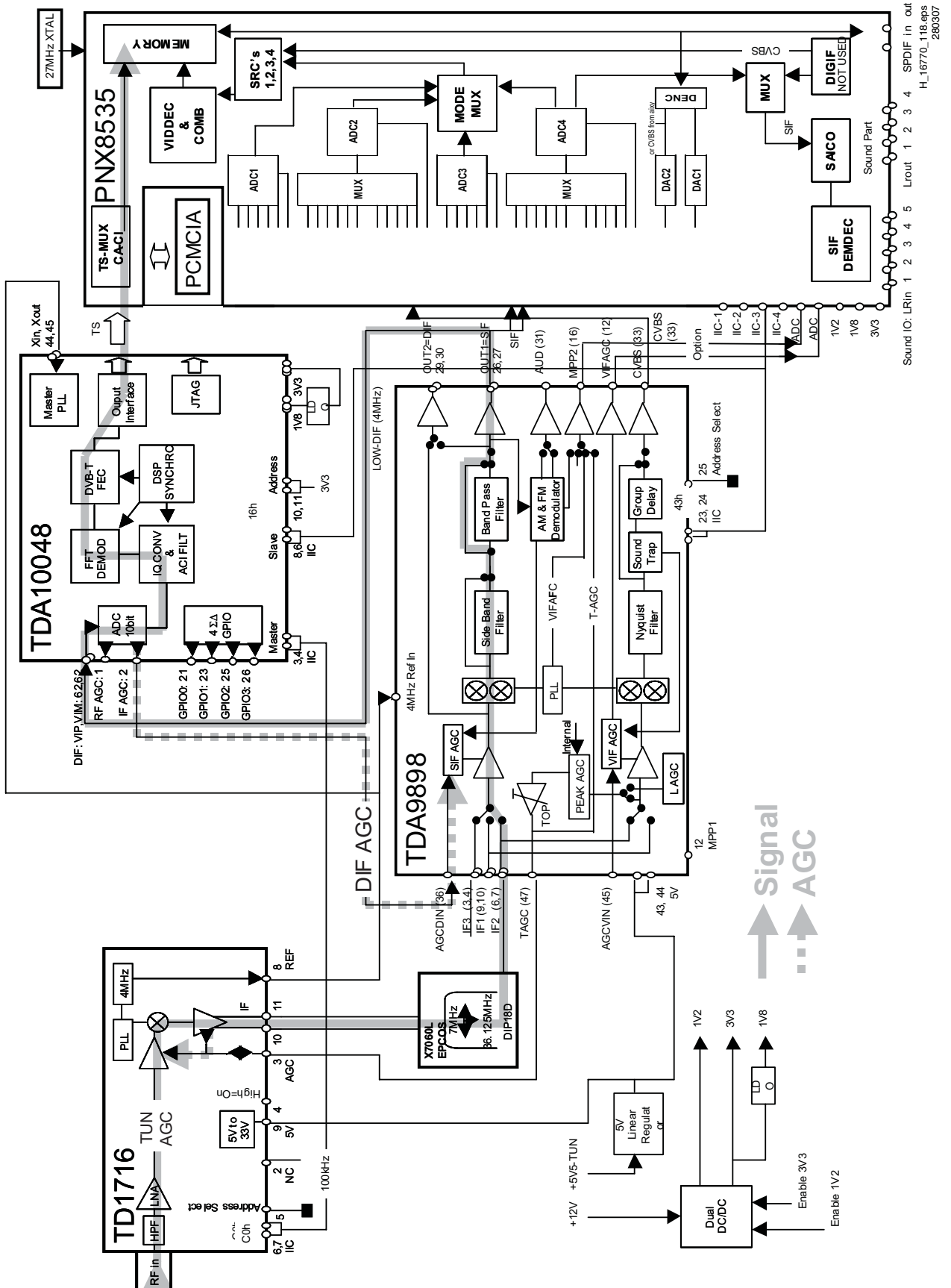


Figure 9-9 DVB-T signal broadcast reception block diagram

## 9.6 PNX8535

In this chassis, the PNX8535 is responsible for the audio/video source decode functions and video improvement processing on both digital and analogue sources. It includes a multi-standard digital video decoder for MPEG2, and a multi-standard analogue video decoder for support of PAL, NTSC, and SECAM standards. Refer to diagram B04 for details.

## 9.6.1 Video Subsystem

Refer to figure "Video flow diagram" for a clarification of the blocks that are used in this device.

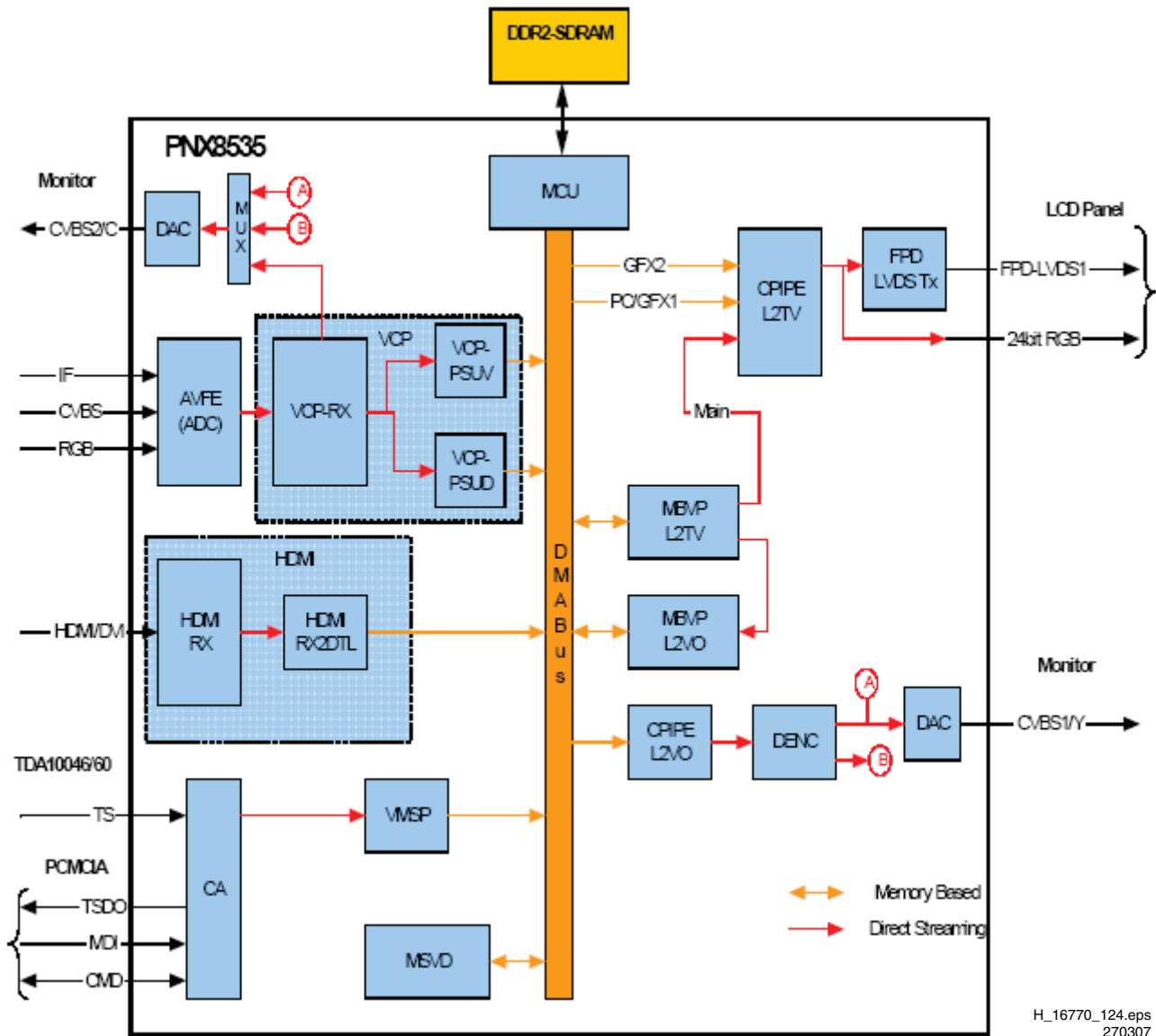


Figure 9-10 PNX8535 video flow diagram

The Analogue Video Front-End (AVFE) provides the interface to external analogue baseband video sources and IF inputs. It supports the following inputs:

- 1fh - CVBS, Y/C, YPbPr, RGB.
- 2fh - YPbPr, RGB.
- IF - low-IF, SSIF.

The Video Capture Pipe (VCP) is used to capture analogue video inputs and consists of a number of blocks:

- The VCP-RX block that contains digital IF processing, a Video Decoder, a 3D-combfilter, and a VBI-Data Capture unit together with a number of smaller control functions.
- The VCP-PSUD which allows VBI data, such as Teletext and Closed Captioning, to be stored in memory.

- The VCP-PSUV which allows captured video data to be stored in memory.

The HDMI receiver interface supports the capture of signals compliant with the HDMI V1.1 specification. It consists of two blocks:

- Block HDMI-RX contains the de-serialiser, HDCP, audio and video data capture and info packet extraction, together with audio formatting.
- Block HDMI-RX2DTL allows captured video data to be stored in memory.

The Memory Based Video Processor TV (MBVP\_L2TV) is used on the main video channel for de-interlacing and scaling of images, together with video measurement functions.

The Video Composition Pipe TV (CPIPE\_L2TV) is used to perform picture improvements on video and merge the video layer and 2 graphics layers into a single stream.

The Flat Panel Display-LVDS (FPD-LVDS) provides a serial interface for 10-bit RGB output data towards the LCD panel with a data rate of 90 Mpixels/s max.

The Memory Based Video Processor VO (MBVP\_2LVO) is used on the main video channel for scaling of images for monitor out.

The Video Composition Pipe VO (CPIPE\_VO) is used to merge a video and a graphics layer into a single stream together with insertion of VBI and CGMS data.

The Digital Encoder (DENC) supports encoding of a digital video stream from the CPIPE\_VO into Analogue CVBS or Y/C.

### 9.6.2 Audio Subsystem

Refer to figure "Audio flow diagram" for a clarification of the blocks that are used in this device.

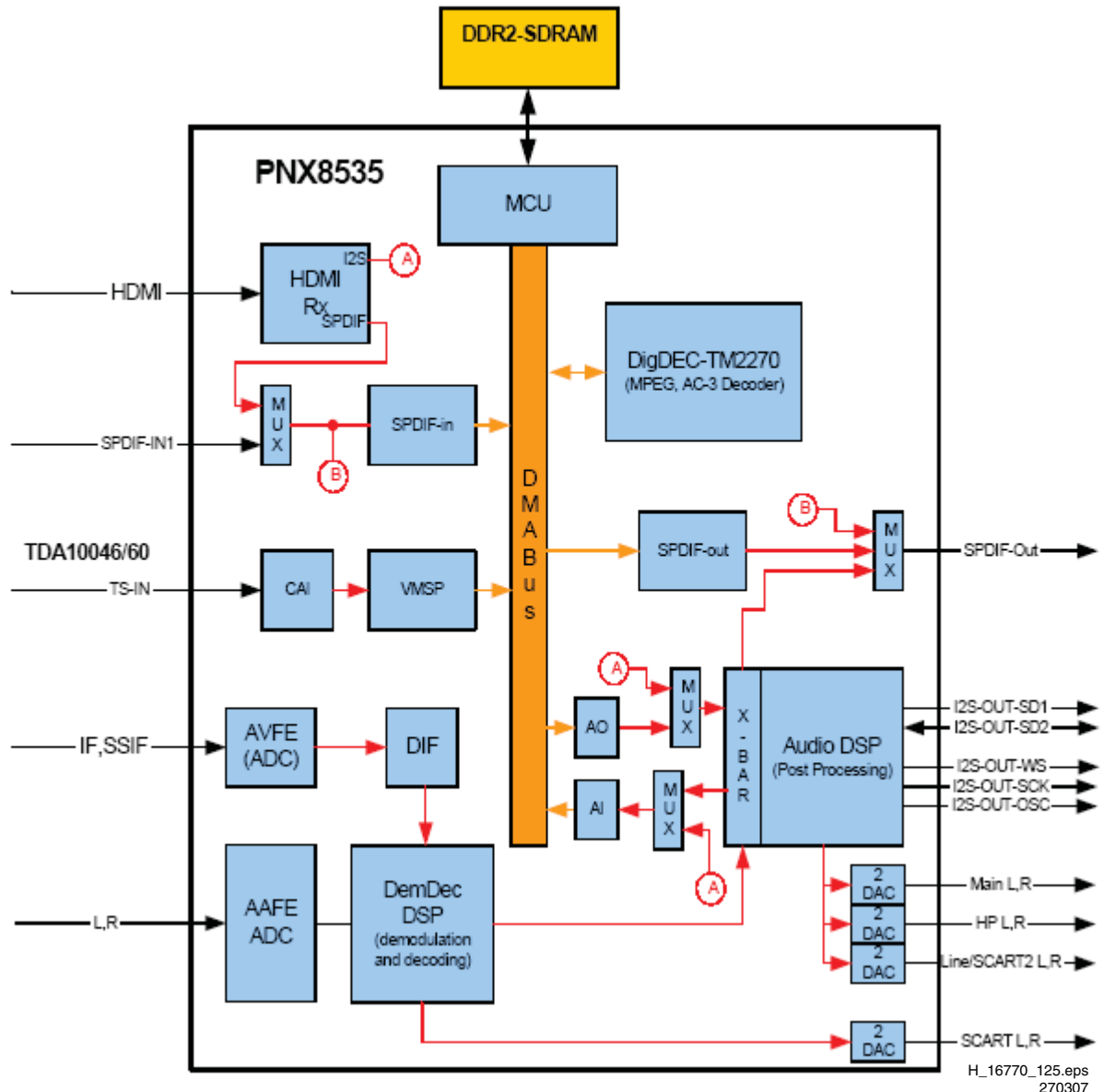


Figure 9-11 PNX8535 audio flow diagram

The Analogue Audio Front-End Input (AAFE) block is used to capture Baseband Audio Inputs.

The Sony/Philips Digital Interface (SPDIF) input is used to get compressed data into the system memory. The multiplexer in front of the block allows two possible sources of SPDIF signals.

The SPDIF Output is used to generate either PCM data or a compliant IEC-61937 compressed stream containing MPEG/Dolby Digital format.

The Audio Input (AI) block is used to transfer stereo audio (I<sup>2</sup>S channel) from the Audio DSP into the system memory for "lip-sync" delay.

The Audio Output (AO) block supports output of up to four stereo I<sup>2</sup>S channels. The AO is used to transfer data from the system memory to the Audio DSP, for post processing of the signal at a sampling frequency of 48 kHz (max.).

Demodulation & Decoding DSP is used for demodulation and decoding of all analogue terrestrial TV sound standards that the TV520 platform covers.

The Audio Post-Processing DSP supports DPLII together with volume and tone control, spatialisers, and equalizers for 6 channels (max.)

Digital Audio Decoder DSP is used to decode digital compressed streams such as MPEG and AC-3. This runs as SW Codecs on the AV-DSP.

**9.6.3 Audio-Video Codec Subsystem**

The AV Codec subsystem consists of the modules required to capture and de-scramble Transport stream inputs together with decoding of Audio/video Streams. Refer to figure “PNX8535 video flow diagram” for a clarification.

The sub-system consists of the following modules:

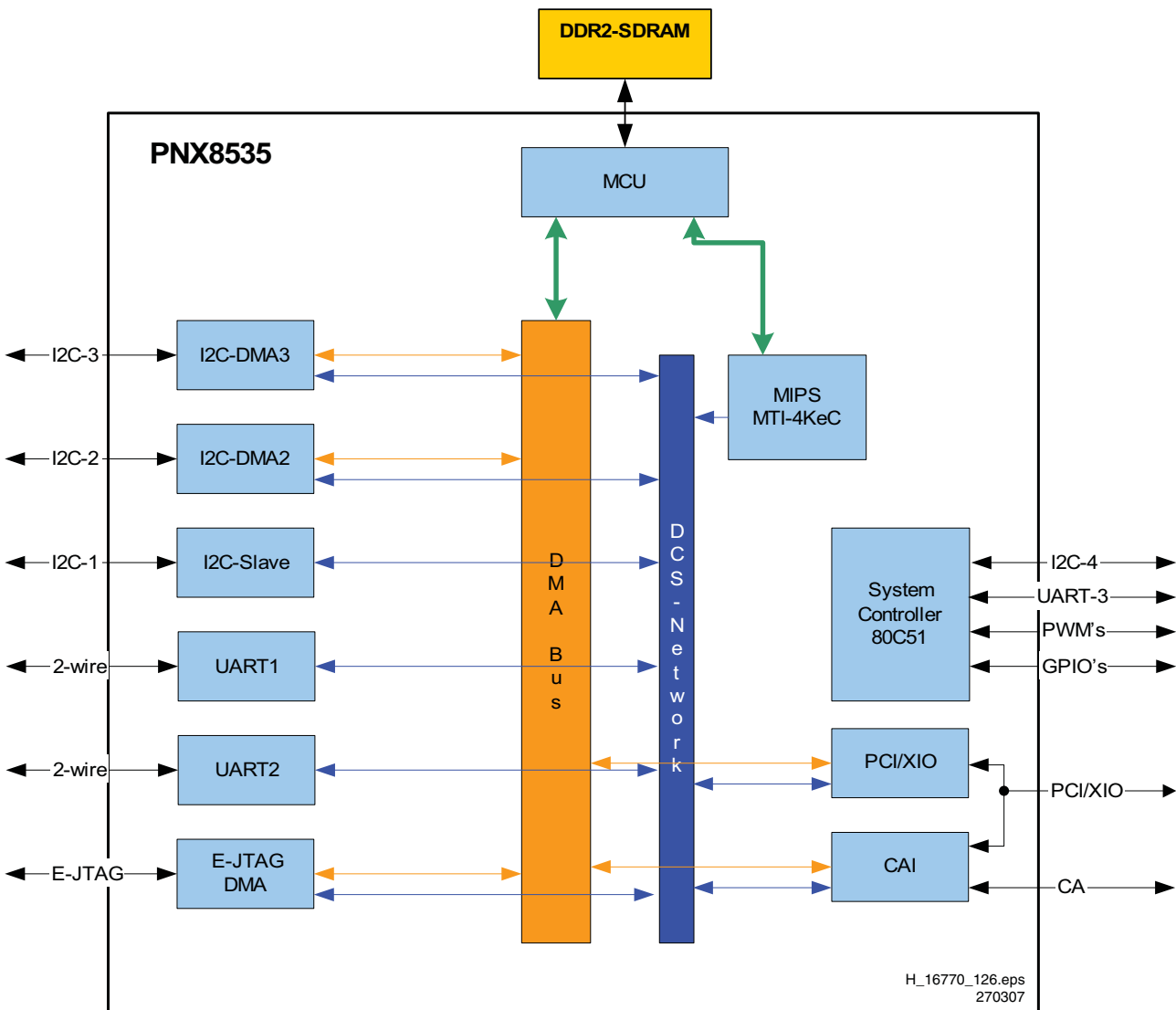
The Conditional Access Interface block provides a direct interface towards a PCMCIA socket for Conditional Access. It supports both the DVB CI-CA Specification and the CableCard (POD) Interface.

The MPEG System Processor (VMSP) provides parsing an MPEG-2 transport stream, including de-scrambling, de-multiplexing and appropriate routing of data to the memory.

The Video MPEG Decoder (VMPG) performs MPEG2 decoding for both MP@ML and MP@HL streams.

**9.6.4 Control and Compute Subsystem**

Refer to figure “Control and compute subsystem” for a clarification of the blocks that are used in this device.



**Figure 9-12 Control and compute subsystem**

The Control and compute subsystem consists of the main processor, control peripherals and the memory system.

The MIPS 4KEc is a 32-bit MIPS RISC core. It has direct access to connectivity peripherals to support system features via PCI, I<sup>2</sup>C, UART or General Purpose I/O. A JTAG interface provides processor software debug capabilities.

The Memory Control Unit (MCU) is a 32-bit DDR2 SDRAM interface supporting DDR2-533 with an address range of 128 MB (max.).

The PCI/XIO interface supports PCI Rev2.2 and can be used to access 8/16-bit external NAND-Flash memory.



The Conditional Access Interface supports direct control and communication to the PC-Card attached to a PCMCIA interface. The interface supports the DVB CI-CA and CableCard specification.

### 9.7 Back-end

Refer to figures “Architecture of TV520 platform” earlier in this chapter for details. Refer also to block diagrams B04, B05 and B06.

Table 9-1 Back-End key component overview

Region/ specification	Processing	Picture enhancement	DFI panel
EU	PNX5050	Pacific	no
HD @ 50 Hz			
HD @ 100 Hz			
fHD @ 50 Hz			
fHD @ 100 Hz			yes

#### 9.7.1 HD sets @ 50 / 100 Hz

In HD sets (50 / 100 Hz), the output signal coming from the PNX8535 is fed to the PNX5050 and then to the PACIFIC 3; the PACIFIC 3 also drives the AmbiLight units. The PACIFIC 3 also generates the pulse-width modulated signal needed for the “Dimming Backlight” feature, which ensures additional motion sharpness. As some displays require an analogue signal to switch the LCD, a multiplexer is added to transform the pulse-width modulated signal. An additional signal, coming from the PNX8535, makes the selection between analogue and pulse-width modulation, depending on which display is used. Scanning Backlight displays require an analogue signal, and all other displays a pulse-width modulated.

Refer to figure “Block diagram display control HD-sets 50 / 100 Hz”.

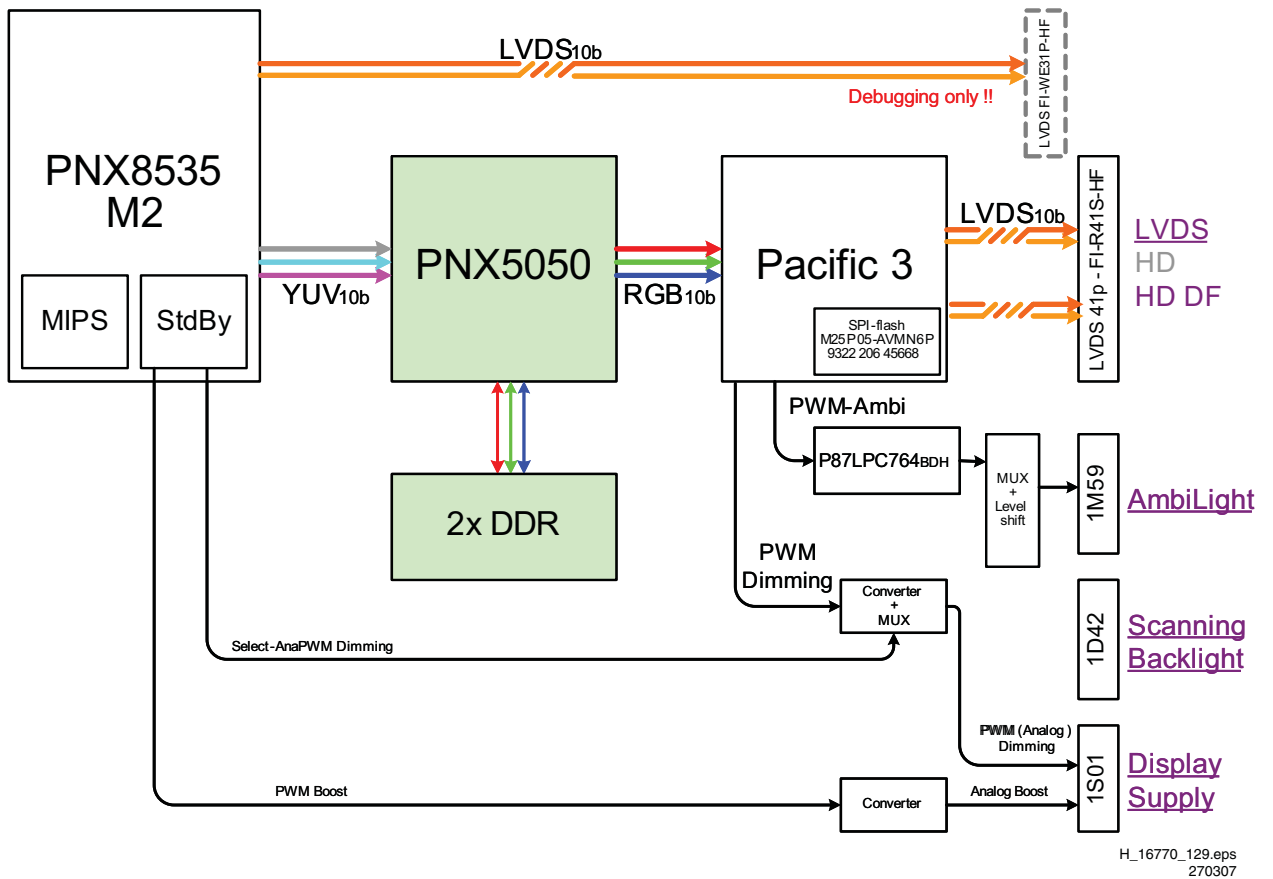


Figure 9-13 Block diagram display control HD-sets 50 / 100 Hz

9.7.2 Full-HD sets @ 50 / 100 Hz

In full-HD sets @ 50 / 100 Hz, the output signal coming from the PNx8535 is fed to the PNx5050 and then to the PACIFIC 3. The PACIFIC 3 also generates the pulse-width modulated signal needed for the "Dimming Backlight" feature, which ensures additional motion sharpness. As some displays require an analogue signal to switch the LCD, a multiplexer is added to transform the puls-width modulated signal. An additional signal, coming from the PNx8535, makes the

selection between analogue and pulse-width modulation, depending on which display is used. Scanning Backlight displays require an analogue signal, and all other displays a pulse-width modulated. The signal for "Scanning Backlight" is an I<sup>2</sup>C signal coming from the PNx8535.

Refer to figure "Block diagram display control Full-HD sets 50 / 100 Hz".

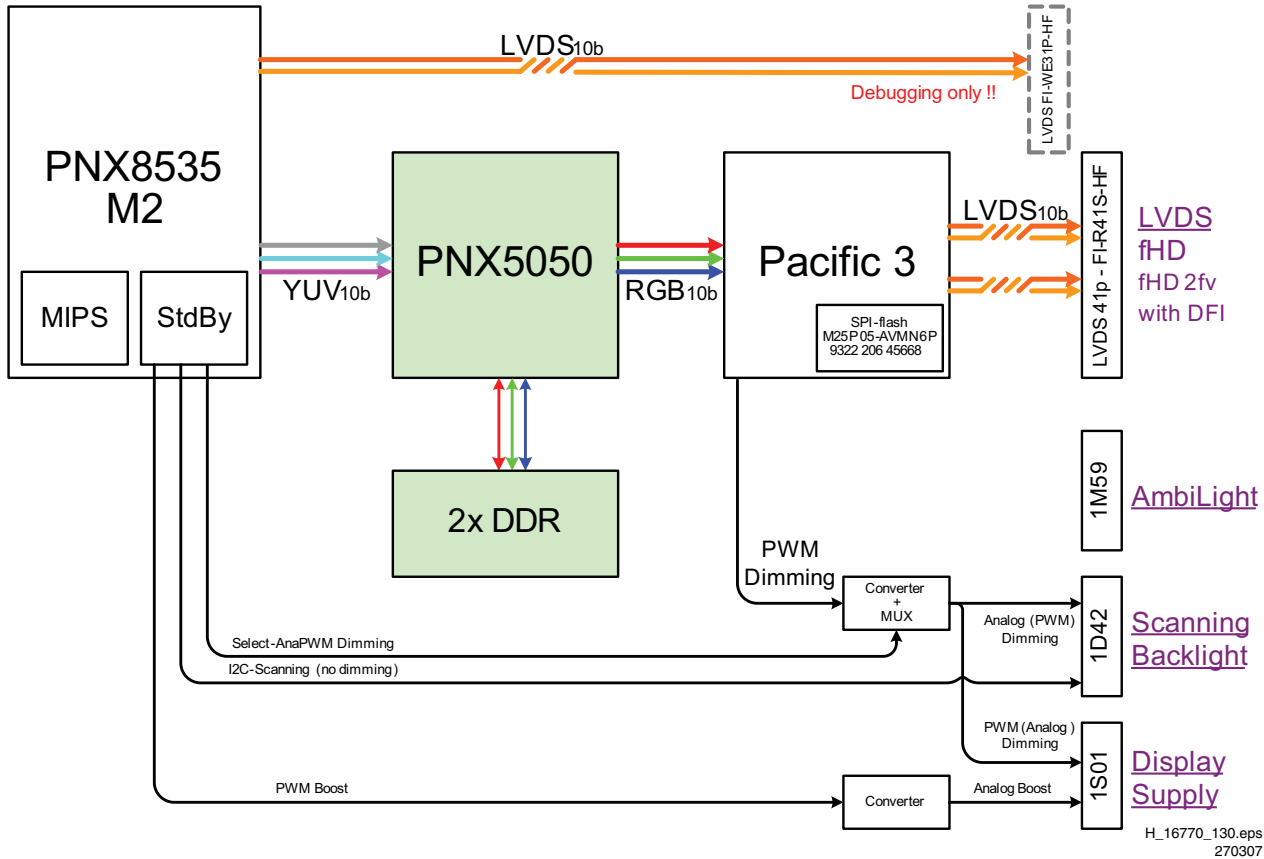
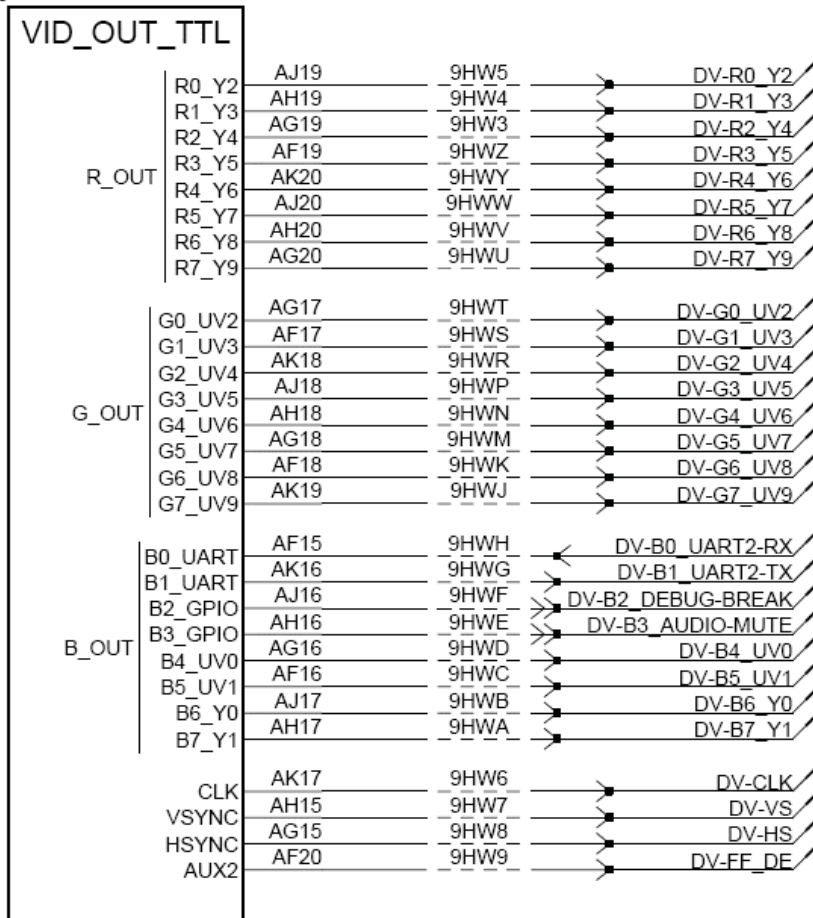


Figure 9-14 Block diagram display control Full-HD sets 50 / 100 Hz

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7H00-8  
PNX8535



H\_16770\_132.eps  
270307

Figure 9-16 PNX8535 CMOS interface to PNX5050 or CYCLONE II (diagram B040)

9.7.4 PNX5050

The PNX5050 performs the following tasks:

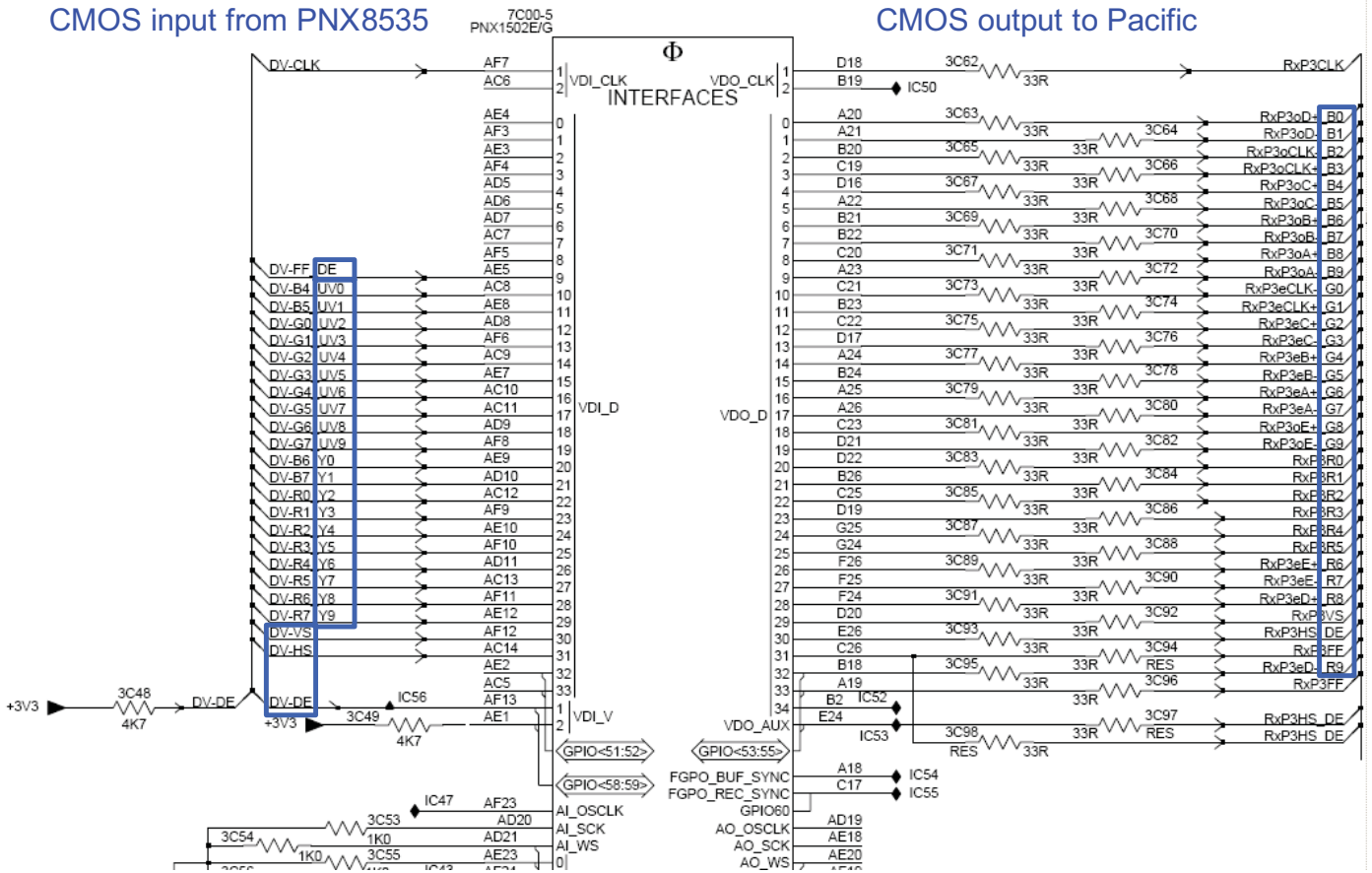
- Picture quality improvement (Natural Motion, etc.).
- Video and graphics (On Screen Display) mixing.
- Upconversion from 50 to 100 Hz.
- Scaling/deinterlacing (720p/1080i to 1080p).

- Video input (CMOS).
- Graphics input (PCI).
- Video & Graphics (CMOS).
- I<sup>2</sup>C.
- Field memory (2x DDR).

The PNX5050 interfaces:

Refer to figure “PNX5050 CMOS interface (input and output)” for details.

CMOS input from PNX8535



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Figure 9-17 PNX5050 CMOS interface (input and output)



9.7.5 PACIFIC 3 (T6TF4HFG)

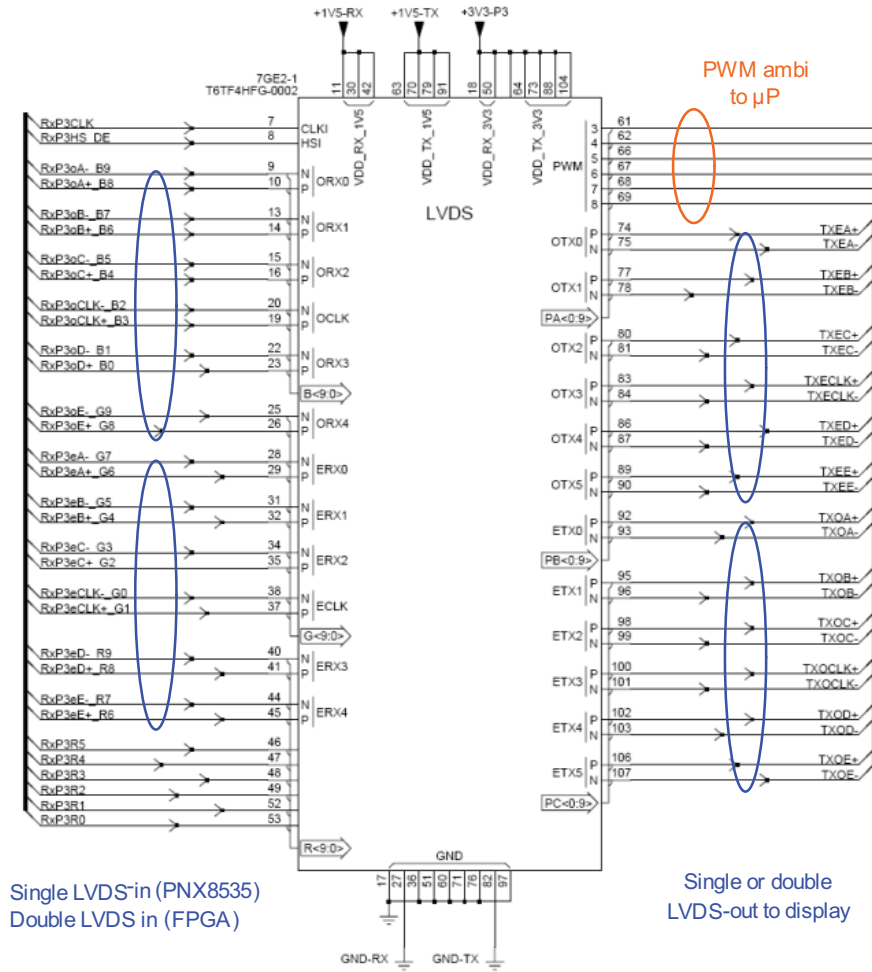
The PACIFIC 3 performs the following tasks:

- Colour processing.
- Sharpness improvement.
- Backlight dimming.
- AmbiLight.
- Display (LVDS) switch on/off.
- Pattern generator.

- Video input:
  - US sets: single LVDS from PNX8535, dual LVDS from CYCLONE II
  - CMOS from PNX5050
- Video output: LVDS (single or dual) to display or DFI panel.
- Backlight dimming: pulse-width modulated (PWM) followed by PWM to I<sup>2</sup>C conversion.
- AmbiLight: pulse-width modulated (PWM) followed by microprocessor.

Refer to figure “PACIFIC 3 CMOS + LVDS interface” for details.

The PACIFIC 3 interfaces:



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Figure 9-18 PACIFIC 3 CMOS + LVDS interface

A configuration flash memory is added (item 7GE1).

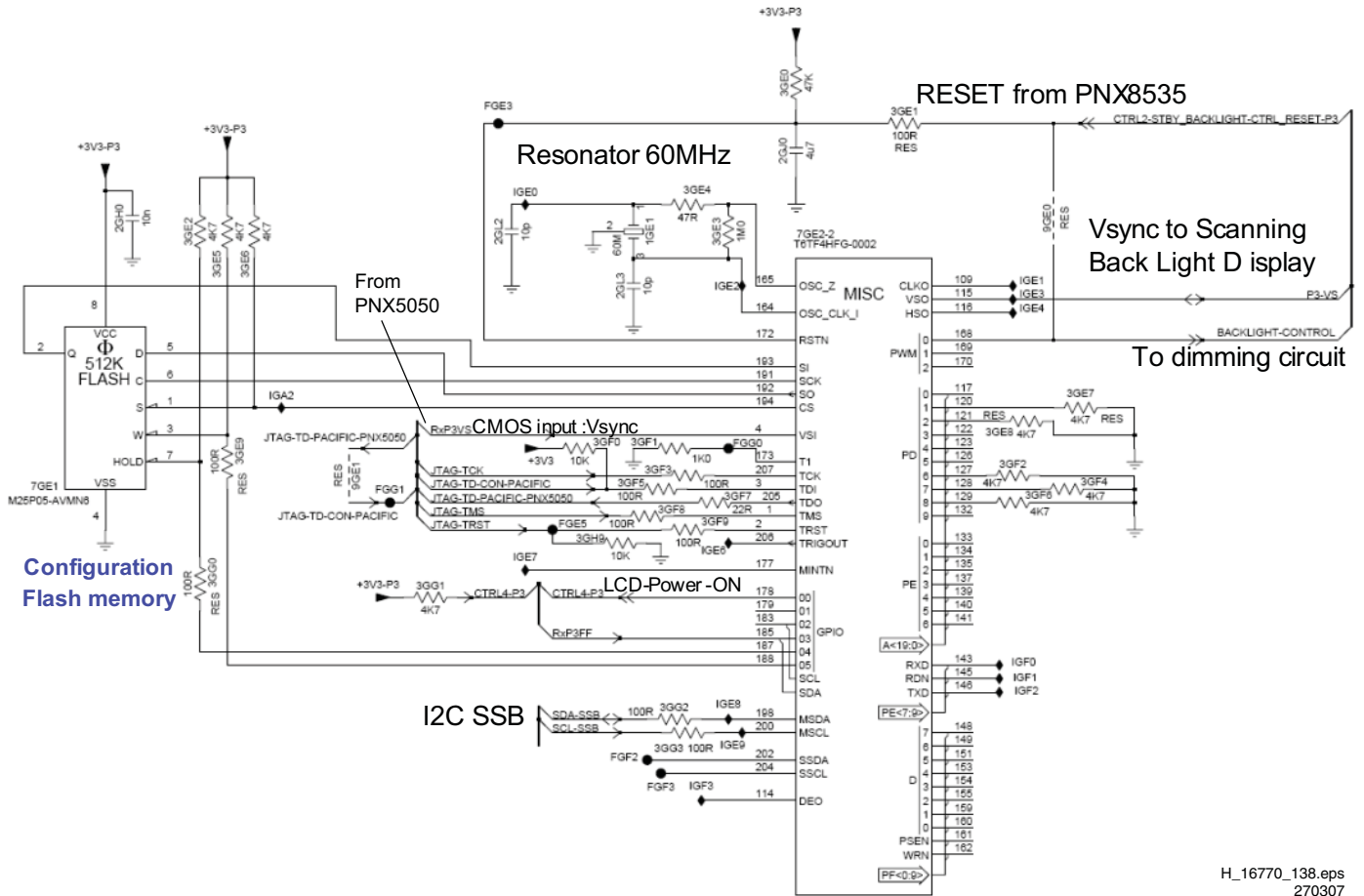


Figure 9-19 PACIFIC 3 control (diagram B06C)

### 9.8 Ambient Light

In this chassis, LED AmbiLight units are introduced as the successor of CCFL AmbiLight units. The system that is used is called "LUMILED". The units are completely aligned in factory and are a "Black Box" for Service. When defective, they must be replaced entirely. Refer to the Spare Parts List for the correct order number.

The AmbiLight units are addressed by I<sup>2</sup>C. The I<sup>2</sup>C-address for all lamp units is "6A". To address each unit separately, selection lines are needed to distinguish between the boards. Control is done by the SSB. Refer to figure "Ambient Light block diagram" for details.

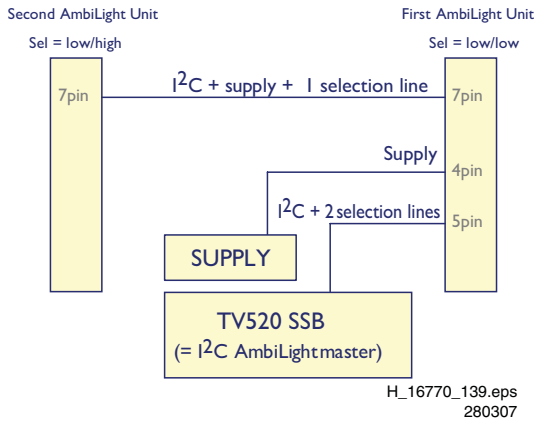


Figure 9-20 Ambient Light block diagram

To address a specific unit, the selection line has to be "low".

In case of 2-sided AmbiLight, 6 data bytes are used to address the units, 3 bytes for each lamp unit. They are driven by Pulse Width Modulation (PWM). Refer to figure "Lamp unit addressing" for details.

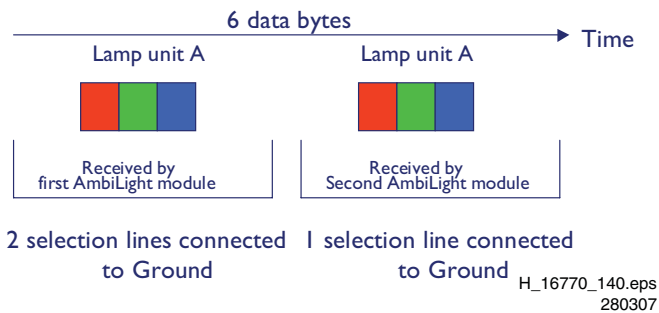


Figure 9-21 Lamp unit addressing

The driver current is different for the "basic colours" (R, G and B) of each LED package. The "red" and "blue" LED's of each package need a driver current of 150 mA, the "green" LED need a driver current of 200 mA. Refer to figure "Ambient Light module driving" for details. Each unit need a 12 V supply to drive the LED's.

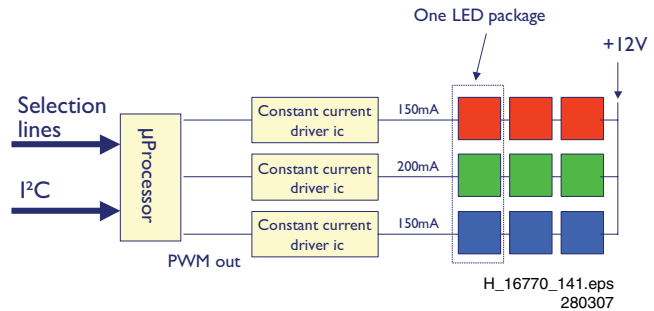


Figure 9-22 Ambient Light module driving

The configuration shown in figure "Ambient Light module driving" is implemented **two** times per module.

## 9.9 Abbreviation List

0/6/12	SCART switch control signal on A/V board. 0 = loop through (AUX to TV), 6 = play 16:9 format, 12 = play 4:3 format	DFI	Dynamic Frame Insertion
2DNR	Spatial (2D) Noise Reduction	DFU	Directions For Use: owner's manual
3DNR	Temporal (3D) Noise Reduction	DMR	Digital Media Reader: card reader
AARA	Automatic Aspect Ratio Adaptation: algorithm that adapts aspect ratio to remove horizontal black bars; keeps the original aspect ratio	DNM	Digital Natural Motion
ACI	Automatic Channel Installation: algorithm that installs TV channels directly from a cable network by means of a predefined TXT page	DNR	Digital Noise Reduction: noise reduction feature of the set
ADC	Analogue to Digital Converter	DRAM	Dynamic RAM
AFC	Automatic Frequency Control: control signal used to tune to the correct frequency	DRM	Digital Rights Management
AGC	Automatic Gain Control: algorithm that controls the video input of the feature box	DSP	Digital Signal Processing
AM	Amplitude Modulation	DST	Dealer Service Tool: special remote control designed for service technicians
ANR	Automatic Noise Reduction: one of the algorithms of Auto TV	DTCP	Digital Transmission Content Protection; A protocol for protecting digital audio/video content that is traversing a high speed serial bus, such as IEEE-1394
AP	Asia Pacific	DVD	Digital Versatile Disc
AR	Aspect Ratio: 4 by 3 or 16 by 9	DVI(-d)	Digital Visual Interface (d= digital only)
ASF	Auto Screen Fit: algorithm that adapts aspect ratio to remove horizontal black bars without discarding video information	E-DDC	Enhanced Display Data Channel (VESA standard for communication channel and display). Using E-DDC, the video source can read the EDID information from the display.
ATSC	Advanced Television Systems Committee, the digital TV standard in the USA	EEPROM	Extended Display Identification Data (VESA standard)
ATV	See Auto TV	EMI	Electrically Erasable and Programmable Read Only Memory
Auto TV	A hardware and software control system that measures picture content, and adapts image parameters in a dynamic way	EPLD	Electro Magnetic Interference
AV	External Audio Video	EU	Erasable Programmable Logic Device
AVIP	Audio Video Input Processor	EXT	EXternal (source), entering the set by SCART or by cinches (jacks)
B/G	Monochrome TV system. Sound carrier distance is 5.5 MHz	FBL	Fast BLanking: DC signal accompanying RGB signals
BLR	Board-Level Repair	FDS	Full Dual Screen (same as FDW)
BTSC	Broadcast Television Standard Committee. Multiplex FM stereo sound system, originating from the USA and used e.g. in LATAM and AP-NTSC countries	FDW	Full Dual Window (same as FDS)
B-TXT	Blue TeleteXT	FLASH	FLASH memory
C	Centre channel (audio)	FM	Field Memory or Frequency Modulation
CEC	Consumer Electronics Control bus: remote control bus on HDMI connections	FPGA	Field-Programmable Gate Array
CL	Constant Level: audio output to connect with an external amplifier	FTV	Flat TeleVision
CLR	Component Level Repair	Gb/s	Giga bits per second
COLUMBUS	COlour LUMinance Baseband Universal Sub-system	G-TXT	Green TeleteXT
ComPair	Computer aided rePair	H	H_sync to the module
CP	Connected Planet / Copy Protection	HD	High Definition
CSM	Customer Service Mode	HDD	Hard Disk Drive
CTI	Colour Transient Improvement: manipulates steepness of chroma transients	HDCP	High-bandwidth Digital Content Protection: A "key" encoded into the HDMI/DVI signal that prevents video data piracy. If a source is HDCP coded and connected via HDMI/DVI without the proper HDCP decoding, the picture is put into a "snow vision" mode or changed to a low resolution. For normal content distribution the source and the display device must be enabled for HDCP "software key" decoding.
CVBS	Composite Video Blanking and Synchronization	HDMI	High Definition Multimedia Interface
DAC	Digital to Analogue Converter	HP	HeadPhone
DBE	Dynamic Bass Enhancement: extra low frequency amplification	I	Monochrome TV system. Sound carrier distance is 6.0 MHz
DDC	See "E-DDC"	I <sup>2</sup> C	Inter IC bus
D/K	Monochrome TV system. Sound carrier distance is 6.5 MHz	I <sup>2</sup> D	Inter IC Data bus
		I <sup>2</sup> S	Inter IC Sound bus
		IF	Intermediate Frequency
		Interlaced	Scan mode where two fields are used to form one frame. Each field contains half the number of the total amount of lines. The fields are written in "pairs", causing line flicker.
		IR	Infra Red
		IRQ	Interrupt Request

ITU-656	The ITU Radio communication Sector (ITU-R) is a standards body subcommittee of the International Telecommunication Union relating to radio communication. ITU-656 (a.k.a. SDI), is a digitized video format used for broadcast grade video. Uncompressed digital component or digital composite signals can be used. The SDI signal is self-synchronizing, uses 8 bit or 10 bit data words, and has a maximum data rate of 270 Mbit/s, with a minimum bandwidth of 135 MHz.	PCM PDP PFC  PIP PLL  POR Progressive Scan  PTC  PWB PWM QRC QTNR QVCP RAM RGB	Pulse Code Modulation Plasma Display Panel Power Factor Corrector (or Pre-conditioner) Picture In Picture Phase Locked Loop. Used for e.g. FST tuning systems. The customer can give directly the desired frequency Power On Reset, signal to reset the uP Scan mode where all scan lines are displayed in one frame at the same time, creating a double vertical resolution. Positive Temperature Coefficient, non-linear resistor Printed Wiring Board (same as "PCB") Pulse Width Modulation Quasi Resonant Converter Quality Temporal Noise Reduction Quality Video Composition Processor Random Access Memory Red, Green, and Blue. The primary colour signals for TV. By mixing levels of R, G, and B, all colours (Y/C) are reproduced.
ITV	Institutional TeleVision; TV sets for hotels, hospitals etc.	RC RC5 / RC6	Remote Control Signal protocol from the remote control receiver
JOP	Jaguar Output Processor	RESET	RESET signal
LS	Last Status; The settings last chosen by the customer and read and stored in RAM or in the NVM. They are called at start-up of the set to configure it according to the customer's preferences	ROM R-TXT SAM S/C SCART	Read Only Memory Red TeleteXT Service Alignment Mode Short Circuit Syndicat des Constructeurs d'Appareils Radiorecepteurs et Televisieurs
LATAM	Latin America	SCL SCL-F SD SDA SDA-F SDI SDRAM SECAM	Serial Clock I <sup>2</sup> C Clock Signal on Fast I <sup>2</sup> C bus Standard Definition Serial Data I <sup>2</sup> C Data Signal on Fast I <sup>2</sup> C bus Serial Digital Interface, see "ITU-656" Synchronous DRAM SEquence Couleur Avec Memoire. Colour system mainly used in France and East Europe. Colour carriers= 4.406250 MHz and 4.250000 MHz
LCD	Liquid Crystal Display	SIF	Sound Intermediate Frequency
LED	Light Emitting Diode	SMPS SoC SOG SOPS S/PDIF SRAM SSB STBY SVGA SVHS SW SWAN	Switched Mode Power Supply System on Chip Sync On Green Self Oscillating Power Supply Sony Philips Digital InterFace Static RAM Small Signal Board STand-BY 800x600 (4:3) Super Video Home System Software Spatial temporal Weighted Averaging
L/L'	Monochrome TV system. Sound carrier distance is 6.5 MHz. L' is Band I, L is all bands except for Band I		Noise reduction 1280x1024
LORE	LOcal REgression approximation noise reduction		Thin Film Transistor
LPL	LG.Philips LCD (supplier)		Total Harmonic Distortion
LS	Loudspeaker		Transmission Minimized Differential Signalling
LVDS	Low Voltage Differential Signalling		TeleteXT
Mbps	Mega bits per second		Dual Window with TeleteXT
M/N	Monochrome TV system. Sound carrier distance is 4.5 MHz		User Interface
MIPS	Microprocessor without Interlocked Pipeline-Stages; A RISC-based microprocessor		Microprocessor 1600x1200 (4:3) V-sync to the module
MOP	Matrix Output Processor		Video Cassette Recorder
MOSFET	Metal Oxide Silicon Field Effect Transistor, switching device		Video Electronics Standards Association
MPEG	Motion Pictures Experts Group		
MPIF	Multi Platform InterFace		
MUTE	MUTE Line		
NC	Not Connected		
NICAM	Near Instantaneous Compounded Audio Multiplexing. This is a digital sound system, mainly used in Europe.		
NTC	Negative Temperature Coefficient, non-linear resistor		
NTSC	National Television Standard Committee. Colour system mainly used in North America and Japan. Colour carrier NTSC M/N= 3.579545 MHz, NTSC 4.43= 4.433619 MHz (this is a VCR norm, it is not transmitted off-air)		
NVM	Non-Volatile Memory: IC containing TV related data such as alignments		
O/C	Open Circuit		
OSD	On Screen Display		
OTC	On screen display Teletext and Control; also called Artistic (SAA5800)		
P50	Project 50: communication protocol between TV and peripherals		
PAL	Phase Alternating Line. Colour system mainly used in West Europe (colour carrier= 4.433619 MHz) and South America (colour carrier PAL M= 3.575612 MHz and PAL N= 3.582056 MHz)		
PCB	Printed Circuit Board (same as "PWB")		

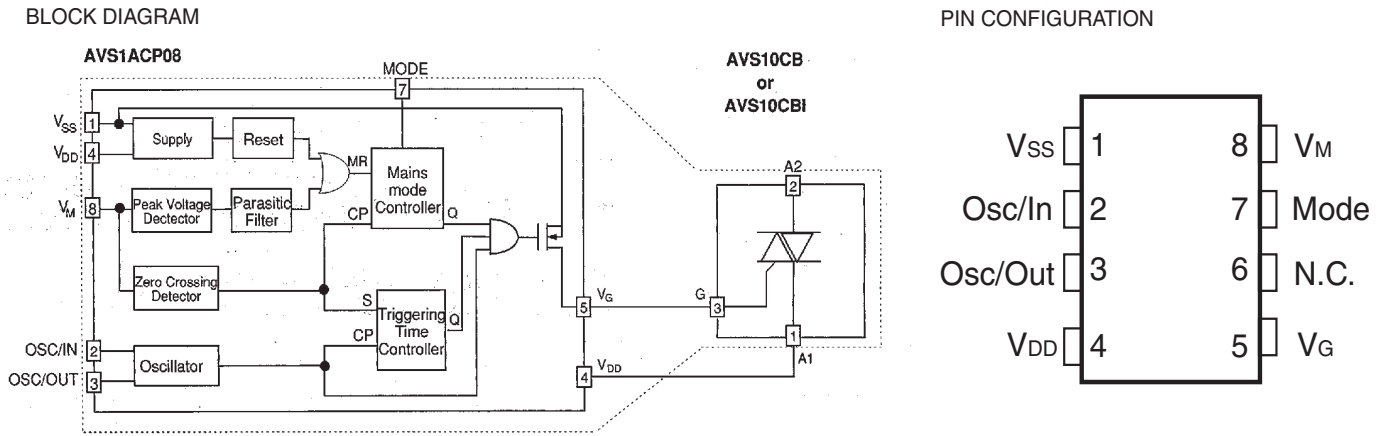


VGA	640x480 (4:3)
VL	Variable Level out: processed audio output toward external amplifier
VS	Vestigial Side Band; modulation method
WYSIWYR	What You See Is What You Record: record selection that follows main picture and sound
WXGA	1280x768 (15:9)
XTAL	Quartz crystal
XGA	1024x768 (4:3)
Y	Luminance signal
Y/C	Luminance (Y) and Chrominance (C) signal
YPbPr	Component video. Luminance and scaled colour difference signals (B-Y and R-Y)
YUV	Component video

9.10 IC Data Sheets

This section shows the internal block diagrams and pin configurations of ICs that are drawn as "black boxes" in the electrical diagrams (with the exception of "memory" and "logic" ICs).

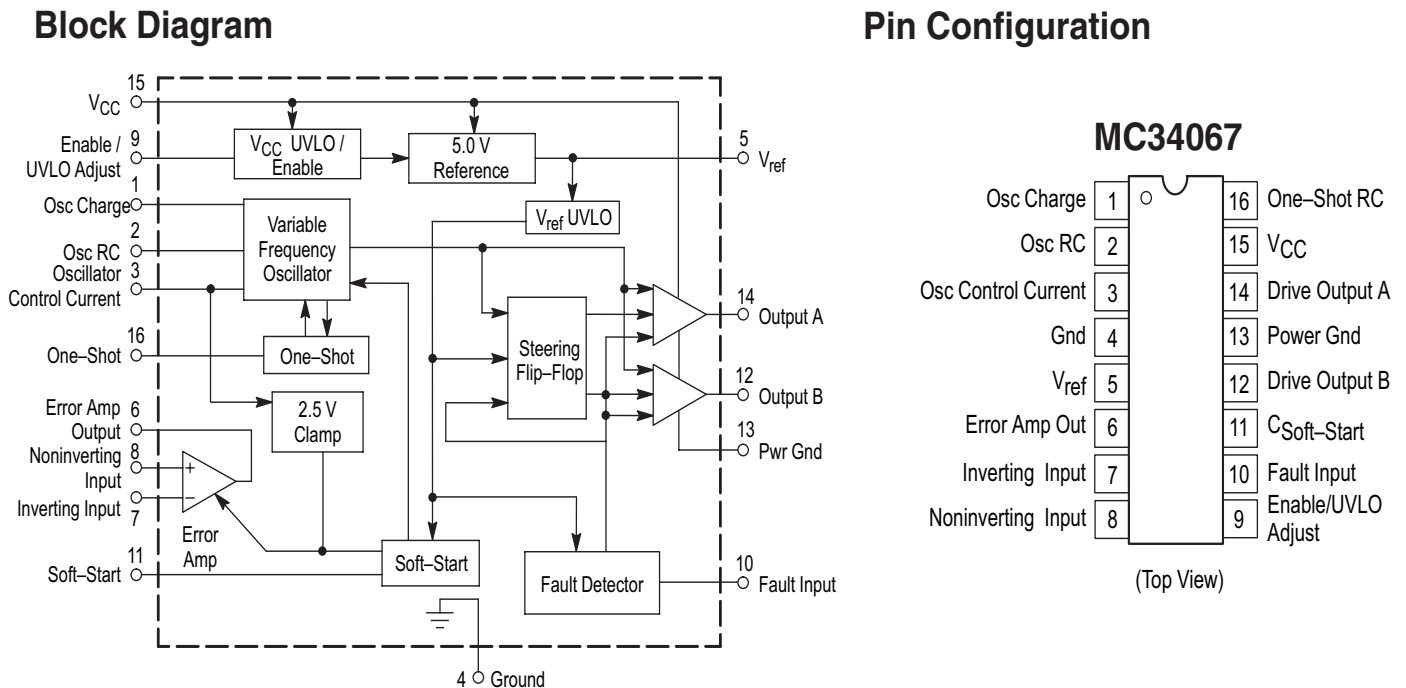
9.10.1 Diagram A1, AVS1ACP08 (IC 7305)



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Figure 9-23 Internal block diagram and pin configuration

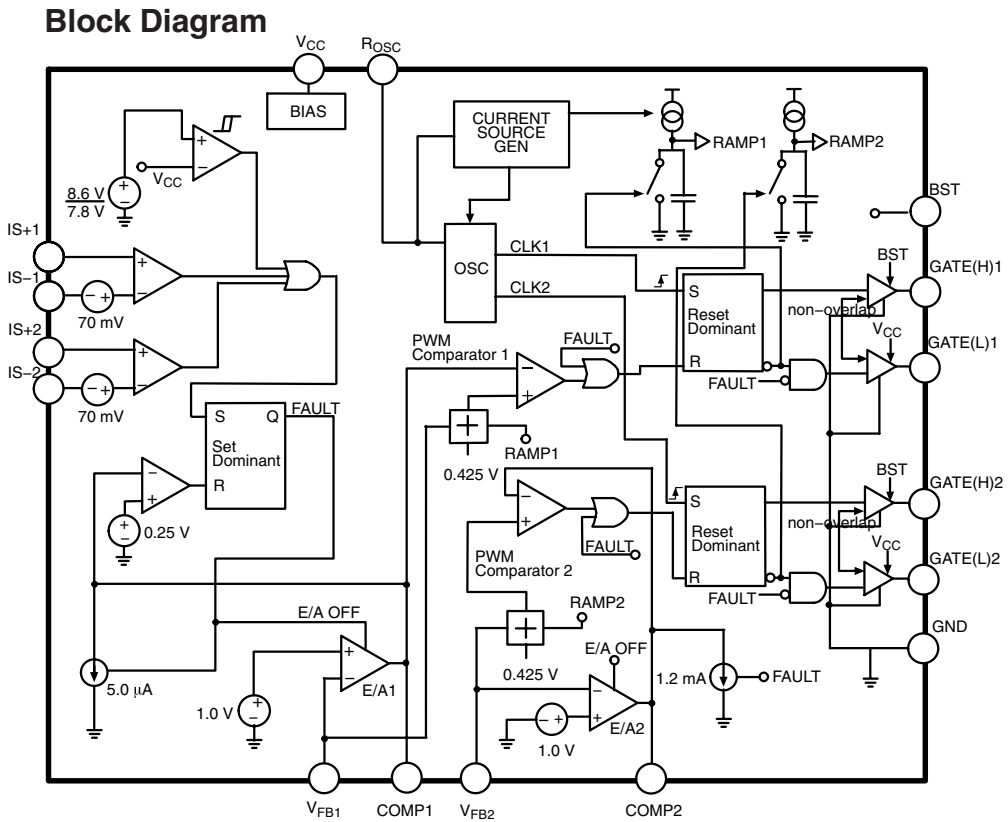
9.10.2 Diagram A2, MC34067P (IC 7001)



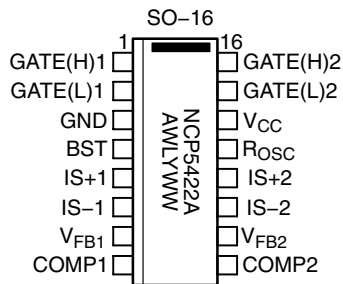
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Figure 9-24 Internal block diagram and pin configuration

9.10.3 Diagram B02A, NCP5422AD (IC 7U00)



### Pin Configuration



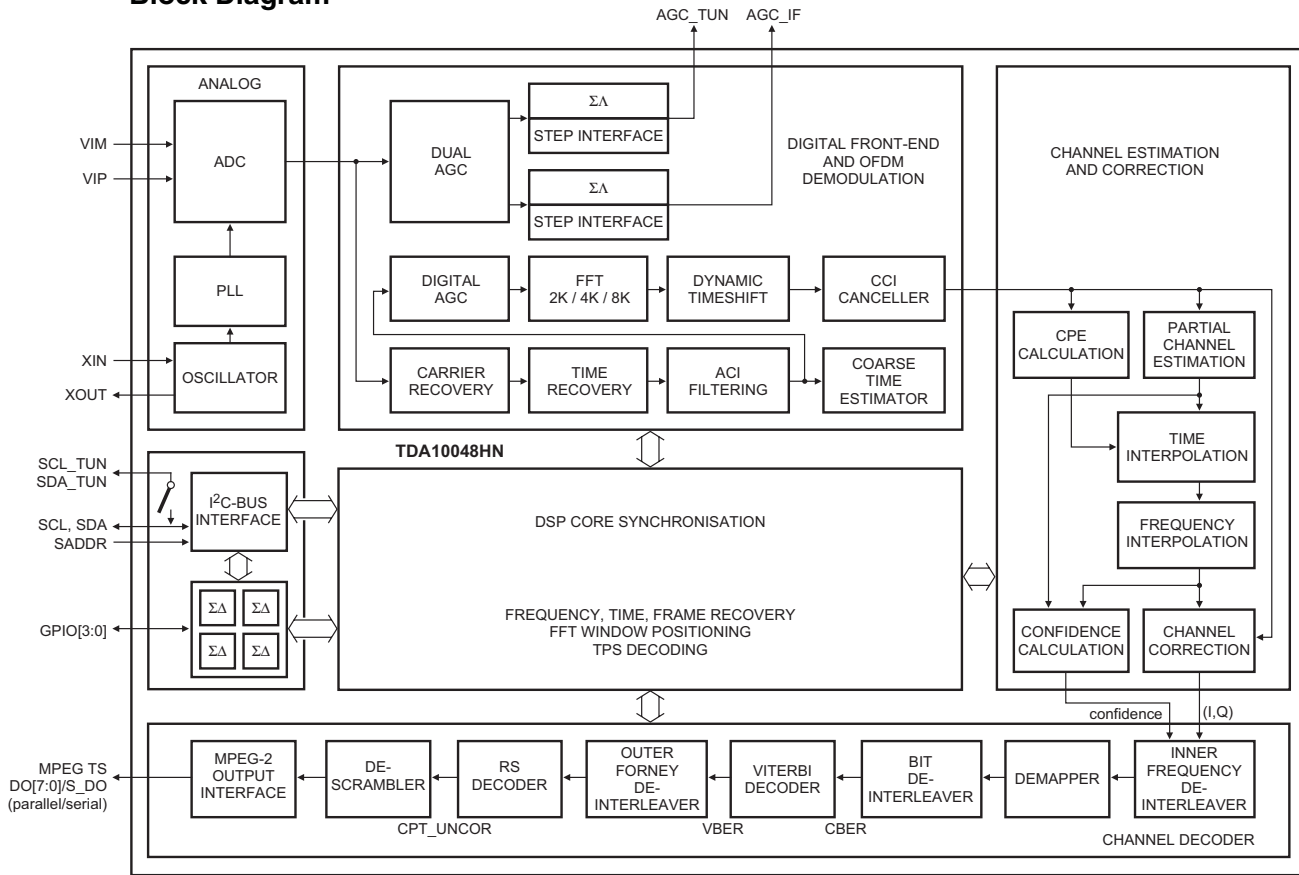
A = Assembly Location  
 WL = Wafer Lot  
 Y = Year  
 WW = Work Week

F\_15400\_129.eps  
 240505

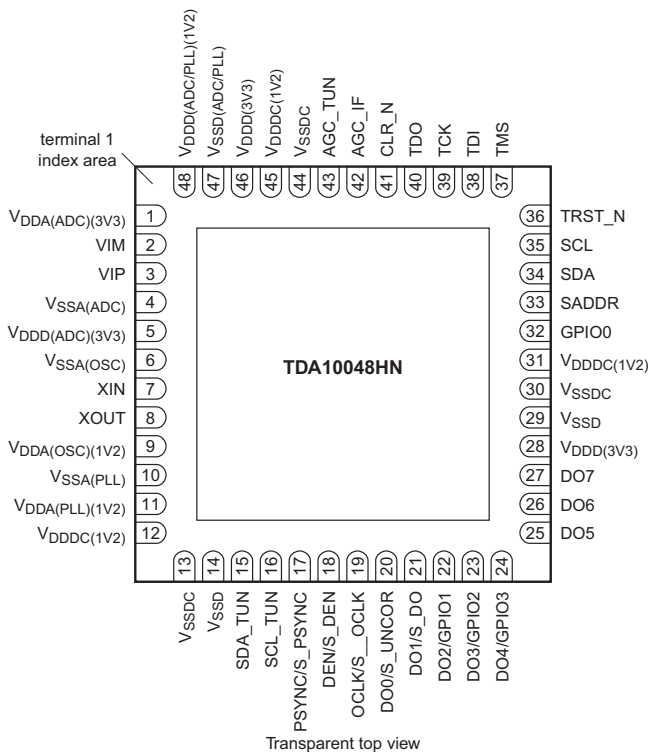
Figure 9-25 Internal block diagram and pin configuration

9.10.4 Diagram B03A, TDA10048HN (IC7T17-1)

Block Diagram



Pin Configuration

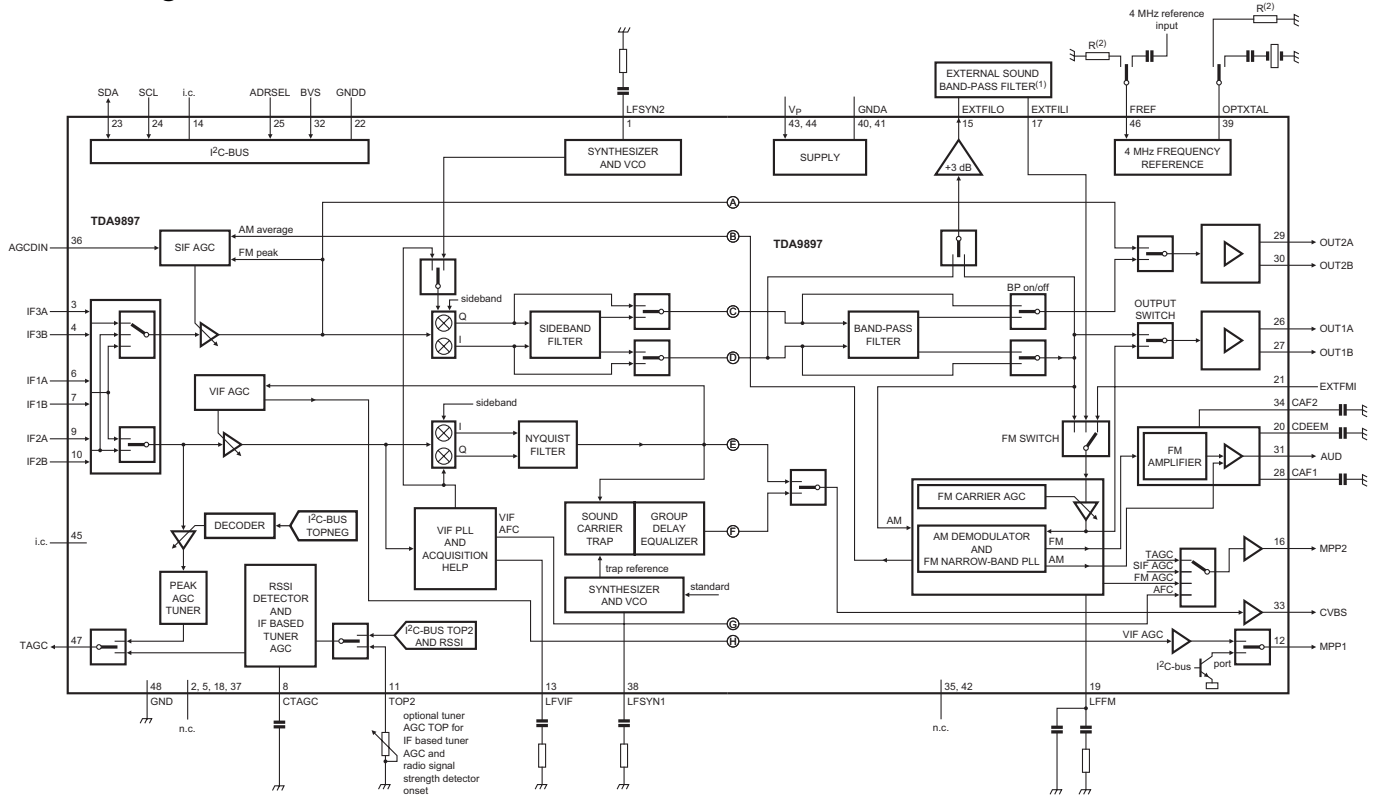


Transparent top view

Figure 9-26 Internal block diagram and pin configuration

9.10.5 Diagram B03B, TDA9898HL (IC 7T57)

Block Diagram



Pin Configuration

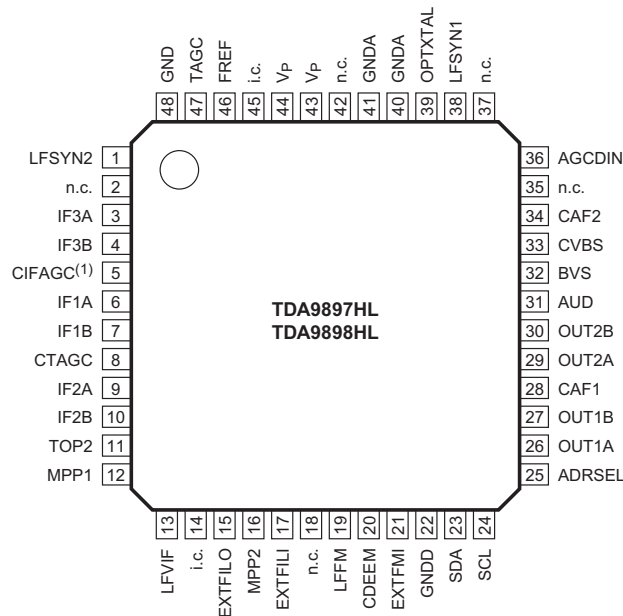
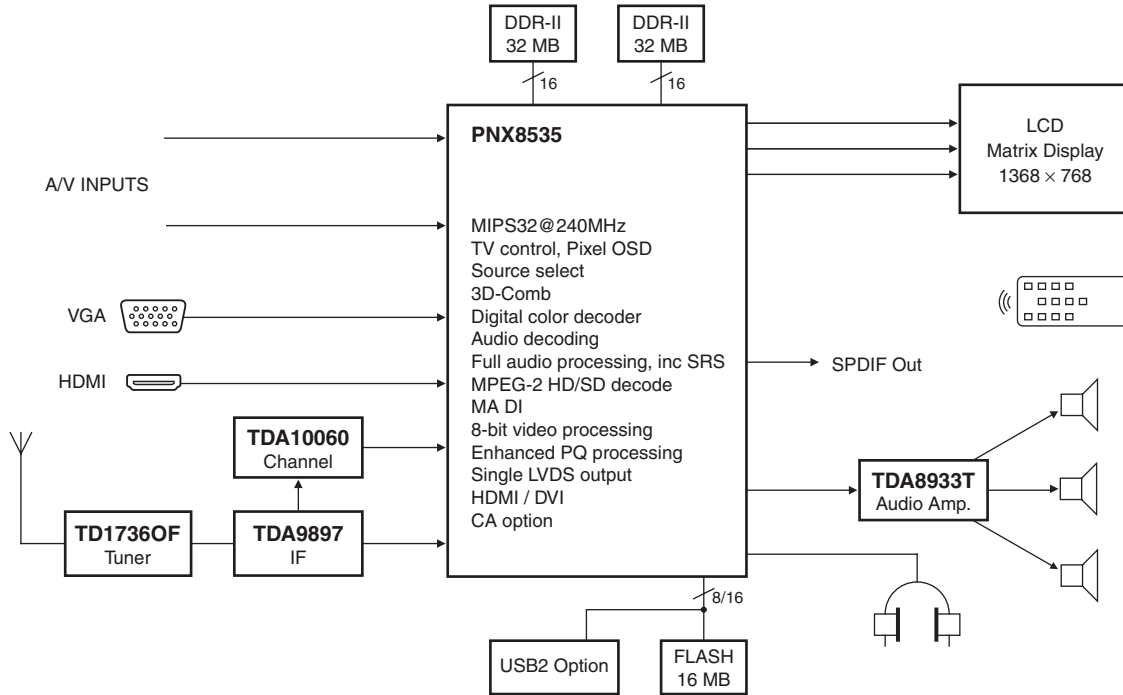


Figure 9-27 Pin configuration

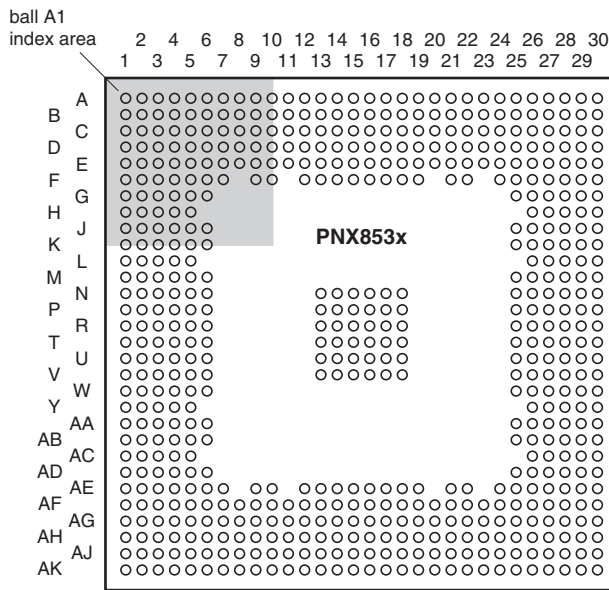


9.10.6 Diagram B04, PNX8535 (IC 7H00)

Block Diagram



Pin Configuration



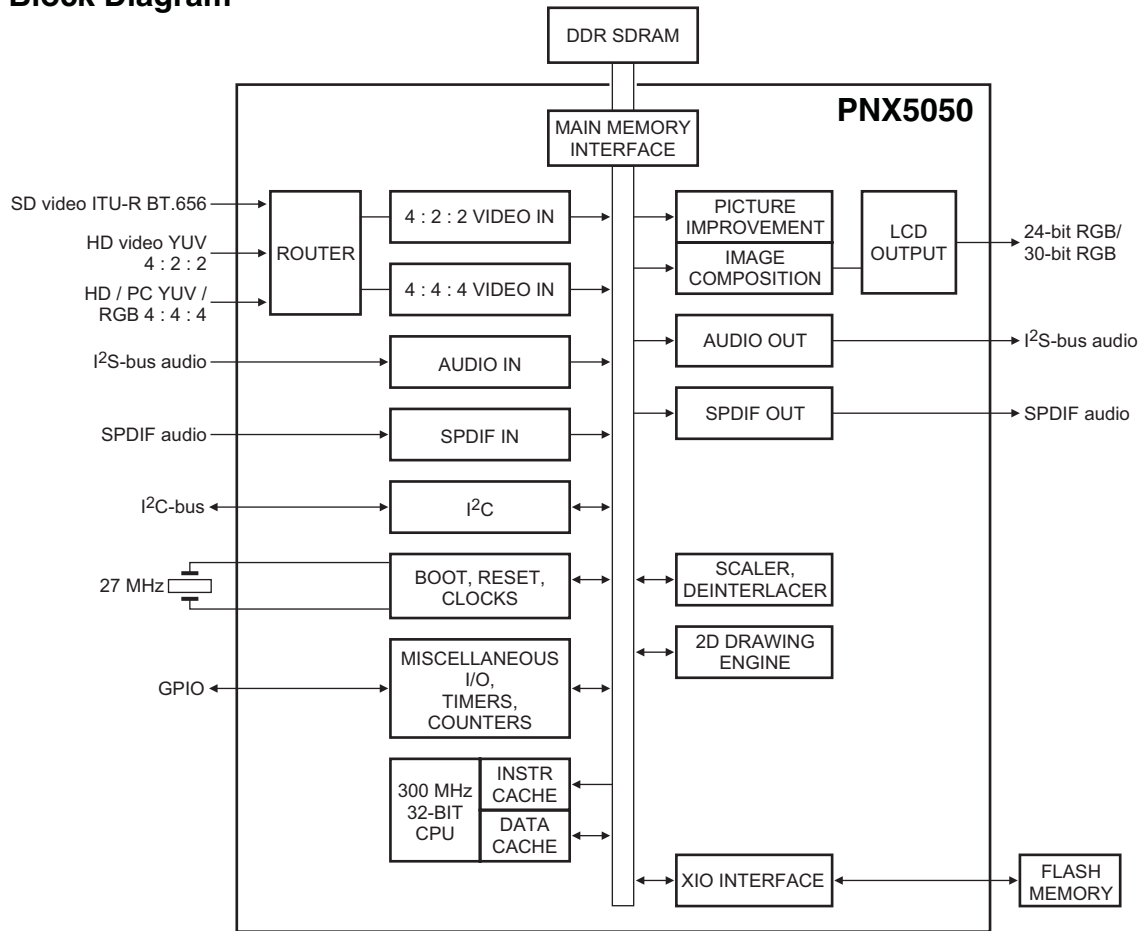
Transparent top view

H\_16800\_128.eps  
090507

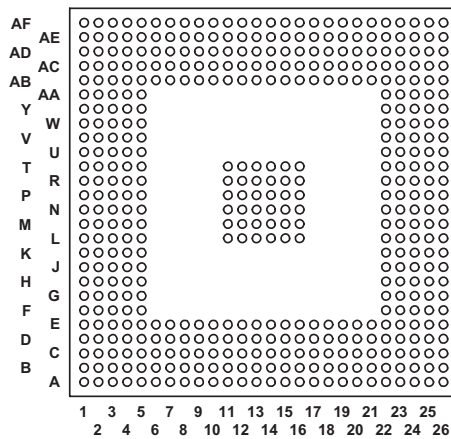
Figure 9-28 Pin configuration

9.10.7 Diagram B05, PNX5050 (IC 7C00)

Block Diagram



Pin Configuration

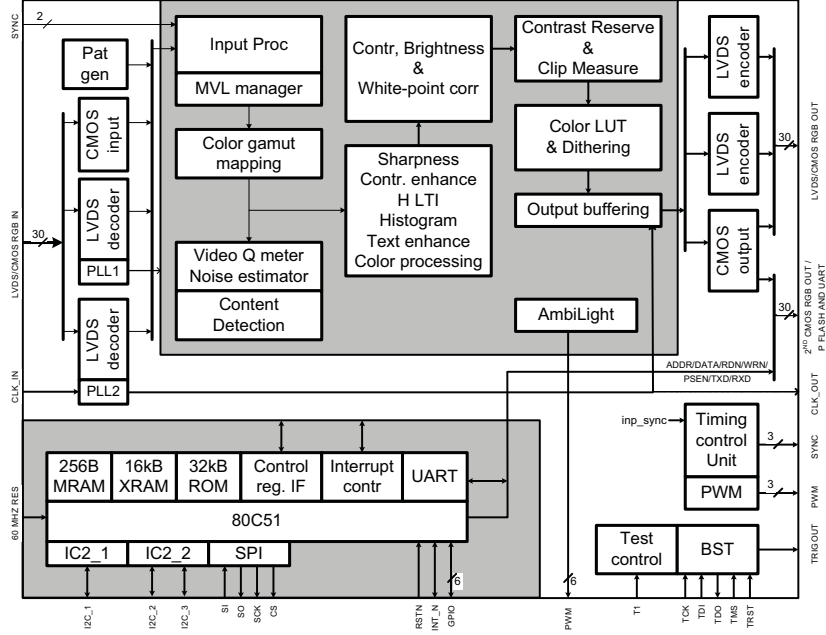


H\_16800\_129.eps  
090507

Figure 9-29 Pin configuration

9.10.8 Diagram B06C, T6TF4HFG (IC 7GE2)

Block Diagram



Pin Configuration

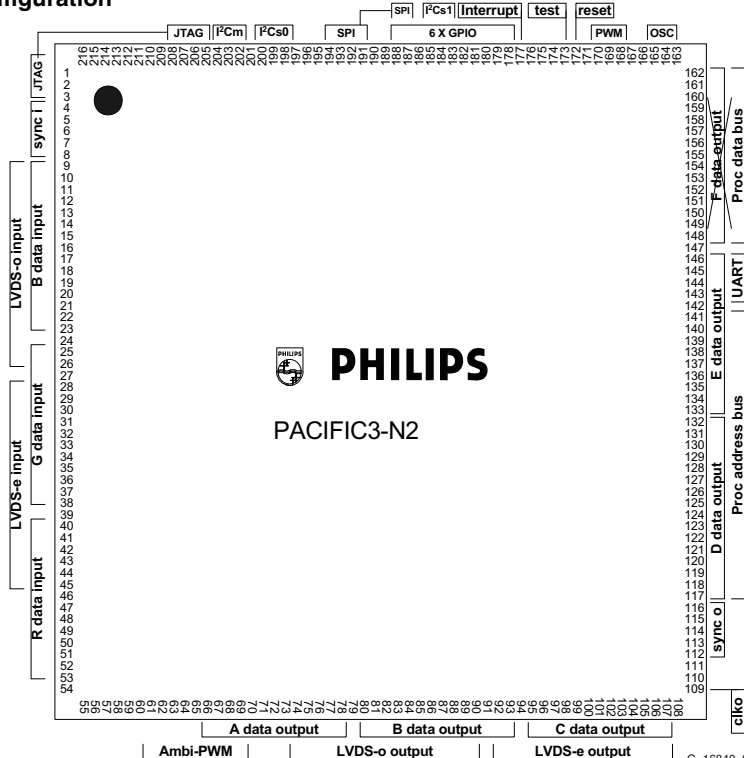
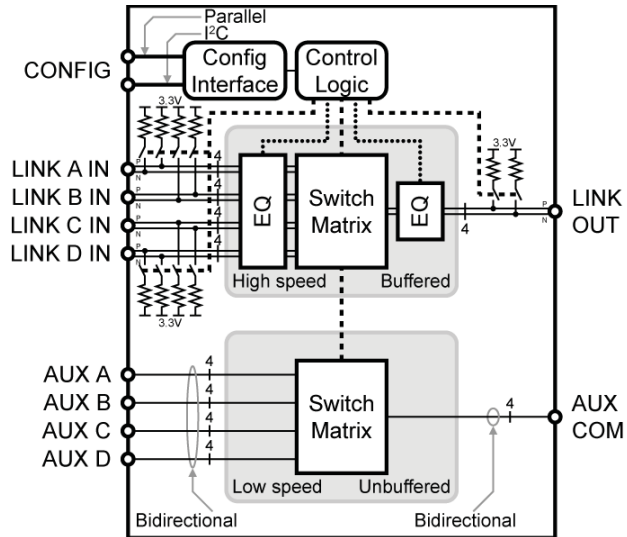


Figure 9-30 Internal block diagram and pin configuration

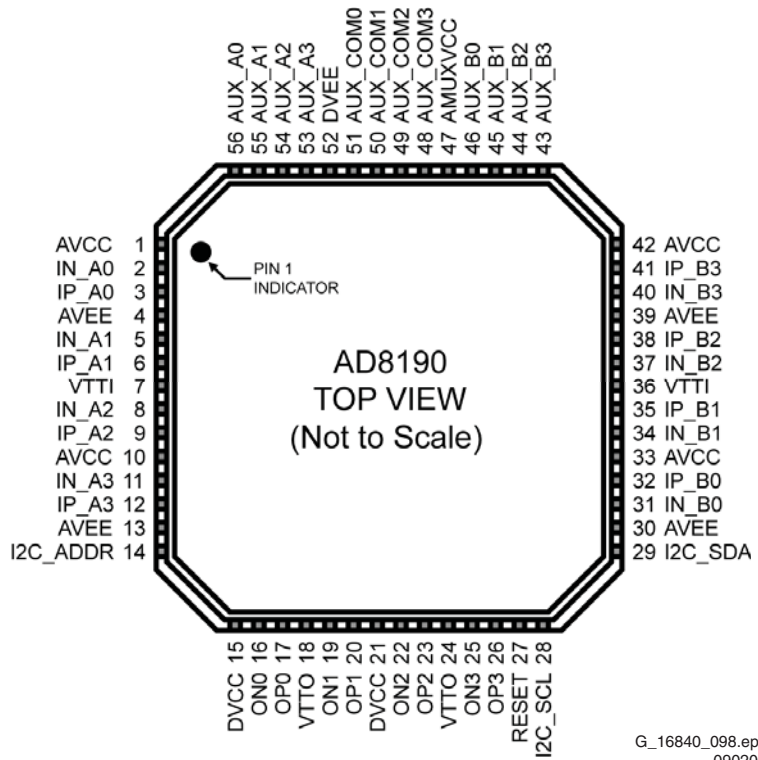
G\_16840\_099.epws  
090207

9.10.9 Diagram B08E, AD8191ASTZ (IC 7J27)

Block Diagram



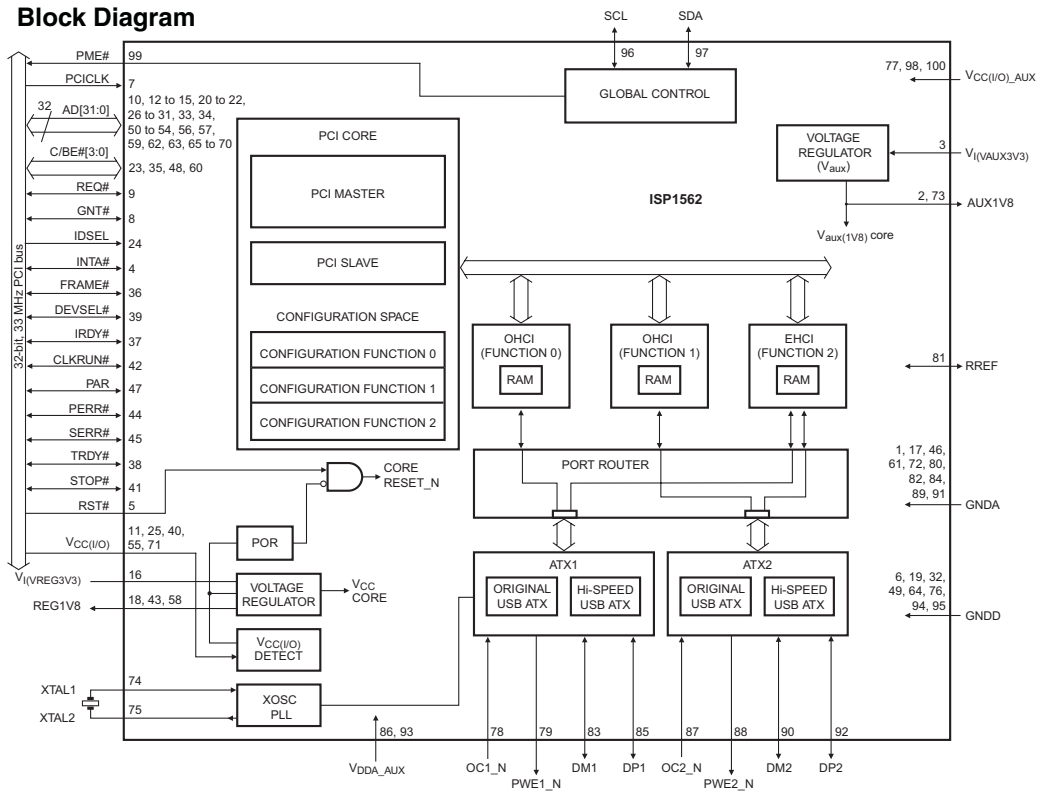
Pin Configuration



G\_16840\_098.eps  
090207

Figure 9-31 Internal block diagram and pin configuration

9.10.10 Diagram B10A, ISP1562BE (IC 7N00)



Pin Configuration

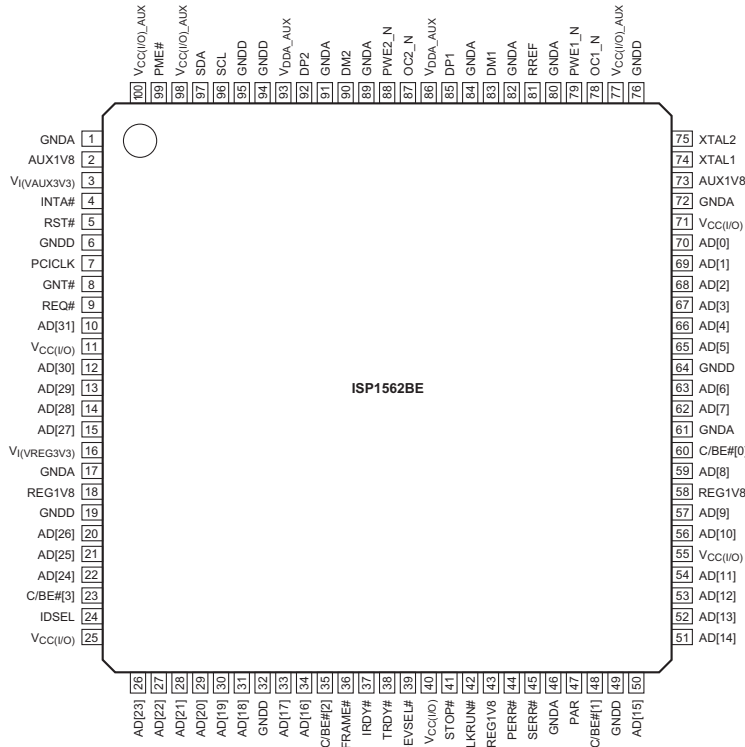


Figure 9-32 Internal block diagram and pin configuration





Table with columns: Part Number, Description, and Part Name. Includes items like BAS316, STPS20H100CT, UDZ4.7B, PDZ22B, BAT54, and Bridge TS6B05G-06.



Table with columns: Part Number, Description, and Part Name. Includes items like MC34067P, BC847BW, FQPF18N50V2, TS2431AI, and BC817-25W.

AmbiLight Panel (2 sided 6 LED) [AL]

Various

Table with columns: Part Number, Description, and Part Name. Includes items like Fuse 1A5T 125V, Connector 3p m, 4p M, 5p m, and 7p m.



Table with columns: Part Number, Description, and Part Name. Includes items like 100nF 20% 50V 0603, 100pF 5% 50V 0402, and 220uF 20% 25V.



Table with columns: Part Number, Description, and Part Name. Includes items like 10kΩ 5% 0.01W 0402, 100Ω 1% 0402, and 1000Ω 5% 0402.

Table with columns: Part Number, Description, and Part Name. Includes items like 180Ω 5% 0402, 820Ω 5% 0.5W, and 10kΩ 5% 0.01W 0402.



Table with columns: Part Number, Description, and Part Name. Includes item: 10μH 20%.



Table with columns: Part Number, Description, and Part Name. Includes items like BZX384-C5V6, PDZ5.1B, and BAS316.



Table with columns: Part Number, Description, and Part Name. Includes items like UPEC LED CLUSTER, LD1117DT33, P89LPC935FDH, and IC DD313.

Small Signal Board [B]

Various

Table with columns: Part Number, Description, and Part Name. Includes items like 21p (SCART), Socket 1p f 3.5mm Phono, and Connector 4p m.

Table with columns: Part Number, Description, and Part Name. Includes items like Xtal 27MHz 18pF, Connector 5p, and Connector 4p.



Table with columns: Part Number, Description, and Part Name. Includes items like 100pF 5% 50V 0402, 10nF 10% 16V 0402, and 220nF 20% 275V.



















7H16	3198 010 42310	BC847BW
7H18	3198 010 42310	BC847BW
7H21	3198 010 44310	PDTC114EU
7H22	3198 010 44310	PDTC114EU
7H90	9322 204 63685	NCP303LSN10
7H91	9322 204 63685	NCP303LSN10
7H92	9322 204 63685	NCP303LSN10
7H93	3198 010 42310	BC847BW
7HA0	SW Main	For SW see item 0802
7HA3	3198 010 44310	PDTC114EU
7HC0	9351 750 00118	74HC4066PW
7HC1	3198 010 44310	PDTC114EU
7HC2	3198 010 44310	PDTC114EU
7HC3	SW Main NVM	For SW see item 0802
7HC4	3198 010 42320	BC857BW
7HD0	9322 204 63685	NCP303LSN10
7HD6	9340 425 20115	BC847BS
7HD8	3198 010 42310	BC847BW
7HF1	9322 212 46668	CY2305SXC-1
7HG0	9322 235 85668	EDE2516ABSE-6E-E
7HG0	9322 249 12668	EDE5116AHSE-6E-E
7HG1	9322 235 85668	EDE2516ABSE-6E-E
7HG1	9322 249 12668	EDE5116AHSE-6E-E
7HM1	9322 185 74668	LM324P
7HP0	9322 213 38685	LD2985BM33
7HP5	9352 760 36118	PCA9540BDP
7HV0	9322 243 46668	TPA4411RTJ
7HV1	3198 010 42310	BC847BW
7HVA	9340 425 30115	BC847BPN
7J27	9322 247 52668	AD8191ASTZ
7J28	3198 010 42310	BC847BW
7J29	SW HDMI-3	For SW see item 0802
7J30	9351 875 80118	74HCU04PW
7J42	9340 425 20115	BC847BS
7J50	3198 010 42320	BC857BW
7J51	5322 130 60159	BC846B
7JA0	SW HDMI-1	For SW see item 0802
7JA1	SW HDMI-2	For SW see item 0802
7JA2	3198 010 42310	BC847BW
7JA3	3198 010 42310	BC847BW
7JE2	9322 174 78668	ST3232C
7JE3	3198 010 42310	BC847BW
7N00	9352 805 26518	ISP1564HL
7N30	9322 247 48668	RYC8620-2M
7P01	9322 173 43668	TPS2211AIDB
7P03	9352 104 20118	74LVC244APW
7P10	3198 010 44310	PDTC114EU
7P51	9352 104 20118	74LVC244APW
7P52	9352 104 20118	74LVC244APW
7P53	9352 115 40118	74LVC245APW
7T13	3198 010 71230	74LVC1GU04GW
7T17	9352 815 27518	TDA10048HN/C200
7T18	9340 425 30115	BC847BPN
7T55	3198 010 42310	BC847BW
7T56	9340 425 20115	BC847BS
7T57	9352 830 78118	TDA9898HL/V2
7U00	9322 207 46668	NCP5422AD
7U01	9322 245 62668	Power FET FDS8884
7U02	9322 160 70668	SI4936ADY
7U03	9322 245 62668	Power FET FDS8884
7U04	9340 425 20115	BC847BS
7U05	9340 425 20115	BC847BS
7U06	9322 245 62668	Power FET FDS8884
7U07	9340 219 30115	BC817-25W
7U08	9322 245 62668	Power FET FDS8884
7U10	9340 425 10115	BC857BS
7U13	9340 425 30115	BC847BPN
7U14	9340 425 20115	BC847BS
7U15	9340 425 20115	BC847BS
7U16	9322 204 71668	SI4835BDY
7U17	9322 192 16685	TS2431AI
7U18	9322 213 50685	TS431AIL
7U19	9340 219 30115	BC817-25W
7U20	9340 219 30115	BC817-25W
7U24	9340 560 35235	BSH112
7U26	3198 010 42310	BC847BW
7U27	9322 213 50685	TS431AIL
7U28	9340 575 87118	PHD38N02LT
7U61	9322 242 20668	PowFET SM FDS6982
7U64	9322 182 77668	L6910
7U66	9340 425 20115	BC847BS
7U8B	9322 192 16685	TS2431AI
7U8C	9340 575 87118	PHD38N02LT

**Side I/O Panel [D]****Various**

1301	4822 267 10484	YKF51-5359
1302	2422 026 05808	Cinch 3p f Ye
1303	4822 265 10838	Socket 1p f 3.5mm Phono
1304	2422 025 10655	Connector 11p m

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2301	4822 126 11785	47pF 5% 50V 0603
2302	4822 126 11785	47pF 5% 50V 0603
2303	4822 122 33761	22pF 5% 50V
2304	4822 126 11785	47pF 5% 50V 0603
2305	2020 552 94427	100pF 5% 50V
2306	2020 552 94427	100pF 5% 50V
2309	2238 916 15641	22nF 10% 25V 0603
2310	2238 916 15641	22nF 10% 25V 0603
2311	4822 122 33761	22pF 5% 50V

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3301	4822 051 30759	75Ω 5% 0.062W
3302	4822 051 30759	75Ω 5% 0.062W
3303	4822 051 30109	10Ω 5% 0.062W
3305	4822 051 30109	10Ω 5% 0.062W
3308	4822 051 30101	100Ω 5% 0.062W
3309	4822 117 13632	100kΩ 1% 0603 0.62W
3310	4822 051 30101	100Ω 5% 0.062W
3311	4822 117 13632	100kΩ 1% 0603 0.62W
3312	4822 051 30103	10kΩ 5% 0.062W
3313	4822 051 30103	10kΩ 5% 0.062W
3999	4822 051 30102	1kΩ 5% 0.062W

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6302	4822 130 11416	PDZ6.8B
6303	4822 130 11416	PDZ6.8B
6304	9322 146 61685	DF3A6.8FU
6305	9322 146 61685	DF3A6.8FU
6306	9322 146 61685	DF3A6.8FU
6307	9322 146 61685	DF3A6.8FU

**Keyboard Control Panel [E]****Various**

1701	4822 276 13775	Switch 1p 0.1A 12V
1702	4822 276 13775	Switch 1p 0.1A 12V
1703	4822 276 13775	Switch 1p 0.1A 12V
1704	4822 276 13775	Switch 1p 0.1A 12V
1705	4822 276 13775	Switch 1p 0.1A 12V
1706	4822 276 13775	Switch 1p 0.1A 12V
1M01	4822 267 10459	Connector 3p
8136	3104 311 07951	Cable 11p/680/11p
8136	3104 311 09711	Cable 11p/820/11p

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3002	4822 051 30151	150Ω 5% 0.062W
3003	4822 051 30391	390Ω 5% 0.062W
3004	4822 051 30561	560Ω 5% 0.062W
3005	3198 021 31820	1.8kΩ 5% 0.062W 0603
3006	4822 117 12968	820Ω 5% 0.62W
3999	4822 117 12968	820Ω 5% 0.62W

**IR & LED Panel [J]****Various**

1040	9322 223 97668	TSOP36236 IR
1M01	2422 025 18749	Connector 3p m
1M22	2422 025 18754	Connector 7p m

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2040	4822 124 12095	100μF 20% 16V
2070	4822 124 12095	100μF 20% 16V

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3040	4822 117 13597	330Ω 5% 0.01W 0402
3041	4822 117 13606	10kΩ 5% 0.01W 0402
3042	3198 031 06820	6.8kΩ 5% 0.01W 0402
3043	4822 117 13545	100Ω 1% 0402
3051	3198 031 05610	560Ω 5% 0.01W 0402
3053	4822 117 13605	Jumper 0402
3061	4822 117 13548	1kΩ 5% 0402
3063	4822 117 13605	Jumper 0402
3071	4822 117 13603	33kΩ 5% 0402
3072	4822 117 13606	10kΩ 5% 0.01W 0402
3074	4822 117 13606	10kΩ 5% 0.01W 0402
3077	4822 117 13606	10kΩ 5% 0.01W 0402
3079	2322 705 70475	4.7MΩ 5% 0402
3999	4822 117 13548	1kΩ 5% 0402

9120	4822 117 13605	Jumper 0402
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6051	9322 242 74685	LED SML412BC6T
6052	9322 195 26685	LED SML-512UW



7051	3198 010 44310	PDTC114EU
7061	3198 010 44310	PDTC114EU
7070	9322 218 83685	TEMT6000
7071	3198 010 42310	BC847BW

# 11. Revision List

Manual xxxx xxx xxxx.0

- First release.